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REMOTE-TERMINAL EMULATOR  
(DESIGN VERIFICATION MODEL) — USER'S MANUAL

T. Suyemoto

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JAMES S. CAMERON, Maj, USAF  
Project Engineer

  
MARVIN E. BROOKING  
Project Officer

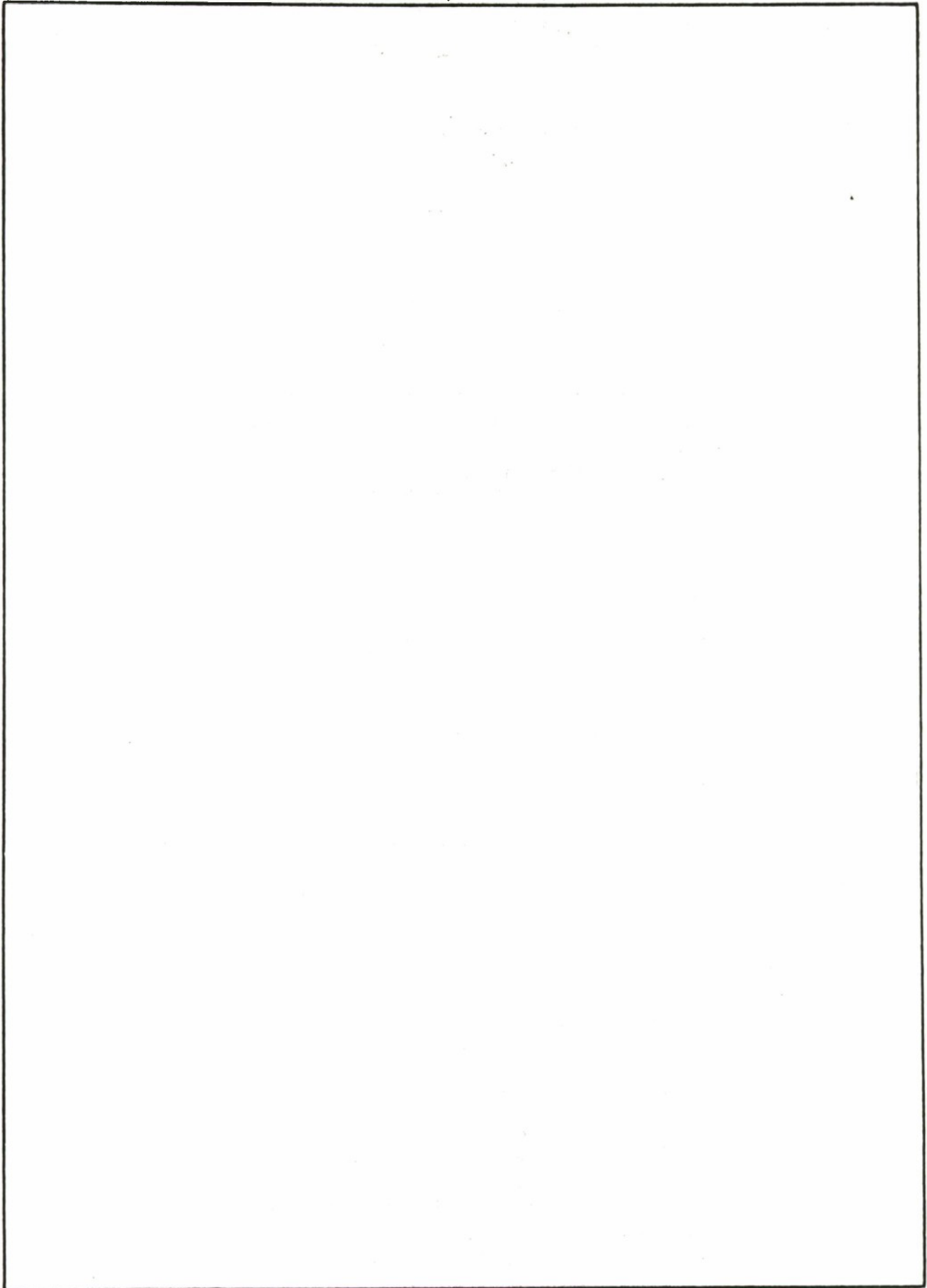
FOR THE COMMANDER

  
ROBERT J. LATINA, Colonel, USAF  
Director of ADPE Selection  
Deputy for Command and Management Systems

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20. ABSTRACT (Continue on reverse side if necessary and identify by block number) The Remote-Terminal Emulator is a minicomputer-based system which generates message traffic for use in testing and evaluating large-scale, multi-terminal computer systems. This series of reports will describe the two Design Verification Models that were developed on Data General NOVA 800 minicomputers. This volume is a user's manual which contains the information necessary to prepare and run the software portions of the Remote-Terminal Emulator.		





## PREFACE

The Remote-Terminal Emulator is a minicomputer-based system which generates message traffic for use in testing and evaluating large-scale, on-line computer systems. In real-time testing, it emulates the actions of a collection of operators, terminals, and, depending upon configuration, modems. In 1972 and early 1973, two Design Verification Models (DVM) of the emulator were developed by The MITRE Corporation under the sponsorship of the Air Force Directorate of Automatic Data Processing Equipment Selection (MCS). The fixed-site system, which is used primarily for program and scenario development, is located at MITRE/Bedford and interfaces with the computer system under test (SUT) through the switched telephone network. The on-site system, which is used primarily for detailed emulator test and evaluation, is representative of the equipment planned for operational use in future computer procurements. This system, which is moved to each SUT site, interfaces through cables directly with the SUT's communication line adapters.

The primary hardware components of each of these systems are a Data General NOVA 800 minicomputer, a fixed-head disk, a magnetic tape unit, a control teletype, and an appropriate emulator/SUT interface unit. Both DVM's have sufficient hardware to emulate up to 16 low-speed interactive terminals. The on-site DVM also has hardware to emulate eight additional terminals or terminal networks by the use of high-speed synchronous line adapters and associated circuitry. The primary software components that have been developed for this project consist of the Macro Preprocessor, the Scenario Assembler, the Real-Time Executive, the Scenario Interpreter and the Data Reduction Program.

The common denominator of remote-terminal emulation is the scenario, which is a program that controls the actions to be taken by the emulator in emulating a given device and mix of devices. The scenario defines the queries (system commands, input data, and control characters) to be sent to the SUT, how SUT responses are to be processed, and other details of the test to be conducted. The Macro Preprocessor is a general purpose support program that provides a basic macro capability to aid in scenario writing and which was also used in emulator program development. In the scenario development process, the Scenario Assembler is used to convert external (symbolic) scenarios to internal (absolute) scenarios which are tailored to a specific terminal type and to specific data communications control procedures. Both the Macro Preprocessor and the Scenario Assembler run under the Data General Disk Operating System (DOS). In real-time testing, internal scenarios are brought into core from disk and are processed by the Scenario Interpreter which runs under the Real-Time Executive. All messages sent to and received from the SUT, as well as messages describing other actions of the emulator, can be time-tagged and logged on magnetic tape. Upon completion of the test, these data are processed in various fashions by the Data Reduction program (which also runs under DOS) to produce scenario trace data and various statistics on the performance and utilization of both the emulator and the SUT.

This document is part of a series of reports which will describe the design, implementation and use of the two Design Verification Models. The titles of the reports in the series are as follows:

<u>Volume</u>	<u>Title</u>
1	Introduction and Summary
2	Scenarios and Data Structures
3	Macro Preprocessor
4	Scenario Assembler

<u>Volume</u>	<u>Title</u>
5	Scenario Interpreter
6	Real-Time Executive
7	Data Reduction Program
8	Hardware
9	Support Software
10	User's Manual

It is suggested that the reader become familiar with the emulator concepts and terminology presented in Volume 1 preparatory to reading other volumes in the series.

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## SECTION I

### INTRODUCTION

The Remote-Terminal Emulator consists of a combination of hardware components and software packages designed to generate message traffic for use in testing and evaluating on-line computer systems. The hardware configurations for both the fixed-site and on-site systems are discussed in Volume 8 of this series. This user's manual presents the user information necessary to prepare and run the software portions of the system. Included here are excerpts from previous volumes as well as additional material required for running the Remote-Terminal Emulator.

The common denominator of remote-terminal emulation is the scenario, which is a program that controls the actions to be taken by the emulator in emulating a given device or mix of devices. A scenario is formed by a series of scenario instructions which determine the queries to be sent to a SUT, how responses are to be handled, and the various control functions of a test. The command is a special instruction which exerts gross control over emulator actions, and is the only means by which the user can exert external control during an emulation run. Both instructions and commands are described in detail in Sections IV and V of Volume 2 of this series.

This paper is organized as a logical presentation of steps needed for preparation, execution, and data reduction of an emulator run. Section II describes both the NOVA Disk Operating System (DOS) as it applies to the Emulator, as well as the system support software which may be applicable in most phases of emulation. The macro processing function is described in Section III and the assembly function is presented in Section IV. These two functions prepare the scenario for the real-time run. Sections V and VI respectively deal with



preparing the Equipment Table, and following this, building an emulator system. The operating instructions and other information necessary for execution of a real-time emulation run are presented in Section VII. The final phase of an emulator run, data reduction, is discussed in Section VIII. An example of the on-line teletype output for all processing steps for a single emulation run is given in Appendix V. Section IX contains timing information for both the real-time and non-real time functions of the emulator.

## SECTION II

### DOS AND SUPPORT SOFTWARE

#### DOS

All of the non-real time programs included in the Emulator system run under Revision 5 of Data General's Disk Operating System (DOS). The support software described in Volume 9 of this series also operates under control of DOS. A complete description of DOS can be found in Reference 1. Under DOS a carriage return and a line feed are echoed back when the RETURN key is depressed. In this document the symbol ↵ is used to denote the depression of the RETURN key and the echo back of both the carriage return and line feed.

#### Loading DOS

The DOS system can be loaded into core from tape, or, if it already exists on disk, it can be loaded from there. To load from tape, the following sequence should be performed:

- (1) Turn on CPU, disk, tape drive, and system teletype;
- (2) Mount the system tape; press LOAD to advance tape to ready position;
- (3) Set panel data switches to 100022;
- (4) Raise the RESET panel switch and then raise the PROGRAM LOAD panel switch;
- (5) The remainder of the process involves the following activity on the system teletype. The underlined portion is what is to be entered by the user. The non-underlined portion is the response of the system.

```

FULL(0) OR PARTIAL(1)? 0
R
XFER MT0:1 SYS.SV ↘
R
CHATR SYS.SV SP ↘
R
INSTALL SYS.SV ↘
R
LOAD/A MT0:2
FILE ALREADY EXISTS, FILE: SYS.DR
FILE ALREADY EXISTS, FILE: MAP.DR
R

```

To load DOS from disk the following sequence should be performed:

- (1) Turn on CPU, disk, and TTY;
- (2) Set panel data switches to 100020;
- (3) Raise the RESET panel switch and then raise the PROGRAM LOAD panel switch;
- (4) The system will respond as follows:

DOS REV 05

Press the continue panel switch and DOS responds:

R

There is not enough disk space on the present NOVA to accommodate the complete Disk Operating System plus the emulator system. Therefore, to delete from disk all DOS files which are not essential to preparing or executing an emulator run, the following command line should be typed directly after loading DOS.

@REMALE ↘

This frees space on the disk to allow for the emulator system and scenarios, which can then be loaded.

#### Executing Under DOS

Programs which operate under control of DOS are executed in response to a user input request entered at the system teletype. The

input message is called a command line and is processed by an executable program called the Command Line Interpreter (CLI). The CLI indicates to the user that it is ready to accept commands by typing the ready message, `R/`. The user enters a command by typing a line and depressing the RETURN key. When execution of a program running under DOS is completed, control is returned to the CLI.

When operating under DOS, depressing CTRL and A simultaneously on the system teletype causes an immediate interrupt to the executing program, regardless of the program status. This can be useful, for instance, to discontinue a run when errors have been detected. The word INT is typed by the CLI upon recognition of the CTRL-A break, and control is returned to the CLI which then types R.

## SUPPORT SOFTWARE

All support software programs operate under control of DOS. They are described in detail in Volume 9 of this series. A brief presentation of operating instructions for the most commonly needed functions is given here. This section does not include all available programs.

### Utilities

The utilities transfer data from one DOS file to another. Note that all peripheral devices are treated as files. Table I below shows some methods for moving data. Where appropriate, filenames for peripherals may be used for input or output files to the utility programs. These names include:

\$CDR	card reader input
\$TTI	teletype keyboard input
\$TTO	teletype printer output
\$LPT	line printer output

Table I  
Common Utility Programs

Operation	CLI Input Message
Card to disk	XFER/A \$CDR filename ↵ LXFER \$CDR filename ↵
Tape to disk	LOAD MT0:x [filename1 filename2...] ↵
Disk to line printer	PRINT filename1 ... ↵ PRINTL filename1 ... ↵
Disk to tape	DUMP MT0:x filename1 ... ↵

The switch /A on the XFER command causes the data to be input from the card reader (\$CDR) as ASCII data with a carriage return inserted at the end of the text on a card to denote an end of line. Without the switch the input is transferred sequentially without alteration. The LXFER program is MITRE generated and provides the capability to convert Hollerith data to ASCII (the code of the NOVA), including control characters and lower case. It also permits entry of any 8-bit value via card input. A description of the program is given in Volume 9.

Both the LOAD and DUMP commands have an additional option, /V, which causes the names of the files to be verified on the teletype. Also in these commands MT0 signifies transport 0 of the tape drive, and x designates which file on the tape is selected. The brackets indicate optional information; if no filename is specified, all non-permanent files are moved. The PRINT program lists the designated file(s) on the line printer without either a title or line numbers,

and truncates a line after 80 characters. The PRINTL program, however, lists the file(s) with both a title and line numbers, and prints lines longer than 76 characters on successive lines without associating a new line number.

### File Management

Several DOS programs may be useful in handling files containing scenarios or libraries. Table II shows some of the more common commands.

Table II

Common File Management Commands

Operation	CLI Input Message
Delete file(s) from directory and free space	DELETE filename1 ... ↵
Change filename	RENAME oldfilename newfilename ↵
Concatenate copies of files to produce a new file	APPEND newfilename filename1 ... ↵
List number of disk blocks in use and number available	DISK ↵
List names, byte count, and attributes of files in directory	LIST [filename1 ...] ↵

The specific command DELETE\*.\* deletes from disk all files which are not permanent. The LIST command with no parameters causes a listing of the byte count for each file on the teletype. In the option /L is used, the listing is printed on the line printer. If the option /A is used, all permanent files are also listed. If specific files are designated, only those specified are listed.



### Programming Aids

The two programs most often employed by an Emulator user are the EDIT and OEDIT (octal edit) programs. The EDIT program is used to build a new source file or edit an existing one. This program is described in full in Reference 2. The octal editor is used to examine and/or modify, in octal, any location in any disk file. A complete description of this program can be found in Reference 1.

## SECTION III

### MACRO PROCESSOR

#### INTRODUCTION

The basic function of a macro processor is text substitution, where a name appearing in the source code is replaced by an associated string of characters. A general purpose macro capability, including a macro library generator (MACDEF) and a macro processor (SSUB), was developed on the NOVA 800. One of the main purposes of this software is to facilitate scenario writing by (1) providing a one-to-many statement capability and (2) allowing for substitution of parameter values at the external scenario level. This permits the scenario writer to include common pieces of code in different scenarios and to change subscenario calls to in-line code, or vice-versa. Another use for the macro capability is in writing code in NOVA Assembly language, which is the means used for generating an emulator Equipment Table.

Macros may be created and saved separately in a macro library by using the MACDEF program; or they may be defined in the source file itself during execution of SSUB. Both MACDEF and SSUB are written in Extended ALGOL and operate in 24K core under control of DOS. A description of the design and implementation of the Macro Processor can be found in Volume 3 of this series of reports.

#### PREPARATION AND USE OF MACROS

The discussion of macros presented here applies to all macros whether they are defined in a library, or directly in the source code.

##### Macro Names

Macro names are identifiers consisting of ten or less alphanumeric characters.

### Macro Body

In its simplest form a macro body consists of a string of ASCII characters to replace every occurrence of the macro name in the source data. No extra spaces are inserted.

### Macro Definition

A macro definition associates an identifier (the macro name) with a string of text (the macro body). Format for a macro definition is as follows:

```
MDEF macroname (number of arguments)
macro body
.
.
.
MEND
```

The literals MDEF and MEND are left-adjusted on separate lines (or cards). The macro body consists of all characters beginning with the next line after MDEF up to, but not including, the carriage return before the MEND. If the macro has no arguments, the initial line may be terminated after the macro name.

### Macro Call

A macro call is any reference to a macro name in the source file. Formats for a call are:

```
macroname (arg 1,arg 2...)  if the macro has arguments.
macroname                  if there are no arguments.
```

Arguments are separated by commas and enclosed in parentheses.

Example 1: Simple Substitution		
Source Data: ALGOL Program		
Macro Definition	Source Code	Output Code
MDEF DIGIT ((CHAR>=60R8) AND (CHAR<=71R8)) MEND	IF DIGIT THEN GO TO EXIT;	IF ((CHAR>=60R8) AND (CHAR<=71R8)) THEN GO TO EXIT;

#### Parameter Substitution

Macro bodies may contain formal parameters which will be replaced by actual parameters (arguments) in a macro call. Up to 9 formal parameters can be used in a macro definition. Each formal parameter is specified by a \$ (dollar sign) followed by a digit n where  $0 < n < 10$ . When the macro name and its arguments are encountered by SSUB in the source code, the first positional argument will be substituted for the formal parameter \$1; the second, for \$2, etc. Formal parameters may be passed as macro arguments.

Example 2: Use of Parameters		
Source Data: NOVA Assembly		
Macro Definition	Source Code	Output Code
MDEF LDI (2) JMP .+2;MLDI (R\$1,\$2) \$2 LDA \$1,.-1 MEND	LDI (3,50)	JMP .+2;MLDI (R3,50) 50 LDA 3,.-1

Macro calls may be nested within arguments and within macro bodies.

Example 3: Nested Macro Call in Macro Argument		
Source Language: NOVA Assembly Language		
Macro Definitions	Source Code	Output Code
MDEF LDI (2)	LDI (3, DEC (50))	JMP .+2
JMP .+2		.RDX 10
\$2		50
LDA \$1,.-1		.RDX 8
MEND		LDA 3,.-1
MDEF DEC (1)		
.RDX 10		
\$1		
.RDX 8		
MEND		

#### Label Generation (The TAIL Function)

To insure that labels appearing within macro bodies will not be multiply defined, a special function \$T is provided. Each reference to \$T is replaced by a numeric value. This value is unique for each macro call, but remains constant for all \$T references within a macro body. \$T may be passed one level as a macro argument.

Example 4: Use of \$T Function		
Source Data: Scenario Assembly Code for Login Sequence		
Macro Definitions	Source Code	Output Code
MDEF FINDLIT (1)	ALLOCREGS 10	ALLOCREGS 10
L FL\$T	FINDLIT (6000)	L FL3
R ''	QCESDM002	R ''
S FL\$T \$1	FINDLIT (PASSWORD)	S FL3 6000
MEND	QXXXX	QCESDM002
	FINDLIT (SYSTEM?)	L FL4
		R ''

Example 4: Use of \$T Function (Concluded)		
Source Data: Scenario Assembly Code for Login Sequence		
Macro Definitions	Source Code	Output Code
		S FL4 PASSWORD QXXXX L FL5 R '' S FL5 SYSTEM?

Example 5: Nested Macro Calls in Macro Body		
Source Data: Scenario Assembly Code		
Macro Definitions	Source Code	Output Code
MDEF FINDLIT (1) L FL\$T R '' S FL\$T \$1 MEND MDEF BACKUP QB REDY MEND MDEF REDY FINDLIT (READY) MEND MDEF LIST **PRINT FILE** BACKUP REDY QPRINT;* EOF MEND MDEF EOF FINDLIT (FILE) MEND	LIST	**PRINT FILE** QB L FL40 R '' S FL40 READY QPRINT;* L FL42 R '' S FL42 FILE



## Character Set

Source input to both SSUB and MACDEF normally consists of ASCII characters. The results of using non-ASCII characters are not defined, although in the current version most values are processed correctly. Two known exceptions are the eight-bit values 0 and 1, which are used internally by SSUB and MACDEF and should never be included in source code for either program.

## Features

### Special Characters

\$	The dollar sign is used for three special functions performed by SSUB. It is illegal to use it otherwise in normal source data, other than in a quote string.  \$T specifies the TAIL function. \$Q specifies the quote function. \$digit is used to specify formal parameters.
'	A single quote delimits a string not to be scanned by SSUB. The string is passed with quotes.
()	Parentheses are used to enclose arguments in a macro call. Parentheses may appear elsewhere in source data.
,	Commas are used to separate macro arguments. They may also appear elsewhere in source data.

### Quotes

When a string of characters is enclosed in single quotes, it is passed on (including quotes) without being scanned.

\$Q is a special macro function which can be used to pass a string of characters including commas, leading blanks, etc., in macro arguments. \$Q is followed by a string delimited at the beginning and end by a character selected by the user. Delimiter characters may be any ASCII characters except those listed above in the special group and the space character. The expansion of \$Q is the string without delimiters. The string itself will be scanned when it is substituted for its corresponding formal parameters.

Example 6: \$Q Function		
Source Data: Scenario Assembly Code		
Macro Definition	Source Code	Output Code
MDEF INSTR (1) \$1 MEND	INSTR (\$Q*LDA 3,A*)	LDA 3,A

#### Master Macro Directory

As part of its initialization, SSUB creates a master directory which is effectively the sequential concatenation of all library directories in left-to-right order as they appear in the DOS command line. Later, if more definitions are encountered in the source file, they are added to the master directory. During an SSUB run names are never deleted, and no name duplication check is made. The directory is ordered so that if duplicate macro names occur, the text of the macro most recently added to the directory will be used.

### Notes and Restrictions

1. Single quote strings are limited to 1000 characters.
2. In an SSUB run the total of all macros in the libraries and all macros defined during the run itself cannot exceed 160.
3. Each macro library is limited to 100 macros.
4. \$Q is legal only in macro arguments.
5. The identifiers MDEF and MEND are reserved and cannot be used as macro names, or appear in any source data except in their normal use in macro definitions.
6. The file name TSUB.MB is reserved.
7. The system error message "stack overflow" usually indicates a recursion loop in macro substitution.

Example:

```
MDEF OR
COM 1, 1
      AND 1, 2 ; PERFORMS LOGICAL OR
MEND
```

When the macro OR is called, infinite recursion will occur because of the "OR" in the comment within the macro body.

9. If an unsuccessful MACDEF run has been made, the .ML file should be deleted before MACDEF is rerun with the same name. Otherwise a new file is not created and the new information is written over the old information. If

this occurs, and if the new file is to be smaller than the old file, whatever has not been overwritten will remain at the end of the file.

## SYSTEM FLOW

Overall system flows for SSUB and MACDEF are shown in Figure 1 and Figure 2, respectively. Operations taking place on the NOVA are listed at the bottom of the figures with the required DOS commands.

## OPERATING PROCEDURES

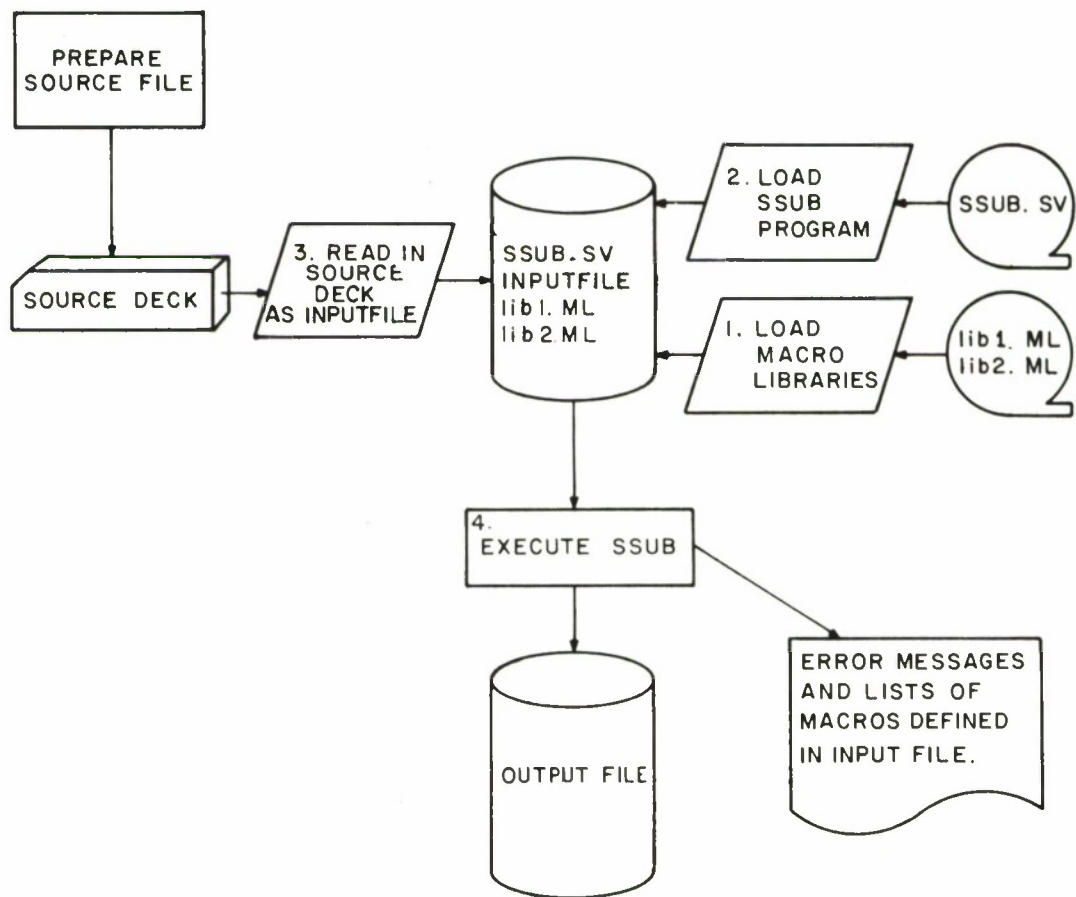
### SSUB

SSUB is the actual macro processing program; it performs the macro substitutions. Input to SSUB consists of a source file and up to four macro libraries. SSUB copies the source file into an output file. While copying, it scans the source data for macro definitions and references to macro names (macro calls). When a macro name is detected, the text of the specified macro is copied into the output file replacing the macro name. Macros may have arguments which modify the text of the macro as it is copied. For SSUB, modification consists simply of replacing formal parameter references contained in the macro body by actual parameters supplied as arguments.

To use the SSUB program the following steps should be performed:

1. Load the SSUB save file.
2. Create or load the source file.
3. Load any macro library files to be used.
4. Ready the line printer.
5. Enter the following command at the teletype:

SSUB input-file output-file library-names)

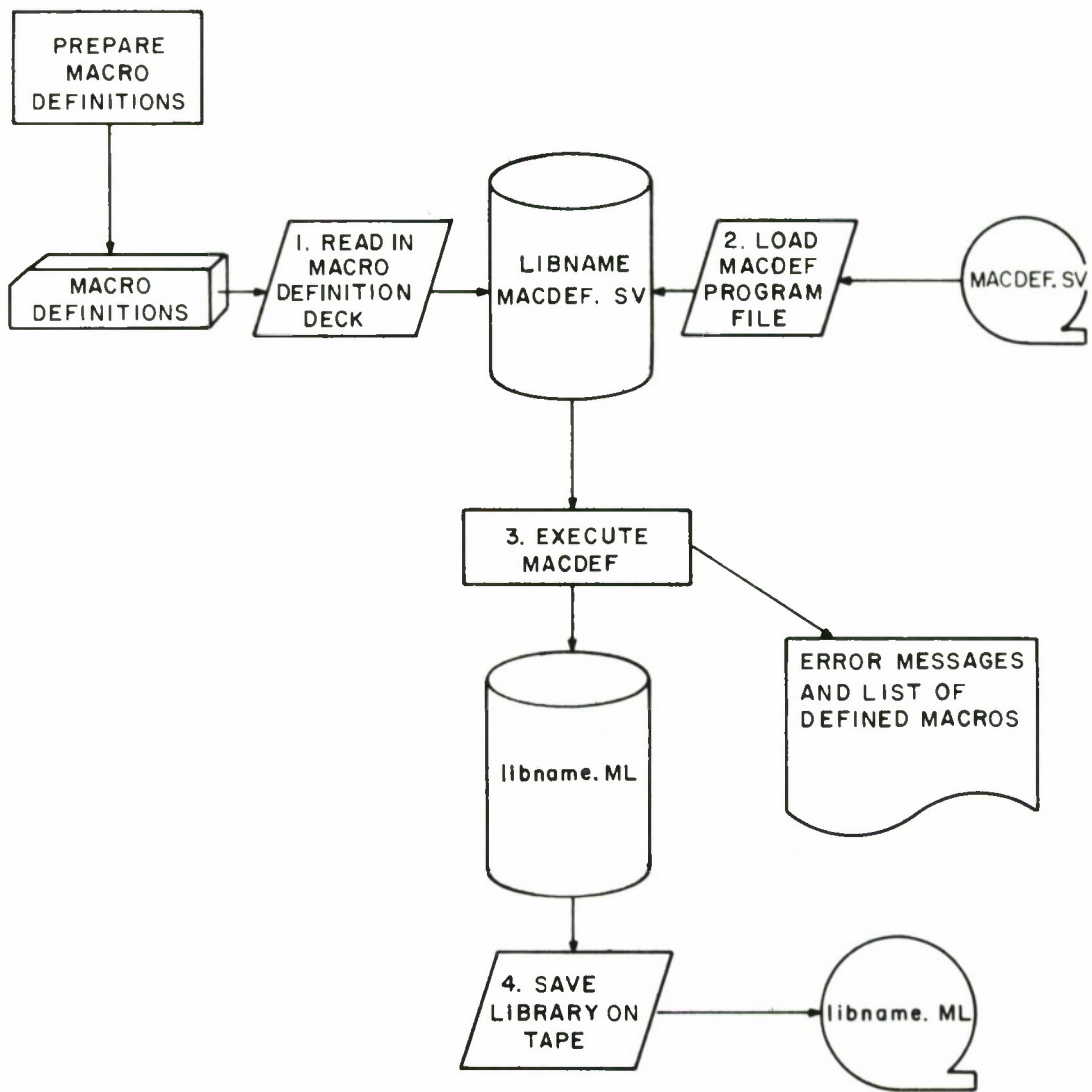


1. LOAD LIBRARY FILE	LOAD MTO: X lib1. ML lib2. ML,
2. LOAD PROGRAM FILE	LOAD MTO: X SSUB.SV,
3. LOAD INPUT FILE	XFER/A \$CDR INPUTFILE,
4. EXECUTE SSUB	SSUB INPUTFILE OUTPUTFILE lib1 lib2,

UP TO 4 LIBRARIES ARE ALLOWED ALTHOUGH ONLY 2 ARE SHOWN ABOVE.

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Figure 1. SSUB SYSTEM FLOW



1. LOAD MACDEF PROGRAM

2. LOAD MACRO DEFINITION DECK

3. EXECUTE MACDEF

4. SAVE MACRO LIBRARY

LOAD MTO:X MACDEF. SV,

XFER/A \$CDR libname,

MACDEF libname

DUMP MTO:X libname. ML

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Figure 2. MACDEF SYSTEM FLOW

Do not include the .ML after library names. Up to four names may be specified. All libraries must have been processed previously by MACDEF. Error codes will be printed on the line printer. An R ↵ typed out by the CLI indicates that the program is completed.

#### Input File

The input file contains source data containing macro calls and optionally macro definitions. It should be a normal ASCII file with a legal DOS name. Read-protect attribute must be off.

#### Output File

File must be new, with a legal DOS file name.

#### MACDEF

MACDEF is a separate program used to generate macro libraries for later use in SSUB runs. Input to the program is a file containing definitions of commonly used macros. MACDEF produces a file consisting of a library directory and the texts of all macro bodies in the library. This library file is generally saved on magnetic tape by the user for later use with the macro preprocessor program.

To use the MACDEF program the following steps should be performed:

1. Load the MACDEF save file.
2. Create a new file containing the definitions for all macros to be included in the library. The name given to this file is used to form the macro library name.
3. Ready the line printer.
4. Enter the following command at the teletype:

MACDEF library-name ↵



The names of all defined macros and any error message codes will be printed on the line printer. An R ↓ typed out by the CLI indicates that the program is finished.

5. To save the library on tape, dump the file created by MACDEF. This file is named "library-name.ML".
6. If any errors are detected by MACDEF, the original file should be corrected, the .ML file deleted, and the program rerun.

#### Input File

The input file consists of up to 100 macro definitions. Extra cards should not be placed between macro definitions. The file should be a normal ASCII file with a legal DOS name. Read protect attribute should be off.

#### Output File

The output file is created on disk by MACDEF. The name of this file is the same as the input file with a .ML extension appended.

#### Output Message

Error messages from SSUB and MACDEF are output to the printer. Error messages have the following format:

"LINE line-number ERROR NO. number"

where "line-number" identifies a line in the input file and "number" identifies the type of error. In Table III errors related to macro definitions are listed under MACDEF although they may also occur in any SSUB run.

Error messages appearing on the teletype are DOS system messages and are described in the DOS User's Manual.

Table III  
Output Messages  
For Macro Processor

<u>SSUB Errors</u>		
<u>Number</u>	<u>Problems</u>	<u>Program Action</u>
6	Input file not specified or not a legal DOS file.	Exit from program.
7	Disk read error.	Processing continues.
8	Output file already exists.	Exit from program.
9	a. Disk write error. b. Disk space exhausted.	Processing continues.
10	End of source data while processing quote string. Source data may be the input file, a macro parameter value, or a macro body.	String is terminated. If source is input file, exit from program. Otherwise processing continues.
11	Quote string greater than 1000 characters.	String terminated. Processing continues.
12	Illegal use of \$ in source data.	Processing continues.
13	Illegal number of arguments in macro call.	Macro call is ignored. Processing continues.
14	Illegal delimiter character following \$Q.	Processing continues. \$Q ignored.
15	Preprocessor storage area exceeded.	No more argument values are accepted. Processing continues but other errors will likely occur.

Table III (Continued)  
Output Messages  
For Macro Processor

<u>SSUB Errors</u>		
<u>Number</u>	<u>Problems</u>	<u>Program Action</u>
16	Error in macro call argument a. No left parenthesis when arguments expected. b. End of input source before all argument values obtained.	Macro call is ignored. Processing continues.
17	Too many macros. Limit is 160.	Program is terminated.
19	Library file could not be opened.	Program terminates.
<u>MACDEF Errors</u>		
<u>Number</u>	<u>Problems</u>	<u>Program Action</u>
7	Disk read error.	Processing continues.
9	a. Disk write error. b. Disk space exhausted.	Processing continues.
30	Number of arguments on MDEF line not a digit.	Macro is not defined. Scan to next MDEF line.
31	Illegal or missing macro on MDEF line.	Macro is not defined. Scan to next MDEF line.
32	"MDEF" not found where expected.	Continues scan for "MDEF".
33	Unexpected end of input file a. While reading macro body. b. Extra characters follow final MEND line.	Macro is terminated as if MEND found. Termination of program.

Table III (Concluded)  
Output Messages  
For Macro Processor

<u>MACDEF Errors</u>		
<u>Number</u>	<u>Problems</u>	<u>Program Action</u>
34	Input file cannot be opened.	Termination of program.
35	Attempt to put more than 100 macros in a library.	Program terminates as if end of file read.
<u>MACDEF Informational Message</u>		
"MACRO name DEFINED"		

## SECTION IV

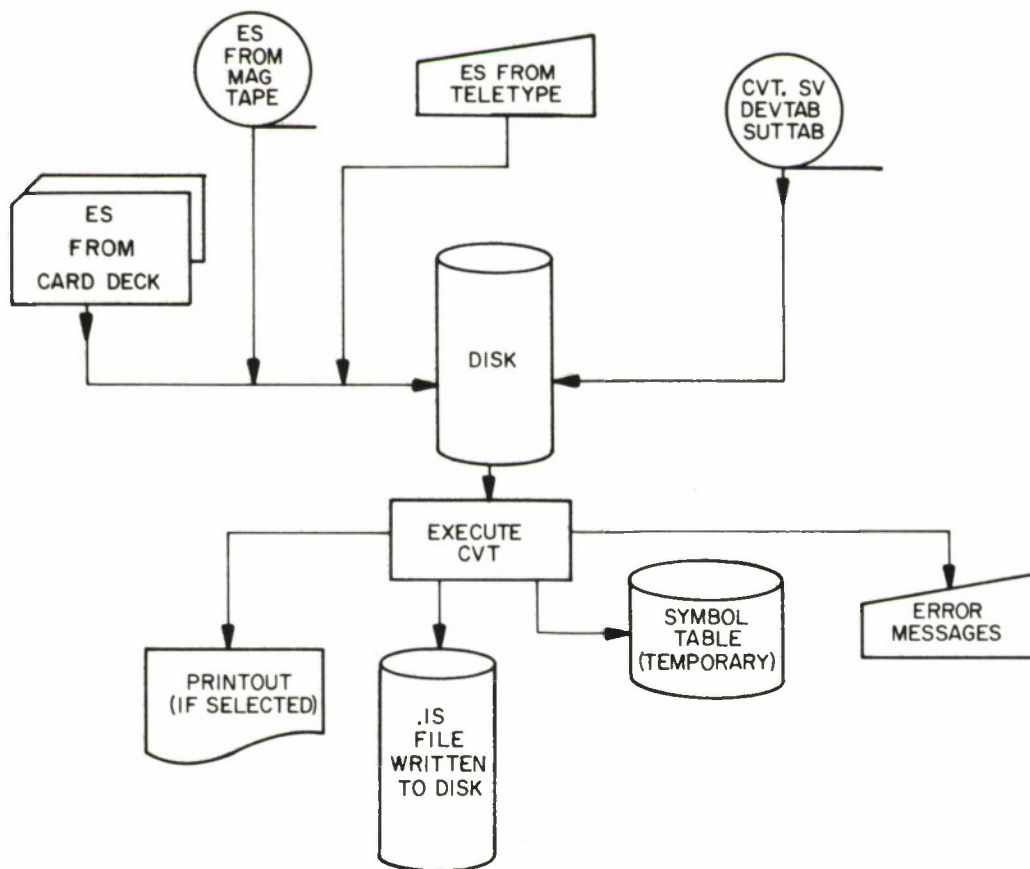
### SCENARIO ASSEMBLER

#### INTRODUCTION

The Scenario Assembler program (CVT) converts external (symbolic) scenarios into internal (absolute) scenarios which are tailored to a specific terminal type and data communications control procedure. This reduces the real-time work of the Scenario Interpreter in the area of scenario processing. To further ease the burden of the Scenario Interpreter, the Scenario Assembler performs character conversions where appropriate and adds start-of-message/end-of-message (SOM/EOM) sequences to queries to be sent to a system under test (SUT). CVT runs under Data General's Disk Operating System (DOS) and its operation must follow the conventions established by DOS. A complete description of the design and implementation of the Scenario Assembler can be found in Volume 4 of this series.

#### SYSTEM FLOW

The system flow of the assembly process is shown in Figure 3. The external scenarios may be input to the system from a card deck, from magnetic tape, or from the control teletype. The Scenario Assembler program (CVT.SV) and its associated tables, DEVTAB and SUTTAB, must be input from magnetic tape. The external scenarios, the Assembler, and the tables must reside on disk before execution is initiated. The symbol table is a temporary file written to disk during execution of the Assembler and then deleted at the end of the assembly. The listing on the line printer is also a temporary file and can be relisted only by re-executing the Assembler. The internal scenario is written to disk and can remain there or be written on magnetic tape for further use.



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Figure 3 SYSTEM FLOW OF THE SCENARIO ASSEMBLER

## OPERATING PROCEDURES

The Scenario Assembler operates with disk files only, and therefore all input files and the program save file itself must reside on disk before execution can begin.

### Preparing Files

#### External Scenario

An external scenario (ES) is a stream of characters containing the scenario instructions to be assembled. The format of the ES is shown in Figure 4. The Assembler processes the ES one instruction at a time, interpreting a carriage return as the end of the instruction. This means that a scenario instruction is not restricted in its length, but must use a carriage return only as an instruction termination character.

The first field of an instruction is the op-code field, which is a single character defining the instruction type. The op-code must always appear as the first character of an instruction with no preceding blanks. If the first character of an instruction is a blank, the instruction is treated as a comment by the Assembler. Following the op-code are 0 to 3 fields, depending upon the requirements of the particular instruction type. These fields are separated by one or more blanks except that a blank between the first field (op-code) and the second field is optional. A detailed list of instruction types and their descriptions may be found in Volume 2 of this series.

Scenarios which are to be assembled may be loaded to disk in several ways, using the Command Line Interpreter (CLI) of the Disk Operating System.



Length in Bytes	Description*
4-6	Allocate instruction to cause a set of Registers to be allocated in core.
1	Instruction type or op code field.
0-j	From 0 to 3 fields (depending on instruction type) which generates fixed length fields in the internal scenario.
0-k	Either Ø or 1 variable length character string field (depending on instruction type). May include control characters.
1	Carriage return character which signals end of a scenario instruction.
	Above 4 fields are repeated for each instruction in the scenario.
	End of scenario signalled by end of DOS file.

\*All character data

Figure 4. External Scenario Format

1. Load from tape to disk  
LOAD MT0:x scen
2. Transfer from card reader to disk  
XFER/A \$CDR scen or LXFER \$CDR scen
3. Created through the DOS Editor
4. Created as an output file of the Macro Preprocessor  
SSUB x scen (lib)

The various DOS commands and programs are fully described in the Data General Software Manuals (References 1 and 2). The Macro Preprocessor is described in Volume 3 of this series.

#### Program Files

The Assembler program and its associated conversion tables reside on tape as files, and they also must be read to disk. This can be accomplished with the DOS command

LOAD MT0:x CVT.SV DEVTAB SUTTAB ↙

This loads the Assembler program save file (CVT.SV) as well as the conversion table (DEVTAB) and start/end-of-message table (SUTTAB), from file x of a magnetic tape mounted on the system tape drive selected as transport 0.

#### Executing Assembler

The Assembler can be operated in either conversational or non-conversational mode from the control teletype (TTY). In non-conversational mode, all input parameters are included in the initial call. In conversational mode, the Assembler requests the input parameters one at a time. To execute in non-conversational mode, type:

CVT  $\begin{bmatrix} /P \\ /N \end{bmatrix}$  scen code1 code2

where:

CVT	is the name of the Assembler program
P	is the optional partial print switch which provides a printout of the ES only
N	is the optional no-print switch
scen	is the name of the external scenario to be assembled
code1	indicates the conversion method and conversion subtable from DEVTAB to be used for string conversions (see Table IV)
code2	indicates the SOM/EOM sequence subtable from SUTTAB to be used (see Table V)

In both conversational and non-conversational modes, the Assembler types the message:

TO CANCEL RUN, TYPE CONTROL-A

which indicates that the assembly process has begun. The Assembler can be interrupted at any time during assembly by depressing the Control and A characters simultaneously.

For conversational mode enter:

$$\text{CVT} \begin{bmatrix} /P \\ /N \end{bmatrix}$$

and the Assembler responds with:

ENTER EXTERNAL SCENARIO NAME

When a valid external scenario name is entered, followed by a carriage return, the program types:

ENTER CODE FOR CONVERSION

Table IV  
Available Codes for Conversion

Code	Comment
1	A one-to-one conversion to 8-bit zero-parity ASCII where the leftmost bit is the parity bit and is always set to zero.
2	A one-to-one conversion to 8-bit even-parity ASCII where the leftmost bit is the parity bit and is set to one only if it is necessary to make the total number of bits in the byte even.
3	A one-to-several conversion to 7-bit 2741 EBCDIC where the parity bit (odd parity) is the rightmost bit, and a zero bit is added at the left to fill the byte. (See Appendix I)
4	A one-to-one conversion to 8-bit one-parity ASCII where the parity bit is the leftmost bit and is always set to 1.
5	A one-to-several conversion to 7-bit 2741 EBCDIC where the seven bits are in the reverse order of those in use for code3 and a zero bit is added at the left to fill the byte.
6	A one-to-several conversion to 7-bit 2741 Correspondence Code reversed for use on the field test system. The parity bit is right most bit and a zero bit is added at the left to fill the byte.

Table IV (Concluded)

Available Codes for Conversion

Code	Comment
7	A one-to-several conversion to 7-bit 2741 Correspondence Code for use in the fixed-site system. The parity bit is the rightmost and a zero bit is added at the left to fill the byte.
8	A one-to-one conversion to 8-bit odd parity ASCII where the leftmost bit is the parity bit.

Table V  
Available Codes for SOM/EOM

Code	EOM	SOM
1	$15_8 = \text{CR}$	none
2	$223_8$	none
3	$176_8 = \sim$	none
4	$133_8 \ 37_8 = \text{CR} \textcircled{\text{C}}$	$26_8 = \textcircled{\text{D}}$
5	$215_8 = \text{CR}$	none
6	$15_8 \ 12_8 = \text{CR LF}$	none
7	none	none
8	$155_8 \ 174_8 = \text{CR} \textcircled{\text{C}}$	$64_8 = \textcircled{\text{D}}$
9	none	$26_8 \ 26_8 \ 26_8 \ 2_8$ $26_8 = \text{SYN SYN}$ SYN STX SYN
10	$04 = \text{EOT}$	none

An integer, from Table IV, should be entered, followed by a carriage return. The Assembler then asks:

ENTER CODE FOR END-OF-MESSAGE SEQUENCE

and a value from Table V should be entered. This completes the conversational mode of input.

If an assembly error occurs, the number of the line which caused it and the error message are printed on the teletype. This happens regardless of the print option selected. At the end of the run, or if Control-A is used, control is returned to the NOVA disk operating system (DOS) and an "R" is typed.

#### OUTPUT

Output of the Assembler is an internal scenario written to disk with the same name as the external scenario but with the extension .IS appended. If an internal scenario already exists for a particular scenario, the old one is automatically deleted and a new one is created for the new Assembly run. Other output of the Assembler includes optional printed listings on the line printer and messages printed to the teletype.

#### Internal Scenario

The internal scenario consists of 3 initial bytes of information, followed by processed scenario instructions, and ended by a 2-byte null word. The first information byte is an 8-bit error indicator, each bit being set only if a specific error occurred during assembly. The Scenario Interpreter will accept an internal scenario only if its first byte is zero, i.e., no errors have occurred.

The second byte of the internal scenario identifies the equipment type for which the scenario was assembled. It contains the conversion parameters used to assemble the scenario and make it specific to a



given SUT and terminal. The first four bits are the conversion code (first input parameter) and the second four bits are the SOM/EOM code (second input parameter). If the internal scenario is completely independent of any conversion parameters (i.e., no queries are sent to or received from the SUT), the scenario is called universal, the equipment type is set to zero, and the Scenario Interpreter will accept it to run on any device because it has not been tailored for a particular SUT or terminal.

The third byte indicates the number of registers to be allocated for each use of this scenario. This number may vary from 3 to 127. The Assembler determines this number, not from input parameters as with byte two, but from an Assembler Directive instruction included within the scenario itself, preferably the first instruction. This instruction (op-code = a) should appear only once per scenario; if the instruction is missing, byte three contains the default value of 8.

The scenario instructions themselves follow these three initial bytes. Each instruction begins with a 2-byte length field, giving the length in bytes of the instruction, including the length field. The 1-byte op-code field is next. Depending upon the particular instruction requirements, there may follow 0 to 3 fixed length fields, 0 or 1 variable-length-string field, or no additional fields. The instructions immediately follow one another, with no intervening delimiters. The end of the internal scenario is signalled by a 2-byte null word.

#### Optional Listings

When running the Scenario Assembler, three print options are available for printing on a line printer.

1. full printing
2. partial printing
3. no printing

A sample output listing is given in Appendix II. Full printing is selected when invoking the Assembler by typing CVT without either the P or N options in either the conversational or non-conversational mode. This produces first a listing of the external scenario. Each line contains the external line number, the starting byte address of the corresponding instruction in the internal scenario, and then up to 58 more characters of the instruction. If the instruction is longer than 59 characters, it is truncated. Interspersed in this listing are error messages listed beneath the instructions which caused them.

The listing of the internal scenario appears after the external scenario. This begins with the printing of the error indicator, equipment type, and the Register allocation bytes. Each instruction of the ES is printed, followed by the corresponding internal scenario instruction if one exists (assembler directives are never written in the internal scenario). The internal scenario instruction is printed, 2 bytes on a line, preceded by the byte address, in decimal, of the first of the two bytes. Following the two bytes is the ASCII translation of the bytes with control characters printed as blanks. Two bytes are always printed. Therefore, if the instruction has an odd number of bytes, the first byte of the next instruction is printed and is also repeated as the first byte of the next IS instruction.

The symbol table is printed after the internal scenario. Each entry of the symbol table is represented by a line of print which gives the length of the label, the label, the internal scenario byte address associated with the label, and the line number of the external scenario instruction which first referenced the label. Also printed is the number of entries in the table. An example of the full print-out is given in Appendix II.

The partial print option is selected by typing CVT/P in either the conversational or non-conversational mode. This option produces the listing of the external scenario as described above plus a printout of the name, indicator byte, equipment byte, and Register allocation byte of the internal scenario. The rest of the listing of the internal scenario and the listing of the symbol table are omitted.

The no-print option produces no listing to the line printer. As in the case of the other two options, if any errors occur, the error messages are printed on the teletype.

#### Output Messages

Messages are printed to the teletype for two reasons:

1. to request an input in conversational mode; and
2. to report an error.

Both types are self-explanatory. To correct errors in input parameters, input corrections must be typed in. For other messages, no immediate action is needed, except when it may be desirable to interrupt the assembly with a Control-A command. If assembly errors occur, they need to be corrected in the external scenario, and the external scenario needs to be reassembled. Otherwise, the error indicator byte will not be zero, and the internal scenario will not be accepted by the Scenario Interpreter. Table VI includes all possible output messages. The error message designates the number of the line which caused it, except for the LABEL UNDEFINED message which indicates the line number of the first reference to the label.

Table VI

Output Messages  
For Scenario Assembler

Messages Requiring Responses

TO CANCEL RUN, USE CONTROL-A.  
ENTER EXTERNAL SCENARIO NAME.  
SCENARIO NAME NOT FOUND, RE-ENTER OR CANCEL RUN.  
ENTER CODE FOR CONVERSION.  
CONVERSION CODE NOT IN TABLE. ENTER NEW CODE OR CANCEL  
RUN.  
ENTER CODE FOR END-OF-MESSAGE SEQUENCE.  
END-OF-MESSAGE CODE NOT IN TABLE. ENTER NEW CODE OR  
CANCEL RUN.  
TABLE NOT FOUND. CANCEL RUN.

Messages Requiring No Responses

TOO MANY FIELDS.  
LABEL --- IS UNDEFINED.  
ALLOCATE IS TOO SMALL.  
UNDEFINED OP CODE = ---.  
LITERAL MISSING.  
OUT-OF-RANGE NUMBER.  
WARNING, SHOULD DOUBLE QUOTE BE TWO SINGLE QUOTES.  
LABEL MULTIPLY DEFINED.  
FIELD MISSING.  
ILLEGAL FIELD.

## SECTION V

### EQUIPMENT TABLE

#### INTRODUCTION

The Equipment Table (ET) is not considered part of the Scenario Interpreter, but is a separate entity to be created by the user to reflect the characteristics of the equipment to be emulated. The Equipment Table consists of a set of ET entries which describe the SUT remote-terminal equipment to be emulated (as well as the control TTY), and relate it to the emulator I/O ports. Each entry (25<sub>8</sub> words long) describes one equipment component of the SUT. The format of an ET entry is given in Table V of Volume 2 of this series.

#### GENERATION

The Equipment Table must be generated by the user to depict the particular equipment configuration to be emulated. A source file (EQUIP) of the ET is normally created and then assembled with the NOVA assembler. The assembled file (EQUIP.RB) must be included when generating an emulator system, as described in Section VI.

(The EQUIP file contains several items in addition to the ET. The ET history record (ETREC), which is the second record written on the log tape during a run, is a proper subset of EQUIP. The ET itself is a proper subset of ETREC. The requirements and conventions of EQUIP, ETREC, and ET will be clarified in the next subsection.)

The ET source file, EQUIP, is normally written in NOVA assembly language, with each entry correctly formatted. This can be accomplished by creating the file line by line as needed, or by using macros and the macro processor to ease the burden of repetition. Most often macros will be used. The macros used to create an Equipment Table



for the present field-test system (including digital I/O facilities) are described in Figure 5.

An ET entry is generated by a sequence of four ordered macro calls: either ETENTRY1, ETENTRY5, ETENTRY3, ETENTRY6 or ETENTRY1, ETENTRY2, ETENTRY3, ETENTRY6. The only difference between the two sequences is that the former (ETENTRY5) allows ETEOM to be specified as a parameter whereas the latter (ETENTRY2) generates an ETEOM value of EOM1. For ease of reading the assembly listing, the ETENTRY1 card should start in column 1 and the others in column 10.\*

An input file, EQ, for an Equipment Table with macros not yet expanded is shown in Figure 6. The six macro definitions used to create the EQUIP file from the EQ file appear at the beginning of the EQ file. A seventh macro definition occurs later in lines 89-91 but is not essential to the proper formatting of the file. Figure 7 shows a portion of the EQUIP file after execution of the macro processor. In this form, the file is acceptable to the NOVA assembler. Figure 8 shows a portion of the ET after it has been assembled. Appendix III contains a complete listing of EQUIP.RB, the assembled Equipment Table file.

#### REQUIREMENTS AND CONVENTIONS

The following mandatory requirements must be met by EQUIP, ETREC, and the ET. Line numbers referenced below are those of Figure 6.

1. The following (defined below) must be declared as entry points (external/global variables) as shown at lines 44 to 48: E00000, E0, E1, ETREC, ETEND, ETENT, E2, ETLEN.

---

\*For the lab system (one with no digital I/O), ETENTRY6 can be eliminated and ETENTRY3 modified to generate zero values for words  $22_8$  -  $24_8$ .

MACRO NAME	PURPOSE	PARAMETERS
ETENTRY1	Generates words 0-5 of ET entry.	<p>\$1 = NOVA assembler label for ET Entry</p> <p>\$2 = ETR0. Should be initialized to zero.</p> <p>\$3 = first ASCII character of ETYPE.</p> <p>\$4 = second ASCII character of ETYPE.</p> <p>\$5 = ETID in decimal.</p> <p>\$6 = CHILD. The NOVA assembler label for some other ET entry or zero.</p> <p>\$7 = LINK. The NOVA assembler label for some other ET entry or zero.</p> <p>\$8 = PARNT. The NOVA assembler label for some other ET entry or zero.</p>
ETENTRY2	Generates words 6-17 <sub>8</sub> of ET entry with help of ETENTRY4	<p>\$1 = ETRAT in octal.*</p> <p>\$2 = TERMT in octal.</p> <p>\$3 = STATI. Enter I or U.</p> <p>\$4 = PORTO in octal.</p> <p>\$5 = PORTI in octal.</p> <p>\$6 = SPRTO in octal.</p> <p>\$7 = SPRTI in octal.</p>
*To enter a decimal value, follow it with a decimal point.		

Figure 5. Equipment Table Macros



MACRO NAME	PURPOSE	PARAMETERS
ETENTRY3	Generates words $20_8 - 21_8$ of ET entry	<p>\$1 = SUTAD in octal.</p> <p>\$2 = ETIND in octal. Bits 1, 2, and 3 should be initialized to zero, the others as desired. Bit 0 must be set to 1 for the control TTY.</p> <p>\$3 = BYTEL in octal.</p> <p>\$4 = PARTY in octal.</p>
ETENTRY6	Generates words $22_8 - 24_8$ of ET entry	<p>\$1 = CCC+1 in ETDID. Number of digital inputs in decimal.</p> <p>\$2 = BSSSS in ETDID. First input in octal.</p> <p>\$3 = DDDDDD in ETDID. Device number in octal.</p> <p>\$4 = CCC+1 in ETDOD. Number of digital outputs in decimal.</p> <p>\$5 = BSSSS in ETDOD. First output in octal.</p> <p>\$6 = DDDDDD in ETDOD. Device number in octal.</p> <p>\$7 = ETDOA.</p>

Figure 5. Equipment Table Macros (Continued)

MACRO NAME	PURPOSE	PARAMETERS
ETENTRY5	Generates words 6-17 <sub>8</sub> of ET entry with help of ETENTRY4	\$1 = ETRAT in octal*. \$2 = TERMT in octal. \$3 = STATI. Enter I or U. \$4 = PORTO in octal. \$5 = PORTI in octal. \$6 = SPRTO in octal. \$7 = SPRTI in octal. \$8 = ETEOM.
ETENTRY4	Generates words 14 <sub>8</sub> - 17 <sub>8</sub> when called by ETENTRY2 or ETENTRY5	\$1 = unused. Enter zero. \$2 = TERMT in octal. \$3 = STATI. Enter I or U. \$4 = PORTO in octal. \$5 = PORTI in octal. \$6 = SPRTO in octal. \$7 = SPRTI in octal.
* To enter a decimal value, follow it with a decimal point		

Figure 5. Equipment Table Macros (Concluded)

The following labels must be used for particular ET entries (although the user may also assign labels of his own choice to the same entries):

2. The label E00000 must be used for the first ET entry which must be the control TTY (see line 83).
3. The label E0 must be used for the ET entry for the control TTY. Therefore, E0 is equivalent to E00000. See line 84; the first parameter of the macro ETENTRY1 is the label E0.
4. The label E1 must be used for the ET entry for the single asynchronous device in the lab system (see line 89).
5. The label E2 must be used for the ET entry for the first asynchronous device in the field-test system and for the first DCM device in the lab system. The Exec assumes that the ET entries for the asynchronous devices in the 64-line field-test system are ordered as shown in Figure 9 and that those for the DCM in the lab system are ordered as shown in Figure 10. (Figure 11 shows the ordering and device numbers used for the 16-line field-test system which are those of Figure 6, lines 96-159.)

ETREC must be defined so that:

6. It includes the entire ET, preceded by four words as shown in Table XII of Volume 2 of this series (see lines 79-191).

EQUIP must include the following definitions:

7. ETEND must contain the length of an ET entry (see line 78).
8. ETLEN is equivalent to ETEND (see line 195).
9. ETENT must contain the number of ET entries (see line 194).
10. One or more EOM lists must be established as in lines 196-225. An EOM list is of variable length, terminated by -1

```

EQ
1      .TITL EQUIP
2      MDEF ETENTRY1(8)
3      $11
4          $2          JETRO
5          "$3*256.+"$4      JETYPE
6          $5          JETID
7          $6          JCHILD
8          $7          JLINK
9          $8          JPARNT
10     MEND
11     MDEF ETENTRY2(7)
12     $1
13         0          JETRAT
14         EQ+1      JETQBP
15         0          JETEDM
16         0          JETRSP
17         0          JETPAD
18     ETENTRY4(0,$2,$3,$4,$5,$6,$7)
19     MEND
20     MDEF ETENTRY4(7)
21         0*256.+$7      JETLGA, ETLGN
22         $2*256.+$3      JETERMT, STATI
23         $4*256.+$5      JPORTO, PORTI
24         $6*256.+$7      JSPTO, SPRTI
25     MEND
26     MDEF ETENTRY3(8)
27     $1
28         0          JETRAT
29         $5          JETQBP
30         0          JETEUM
31         0          JETRSP
32         0          JETPAD
33     ETENTRY4(0,$2,$3,$4,$5,$6,$7)
34     MEND
35     MDEF ETENTRY3(4)
36     $1*256.+$28d      JSUTAD, ETIND
37         $3*256.+$4      JBYTEL, PARTY
38     MEND
39     MDEF ETENTRY6(7)
40     $1.-1*102+$287+$3      JETDID
41         $4.-1*102+$587+$6      JETDUD
42         $7          JETDOA
43     MEND
44         .ENT E0000,E0,E1,ETREC
45         .ENT ETEND
46         .ENT ETENT
47         .ENT E2
48         .ENT ETLEN
49         .DUSR A=101
50         .DUSR I=111
51         .DUSR S=123
52         .DUSR T=124
53         .DUSR U=125
54         .DUSR W=127
55         .DUSR E=105
56         .DUSR Z=132
57         .DUSR N=116
58         .DUSR O=117
59         .DUSR RT1=139.
60         .DUSR BL1=7.
61         .DUSR BL2=8.
62         .DUSR PT1=0

```

Figure 6. File EQ of Equipment Table (Macros not Expanded)

```

53      .DUSN PT2=N
54      .DUSN DDDLINE=3*16,+4
55      .DUSN 1342848=3*16,+4
56      .DUSN 18M2260=3*16,+4
57      .DUSN 18M1053=5
58      .DUSN 02000=6
59      .DUSN 18M2741=7
70      .DUSN 12741=3*16,+4
71      .DUSN ZASC1=1*16,+1
72      .DUSN ZASC6=1*16,+6
73      .DUSN EASC2=2*16,+2
74      .DUSN EASC5=2*16,+5
75      .TXTM C
76      .ZHEL
77      .NREL
78  ETEND:  EEND=E2000
79  ETREC:  ZP20+"E          ;USED TO WRITE ET ON TAPE
80      E2000=E2000+4
81      "
82      .+1
83  E2000:
84  ETENTRY1(E2,M,C,T,0,0,E1,0)
85      ETENTRY5(110.,ZASC6,I,11,10,0,0,EOM2)
86      ETENTRY3(0,1,9.,Z)
87      ETENTRY6(1,0,0,1,0,0,0)
88  E2END:
89  ETENTRY1(E1,M,C,S,14,0,E2,0)
90      ETENTRY5(0110.,EASC2,I,51,52,1,1,EOM3)
91      ETENTRY3(15.,1,9.,E)
92      ETENTRY6(1,0,0,1,0,0,0)
93  MDEF TTY33
94  I2741
95  MEND
96  ETENTRY1(E2,M,T,Y,1,0,E3,0)
97      ETENTRY5(RT1,TTY33,I,43,42,1,1,EOM4)
98      ETENTRY3(30.,3,8L1,PT1)
99      ETENTRY6(2,00.,71,4,00.,66,D066A)
100 ETENTRY1(E3,M,T,Y,2,0,E4,0)
101      ETENTRY5(RT1,TTY33,I,43,42,2,2,EOM5)
102      ETENTRY3(31.,0,8L1,PT1)
103      ETENTRY6(2,02.,71,4,04.,66,D066A)
104 ETENTRY1(E4,M,T,Y,3,0,E4A,0)
105      ETENTRY2(RT1,TTY33,I,43,42,3,3)
106      ETENTRY3(32.,0,8L1,PT1)
107      ETENTRY6(2,04.,71,4,08.,66,D066A)
108 ETENTRY1(E4A,M,T,Y,4,0,E13,0)
109      ETENTRY2(RT1,TTY33,I,43,42,4,4)
110      ETENTRY3(29.,0,8L1,PT1)
111      ETENTRY6(2,06.,71,4,12.,66,D066A)
112 ETENTRY1(E13,M,T,Y,5,0,E14,0)
113      ETENTRY2(RT1,TTY33,I,43,42,5,5)
114      ETENTRY3(33.,0,8L1,PT1)
115      ETENTRY6(2,08.,71,4,16.,66,D066B)
116 ETENTRY1(E14,M,T,Y,6,0,E15,0)
117      ETENTRY2(RT1,TTY33,I,43,42,6,6)
118      ETENTRY3(34.,0,8L1,PT1)
119      ETENTRY6(2,10.,71,4,20.,66,D066B)
120 ETENTRY1(E15,M,T,Y,7,0,E16,0)
121      ETENTRY2(RT1,TTY33,I,43,42,7,7)
122      ETENTRY3(35.,0,8L1,PT1)
123      ETENTRY6(2,12.,71,4,24.,66,D066B)
124 ETENTRY1(E16,M,T,Y,8,0,E17,0)
125      ETENTRY2(RT1,TTY33,I,43,42,8.,8.)

```

Figure 6. File EQ of Equipment Table (Macros not Expanded)  
(continued)

```

126      ETENTRY3(36.,0,BL1,PT1)
127      ETENTRY6(2,14.,71,4,28.,66,DU66A)
128 ETENTRY1(E17.,,T,Y,9,0,E18,0)
129      ETENTRY2(RT1,TTY33,I,45,44,1,1)
130      ETENTRY3(37.,0,BL1,PT1)
131      ETENTRY6(2,16.,71,4,28.,67,DU67A)
132 ETENTRY1(E18.,,T,Y,10,0,E19,0)
133      ETENTRY2(RT1,TTY33,I,45,44,2,2)
134      ETENTRY3(38.,0,BL1,PT1)
135      ETENTRY6(2,18.,71,4,28.,67,DU67A)
136 ETENTRY1(E19.,,T,Y,11,0,E20,0)
137      ETENTRY2(RT1,TTY33,I,45,44,3,3)
138      ETENTRY3(39.,0,BL1,PT1)
139      ETENTRY6(2,20.,71,4,28.,67,DU67A)
140 ETENTRY1(E20.,,T,Y,12,0,E21,0)
141      ETENTRY2(RT1,TTY33,I,45,44,4,4)
142      ETENTRY3(40.,0,BL1,PT1)
143      ETENTRY6(2,22.,71,4,28.,67,DU67A)
144 ETENTRY1(E21.,,T,Y,13,0,E22,0)
145      ETENTRY2(RT1,TTY33,I,45,44,5,5)
146      ETENTRY3(41.,0,BL1,PT1)
147      ETENTRY6(2,24.,71,4,28.,67,DU67A)
148 ETENTRY1(E22.,,T,Y,14,0,E23,0)
149      ETENTRY2(RT1,TTY33,I,45,44,6,6)
150      ETENTRY3(42.,0,BL1,PT1)
151      ETENTRY6(2,26.,71,4,28.,67,DU67A)
152 ETENTRY1(E23.,,T,Y,15,0,E24,0)
153      ETENTRY2(RT1,TTY33,I,45,44,7,7)
154      ETENTRY3(43.,0,BL1,PT1)
155      ETENTRY6(2,28.,71,4,28.,67,DU67A)
156 ETENTRY1(E24.,,T,Y,16,0,E5,0)
157      ETENTRY2(RT1,TTY33,I,45,44,8.,8.)
158      ETENTRY3(44.,0,BL1,PT1)
159      ETENTRY6(2,30.,71,4,28.,67,DU67A)
160 ETENTRY1(E5.,,L,N,5,E6,0,0)
161      ETENTRY2(2400.,,DDLINE,I,32,31,0,0)
162      ETENTRY3(43.,0,BL2,PT2)
163      ETENTRY6(1,0,0,1,0,0,0)
164 ETENTRY1(E6.,,C,N,6,E7,E5)
165      ETENTRY2(2400.,,IBM2848,I,32,31,0,0)
166      ETENTRY3(116,0,BL2,PT2)
167      ETENTRY6(1,0,0,1,0,0,0)
168 ETENTRY1(E7.,,C,N,7,E11,0,E5)
169      ETENTRY2(2400.,,IBM2848,U,32,31,0,0)
170      ETENTRY3(250,0,BL2,PT2)
171      ETENTRY6(1,0,0,1,0,0,0)
172 ETENTRY1(E8.,,D,S,8,0,E9,E6)
173      ETENTRY2(2400.,,IBM2260,I,32,31,0,0)
174      ETENTRY3(240,0,BL2,PT2)
175      ETENTRY6(1,0,0,1,0,0,0)
176 ETENTRY1(E9.,,D,S,9,0,E10,E6)
177      ETENTRY2(2400.,,IBM2260,I,32,31,0,0)
178      ETENTRY3(241,0,BL2,PT2)
179      ETENTRY6(1,0,0,1,0,0,0)
180 ETENTRY1(E10.,,T,10,0,0,E6)
181      ETENTRY2(150.,,IBM1053,U,32,0,0,0)
182      ETENTRY3(242,0,BL2,PT2)
183      ETENTRY6(1,0,0,1,0,0,0)
184 ETENTRY1(E11.,,D,S,11,0,E12,E7)
185      ETENTRY2(2400.,,IBM2260,U,32,31,0,0)
186      ETENTRY3(244,0,BL2,PT2)
187      ETENTRY6(1,0,0,1,0,0,0)
188 ETENTRY1(E12.,,D,S,12,0,0,E7)

```

Figure 6. File EQ of Equipment Table (Macros not Expanded)  
(continued)

```

189      ETRIM2(2420.,IBM2200,U,32,31,0,0)
190      ETRIM3(245,0,BL2,PT2)
191      ETRIM5(1,0,0,1,0,0,0)
192 EQU09:
193 LEN      =EQU0-EQU00
194 ETRINT:   EQU09-EQU00/LEN
195 ETRLEN:   LEN
196 EUM1:     37
197          -1
198          -1
199          -1
200          -1
201          -1
202 EUM2:     12
203          5
204          37
205          -1
206          -1
207          -1
208 EUM3:     -1
209          -1
210          -1
211          -1
212          -1
213          -1
214 EUM4:     37
215          -1
216          -1
217          -1
218          -1
219          -1
220 EUM5:     37
221          43
222          -1
223          -1
224          -1
225          -1
226 D066A:    5
227 D066B:    5
228 D067A:    5
229 D067B:    5
230          .END

```

Figure 6. File EQ of Equipment Table (Macros not Expanded)  
(concluded)



```

EQUIP
1      .TITLE EQUIP
2      .ENT E0000,FM,E1,ETREC
3      .ENT ETEND
4      .ENT ETENT
5      .ENT E2
6      .ENT ETLGN
7      .DUSH A=101
8      .DUSH I=111
9      .DUSH S=123
10     .DUSH T=124
11     .DUSH U=125
12     .DUSH V=127
13     .DUSH E=105
14     .DUSH Z=132
15     .DUSH N=116
16     .DUSH O=117
17     .DUSH PT1=135.
18     .DUSH BL1=7.
19     .DUSH BL2=8.
20     .DUSH PT1=0
21     .DUSH PT2=N
22     .DUSH OPDLINE=3*16.+4
23     .DUSH IRM2048=3*16.+4
24     .DUSH IRM2200=3*16.+4
25     .DUSH IRM1053=5
26     .DUSH U2000=6
27     .DUSH IRM2741=7
28     .DUSH I2741=3*16.+4
29     .DUSH ZASC1=1*16.+1
30     .DUSH ZASC6=1*16.+6
31     .DUSH EASC2=2*16.+2
32     .DUSH EASC5=2*16.+5
33     .TXM 5
34     .ZREL
35     .NREL
36     ETEND: E0END=E0000
37     ETREC: E0REC="E          /USED TO WRITE ET ON TAPE
38           E0999=E0000+4
39           "H
40           .+1
41     E0000:
42     E0:
43         W          /ETR0
44         "C*256.+"T    /ETYPE
45         0.          /ETID
46         0          /CHILD
47         E1          /LINK
48         0          /PAKNT
49         110.          /ETRAT
50         W          /ETQBP
51         E0M2          /ETEOM
52         W          /ETRSP
53         W          /ETPAD
54         W          /RRING,PRING
55         0*256.+37    /ETLGA,ETLGN
56         ZASC6*256.+1 /TERMT,STATI
57         11*256.+10   /PURTO,PORTI
58         0*256.+0     /SPRTO,SPRTI
59         0*256.+188   /SUTAD,ETIND
60         5*256.+2     /BYTEL,PARTY
61         1.-1*102+007+0 /ETDID
62         1.-1*102+007+0 /ETDOU

```

Figure 7. Portion of File EQUIP of Equipment Table  
(Macros Expanded)

63					
64	E0END:				
65	E1:				
66					
67		"D=256.+ "S			
68		14.			
69					
70		E2			
71					
72		0110.			
73					
74		E0H4			
75					
76					
77					
78		L*256.+37			
79		EAS02*256.+I			
80		51*256.+50			
81		1*256.+1			
82		15.*255.+088			
83		5.*256.+E			
84		1.-1*182+087+1			
85		1.-1*182+087+0			
86					
87	E2:				
88					
89		"T=256.+ "Y			
90		1.			
91					
92		E3			
93					
94		RT1			
95					
96		E0H4			
97					
98					
99					
100		W*256.+37			
101		12741*256.+I			
102		43*256.+42			
103		1*256.+1			
104		30.*255.+088			
105		8L1*256.+PT1			
106		2.-1*182+00.87+71			
107		4.-1*182+00.87+56			
108		0086A			
109	E3:				
110					
111		"T=256.+ "Y			
112		2.			
113					
114		E4			
115					
116		RT1			
117					
118		E0H5			
119					
120					
121					
122		L*256.+37			
123		12741*256.+I			
124		43*256.+42			
125		2*256.+2			

Figure 7. Portion of File EQUIP of Equipment Table  
(Continued)

126	31.*256.*088		/SUTAD, ETIND
127	5L1*256.*PT1		/BYTEL, PARTY
128	2.-1*182*02.87*71		/ETDID
129	4.-1*182*04.87*66	/ETDOD	
130	DD08A	/ETDOA	
131	E4:		
132	A	/ETM0	
133	"T*256.*"Y	/ETYPE	
134	3.	/ETID	
135	0	/CHILD	
136	E4A	/LINK	
137	0	/PARNT	
138	RT1		/ETRAT
139	0	/ETQBP	
140	E0.11	/ETEOM	
141	0	/ETRSP	
142	0	/ETPAD	
143	0	/RRING,PRING	
144	0*256.*37	/ETLGA, ETLGN	
145	12741*256.*I	/TERMT, STATI	
146	43*256.*42	/PORTO, PORTI	
147	3*256.*3	/SPRTO, SPRTI	
148	32.*256.*088		/SUTAD, ETIND
149	5L1*256.*PT1		/BYTEL, PARTY
150	2.-1*182*04.87*71		/ETDID
151	4.-1*182*08.87*66	/ETDOD	
152	DD08A	/ETDOA	
153	E4A:		
154	A	/ETM0	
155	"T*256.*"Y	/ETYPE	
156	4.	/ETID	
157	0	/CHILD	
158	E13	/LINK	
159	0	/PARNT	
160	RT1		/ETRAT
161	0	/ETQBP	
162	E0M1	/ETEOM	
163	0	/ETRSP	
164	0	/ETPAD	
165	0	/RRING,PRING	
166	0*256.*37	/ETLGA, ETLGN	
167	12741*256.*I	/TERMT, STATI	
168	43*256.*42	/PORTO, PORTI	
169	4*256.*4	/SPRTO, SPRTI	
170	29.*256.*088		/SUTAD, ETIND
171	5L1*256.*PT1		/BYTEL, PARTY
172	2.-1*182*06.87*71		/ETDID
173	4.-1*182*12.87*66	/ETDOD	
174	DD08A	/ETDOA	
175	E13:		
176	A	/ETM0	
177	"T*256.*"Y	/ETYPE	
178	5.	/ETID	
179	0	/CHILD	
180	E14	/LINK	
181	0	/PARNT	
182	RT1		/ETRAT
183	0	/ETQBP	
184	E0M1	/ETEOM	
185	0	/ETRSP	
186	0	/ETPAD	
187	0	/RRING,PRING	
188	0*256.*37	/ETLGA, ETLGN	

Figure 7. Portion of File EQUIP of Equipment Table  
(Continued)

567	BL2*256.+PT2	BYTEL, PARTY
568	1.-1*182+087+0	ETDID
569	1.-1*182+087+0	ETDDD
570	0	ETDOA
571 E11:		
572	0	ETRO
573	"0*256.+ "S	ETYPE
574	11.	ETID
575	0	CHILD
576	E12	LINK
577	E7	PARNT
578	2400.	ETRAT
579	0	ETQBP
580	EDM1	ETEOM
581	0	ETRSP
582	0	ETPAD
583	0	RRING, PRING
584	0*256.+37	ETLGA, ETLGN
585	IBM2260*256.+U	TERMT, STATI
586	32*256.+31	PORTO, PORTI
587	0*256.+0	SPRTO, SPRTI
588	244*256.+088	SUTAD, ETIND
589	BL2*256.+PT2	BYTEL, PARTY
590	1.-1*182+087+0	ETDID
591	1.-1*182+087+0	ETDDD
592	0	ETDOA
593 E12:		
594	0	ETRO
595	"0*256.+ "S	ETYPE
596	12.	ETID
597	0	CHILD
598	0	LINK
599	E7	PARNT
600	2400.	ETRAT
601	0	ETQBP
602	EDM1	ETEOM
603	0	ETRSP
604	0	ETPAD
605	0	RRING, PRING
606	0*256.+37	ETLGA, ETLGN
607	IBM2260*256.+U	TERMT, STATI
608	32*256.+31	PORTO, PORTI
609	0*256.+0	SPRTO, SPRTI
610	245*256.+088	SUTAD, ETIND
611	BL2*256.+PT2	BYTEL, PARTY
612	1.-1*182+087+0	ETDID
613	1.-1*182+087+0	ETDDD
614	0	ETDOA
616 E9999:		
616 LEN	=E9END-E0000	
617 ETENT:	E9999-E0000/LEN	
618 ETLEN:	LEN	
619 EOM1:	37	
620	-1	
621	-1	
622	-1	
623	-1	
624	-1	
626 EOM2:	12	
626	5	
627	30	
628	-1	
629	-1	

Figure 7. Portion of File EQUIP of Equipment Table  
(Continued)

630		-1
631	EDM3:	-1
632		-1
633		-1
634		-1
635		-1
636		-1
637	EDM4:	37
638		-1
639		-1
640		-1
641		-1
642		-1
643	EDM5:	37
644		43
645		-1
646		-1
647		-1
648		-1
649	D066A:	0
650	D066B:	0
651	D067A:	0
652	D067B:	0
653		.END

Figure 7. Portion of File EQUIP of Equipment Table  
(Concluded)

0001 EQUIP

```

.TITL EQUIP
.ENT E0000,E0,E1,ETREC
.ENT ETEND
.ENT ETENT
.ENT E2
.ENT ETLEN
000101 .DUSR A=101
000111 .DUSR I=111
000123 .DUSR S=123
000124 .DUSR T=124
000125 .DUSR U=125
000127 .DUSR W=127
000105 .DUSR E=105
000132 .DUSR Z=132
000116 .DUSR N=116
000117 .DUSR O=117
000247 .DUSR RT1=135.
000007 .DUSR BL1=7.
000010 .DUSR BL2=8.
000117 .DUSR PT1=0
000116 .DUSR PT2=N
000064 .DUSR DDDLNE=3+16.+4
000064 .DUSR IBM2048=3+16.+4
000064 .DUSR IBM2260=3+16.+4
000005 .DUSR IBM1053=5
000006 .DUSR D2000=6
000007 .DUSR IBM2741=7
000064 .DUSR I2741=3+16.+4
000021 .DUSR ZASC1=1+16.+1
000026 .DUSR ZASC6=1+16.+6
000042 .DUSR EASC2=2+16.+2
000045 .DUSR EASC5=2+16.+5
000005 .TXTM 5
.ZREL
.NREL
00000'000025 ETEND: E0END-E0000
00001'000105 ETREC: 20000+"E ;USED TO WRITE ET ON TAPE
00002'001046 E9999-E0000+4
00003'000110 "H
00004'000025' .+1
          E0000:
          E0:
00005'000000 0 ;ETR0
00006'041524 "C+256.+"T ;ETYPE
00007'000000 0. ;ETID
00010'000000 0 ;CHIL0
00011'000032' E1 ;LINK
00012'000000 0 ;PARNT
00013'000156 110. ;ETRAT
00014'000000 0 ;ETQBP
00015'001057' E0M2 ;ETEOM
00016'000000 0 ;ETRSP
00017'000000 0 ;ETPAD
00020'000000 0 ;RRING,PRING
00021'000037 0+256.+37 ;ETLGA,ETLGN
00022'013111 ZASC6+256.+I ;TERMT,STATI
00023'004410 11+256.+10 ;PORTO,PONTI
00024'000000 0+256.+0 ;SPRTO,SPRTI
00025'000200 0+256.+108 ;SUTAD,ETIND

```

Figure 8. Portion of File EQUIP.RB, Assembled Equipment Table

0002 EQUIP		
00026'004132	8.*256.*Z	;BYTEL, PARTY
00027'000000	1.-1*1B2+0B7+0	;ETDIO
00030'000000	1.-1*1B2+0B7+0	;ETDOO
00031'000000	0	;ETDOA
E0E00:		
E1:		
00032'000000	0	;ETRO
00033'042123	"D*256.*"S	;ETYPE
00034'000016	14.	;ETIO
00035'000000	0	;CHIL0
00036'000057	E2	;LINK
00037'000000	0	;PARNT
00040'000155	0110.	;ETRAT
00041'000000	0	;ETQBP
00042'001065	EOM3	;ETEUM
00043'000000	0	;ETRSP
00044'000000	0	;ETPA0
00045'000000	0	;RRING,PRING
00046'000037	0*256.*37	;ETLGA, ETLGN
00047'021111	EASC2*256.*I	;TERMT, STATI
00050'024450	51*256.*50	;PORT0, PORTI
00051'000401	1*256.*1	;SPRTO, SPRTI
00052'007400	15.*256.*0B9	;SUTAD, ETINO
00053'004105	8.*256.*E	;BYTEL, PARTY
00054'000000	1.-1*1B2+0B7+0	;ETDIO
00055'000000	1.-1*1B2+0B7+0	;ETDOO
00056'000000	0	;ETDOA
E2:		
00057'000000	0	;ETRO
00060'052131	"T*256.*"Y	;ETYPE
00061'000001	1.	;ETIO
00062'000000	0	;CHIL0
00063'000104	E3	;LINK
00064'000000	0	;PARNT
00065'000207	RT1	;ETRAT
00066'000000	0	;ETQBP
00067'001073	EOM4	;ETEUM
00070'000000	0	;ETRSP
00071'000000	0	;ETPA0
00072'000000	0	;RRING,PRING
00073'000037	0*256.*37	;ETLGA, ETLGN
00074'032111	I2741*256.*I	;TERMT, STATI
00075'021442	43*256.*42	;PORT0, PORTI
00076'000401	1*256.*1	;SPRTO, SPRTI
00077'017000	30.*256.*0B8	;SUTAD, ETINO
00100'003517	BL1*256.*PT1	;BYTEL, PARTY
00101'002071	2.-1*1B2+00.B7+71	;ETDIO
00102'060066	4.-1*1B2+00.B7+66	;ETDOO
00103'001107	0066A	;ETDOA
E3:		
00104'000000	0	;ETRO
00105'052131	"T*256.*"Y	;ETYPE
00106'000002	2.	;ETIO
00107'000000	0	;CHIL0
00110'000131	E4	;LINK
00111'000000	0	;PARNT
00112'000207	RT1	;ETRAT
00113'000000	0	;ETQBP
00114'001101	EOM5	;ETEUM

Figure 8. Portion of File EQUIP.RB, Assembled Equipment Table (Continued)



0011 EQUIP			
01020'000000	1.-1*182+087+0	JETODD	
01021'000000	0	JETDOA	
	E12:		
01022'000000	0	JETR0	
01023'042123	"0*256.+ "S	JETYPE	
01024'000014	12.	JETID	
01025'000000	0	JCHILO	
01026'000000	0	JLINK	
01027'000051	E7	JPARNT	
01030'004540	2400.	JETRAT	
01031'000000	0	JETQBP	
01032'001051	EOM1	JETEOM	
01033'000000	0	JETRSP	
01034'000000	0	JETPAD	
01035'000000	0	JRRING, PRING	
01036'000037	0*256.+37	JETLGA, ETLGN	
01037'032125	IBM2260*256.+U	JTERMT, STATI	
01040'015031	32*256.+31	JPORTO, PORTI	
01041'000000	0*256.+0	JSPRTO, SPRTI	
01042'122400	245*256.+088	JUTAD, ETIND	
01043'004116	0L2*256.+PT2	JBYTEL, PARTY	
01044'000000	1.-1*182+087+0	JETOID	
01045'000000	1.-1*182+087+0	JETDOD	
01046'000000	0	JETDOA	
	E9999:		
000025	LEN	=E0ENO-E0000	
01047'000032	ETENT:	E9999-E0000/LEN	
01050'000025	ETLEN:	LEN	
01051'000037	EOM1:	37	
01052'177777		-1	
01053'177777		-1	
01054'177777		-1	
01055'177777		-1	
01056'177777		-1	
01057'000012	EOM2:	12	
01060'000005		5	
01061'000030		30	
01062'177777		-1	
01063'177777		-1	
01064'177777		-1	
01065'177777	EOM3:	-1	
01066'177777		-1	
01067'177777		-1	
01070'177777		-1	
01071'177777		-1	
01072'177777		-1	
01073'000037	EOM4:	37	
01074'177777		-1	
01075'177777		-1	
01076'177777		-1	
01077'177777		-1	
01100'177777		-1	
01101'000037	EOM5:	37	
01102'000043		43	
01103'177777		-1	
01104'177777		-1	
01105'177777		-1	
01106'177777		-1	
01107'000000	D066A:	0	

Figure 8. Portion of File EQUIP.RB, Assembled Equipment Table (Continued)

```
0012 EQUIP
01110'000000 D0668: 0
01111'000000 D067A: 0
01112'000000 D067B: 0
.END
```

Figure 8. Portion of File EQUIP.RB, Assembled Equipment Table (Concluded)

INTERFACE ADAPTER	ASYNCHRONOUS LINE ADAPTERS			
	Port		Subport	
	Output	Input	Output	Input
	24	24	1	1
	24	24	2	2
	24	24	3	3
	24	24	4	4
	24	24	5	5
	24	24	6	6
	24	24	7	7
	24	24	8.	8.
	24	24	9.	9.
	24	24	10.	10.
	24	24	11.	11.
	24	24	12.	12.
	24	24	13.	13.
	24	24	14.	14.
	24	24	15.	15.
	24	24	16.	16.

Figure 9. ET Entries for DCM Devices for Lab System

INTERFACE ADAPTER	ASYNCHRONOUS LINE ADAPTERS				DIGITAL I/O			
					Inputs (ETDID)		Outputs (ETDOD)	
	Port		Subport		First Input (BSSSS)	Device (DDDDDD)	First Output (BSSSS)	Device (DDDDDD)
	Output	Input	Output	Input				
	41	40	0	0	0	73	0	62
	41	40	1	1	2	73	4	62
	41	40	2	2	4	73	8.	62
	41	40	3	3	6	73	12.	62
	41	40	4	4	8.	73	16.	62
	41	40	5	5	10.	73	20.	62
	41	40	6	6	12.	73	24.	62
	41	40	7	7	14.	73	28.	62
	43	42	0	0	16.	73	0	63
	43	42	1	1	18.	73	4	63
	43	42	2	2	20.	73	8.	63
	43	42	3	3	22.	73	12.	63
	43	42	4	4	24.	73	16.	63
	43	42	5	5	26.	73	20.	63
	43	42	6	6	28.	73	24.	63
	43	42	7	7	30.	73	28.	63
	45	44	0-7	0-7	0-14.	74	0-28.	64
	47	46	0-7	0-7	16.-30.	74	0-28.	65
	51	50	0-7	0-7	0-14.	75	0-28.	66
	53	52	0-7	0-7	16.-30.	75	0-28.	67
	55	54	0-7	0-7	0-14.	76	0-28.	70
	57	56	0-7	0-7	16.-30.	76	0-28.	71

Figure 10. ET Entries for Asynchronous Devices for 64-Line Field-Test System

INTERFACE ADAPTER	ASYNCHRONOUS LINE ADAPTERS				DIGITAL I/O			
					Inputs (ETDID)		Outputs (ETDOD)	
	Port		Subport		First Input (BSSSS)	Device (DDDDDD)	First Output (BSSSS)	Device (DDDDDD)
	Output	Input	Output	Input				
	43	42	1	1	0	71	0	66
	43	42	2	2	2	71	4	66
	43	42	3	3	4	71	8.	66
	43	42	4	4	6	71	12.	66
	43	42	5	5	8.	71	16.	66
	43	42	6	6	10.	71	20.	66
	43	42	7	7	12.	71	24.	66
	43	42	8	8	14.	71	28.	66
	45	44	1	1	16.	71	0	67
	45	44	2	2	18.	71	4	67
	45	44	3	3	20.	71	8.	67
	45	44	4	4	22.	71	12.	67
	45	44	5	5	24.	71	16.	67
	45	44	6	6	26.	71	20.	67
	45	44	7	7	28.	71	24.	67
	45	44	8	8	30.	71	28.	67

Figure 11. ET Entries for Asynchronous Devices for 16-Line Field-Test System

(177777 octal). The lists are pointed to by ETEOM in each ET entry. If no EOM checking is to be done, ETEOM must point to a location containing -1. Figure 6 presently contains duplicate lists (EOM1 and EOM4). The lists are longer than needed so that additional EOM character codes can be added octally if needed. The 30 words in lines 196-225 are equivalent to the following seven words (except that the order of list EOM5 is changed):

```

EOM5:    43
EOM1:
EOM4:    37
EOM3:    -1
EOM2:    12
          5
          30
          -1

```

11. One word of storage must be provided for each group of 16 contiguous digital outputs which are to be used in the test, as shown in lines 226-229 as D066A, D066B, D067A, and D067B. The words are pointed to by ETDOA in each ET entry which uses digital outputs. The storage must be initialized to zero.

A number of conventions were observed in generating the file in Figure 6. The Macro Processor was used to perform certain substitutions and the NOVA assembler pseudo-op .DUSR (see lines 49-74) was used to perform others. The Macro Processor performs its substitutions prior to the assembly. The differences can be seen between the file EQ and the EQUIP (symbolic) portion of the assembly listing. The macro TTY33 defined at lines 93-95 of Figure 6 changes TTY33 in line 97, for instance, to I2741. The pseudo-op .DUSR causes the substitution to be made internally by the assembler. Therefore, the symbolic portion

of the assembly listing gives the symbol and the assembled code shows the substituted value. For instance, on line 8 of page 1 of Figure 8, the name I is assigned the value  $111_8$ . On line 56 of the same page, the I is shown in the symbolic code and the 111 is the rightmost portion of the assembled value of  $13111_8$ .

The labels E3, E4, etc., (as well as E0, E1, and E2) for each ET entry are needed to provide values for the cross-reference fields CHILD, LINK, and PARNT. A better tactic than using the arbitrary labels, however, would be to use the device names for labels, to use TY2 as a label rather than E3 at line 100 of Figure 6. The field ETYPE should be used to group like devices and to distinguish unlike devices, for instance: TT for TTY's, TY for IBM 2741's, CT for the control TTY, DS for displays, LN for communications lines, CN for multiplexor device-controllers, PT for printers, etc. Several combinations should be used to distinguish displays with different characteristics, for instance.

The label E0END (line 88) is used to define the end of entry E0 and in defining ETEND (line 78). The label E9999 (line 192) is used to define the end of the last ET entry and in defining ETENT (line 194) and the length of ETREC (line 80). The symbol LEN (line 193) has the value of the length of an ET entry and is used in defining ETENT and ETLEN.

The equivalences for A through W at lines 49-54 are provided for use in giving values to the field STATI although only I and U should normally be used for initial values. The equivalences for W through O at lines 54-58 are for use in defining parity type (PARTY). The meanings are:

- W = one (parity bit set to a constant 1)
- E = even parity
- Z = zero (parity bit set to a constant 0)



N = no parity bit

O = odd parity

Only the values E and O are used by emulator programs.

The equivalences at lines 64-74 are used to define terminal type (TERMT). Those at lines 67-69 are of the earlier, arbitrary type which have not been updated.

The equivalences at lines 59-63 are used so that the fields ETRAT, BYTEL and PARTY in the ET entries may be given symbolic values rather than absolute values. Only the equivalence statement has to be changed to assign a new value rather than changing each ET entry.

#### FUNCTION

Each Equipment Table entry defines one equipment component of the SUT. In the simplest case, one ET entry is used to describe a point-to-point communications channel, possibly a pair of modems, and the single device attached to the channel. In a more complicated case, one entry describes the channel (and possibly modems), one is used to describe each controller or terminal (in a multipoint configuration), and one is used to describe each device at each terminal.

In the latter case, cross references (CHILD, LINK, and PARNT) are used to describe the hierarchical structure. As an example, the hierarchical ET structure described in Figures 6 through 8 is shown in Figure 12. Since each ET entry can reflect only one of each relationship, the arrows and labels indicate which relationship is expressed in the ET. Using this method of cross-referencing most configurations of equipment can be easily described. The number of levels and the number of entries at each level are limited only by core memory.

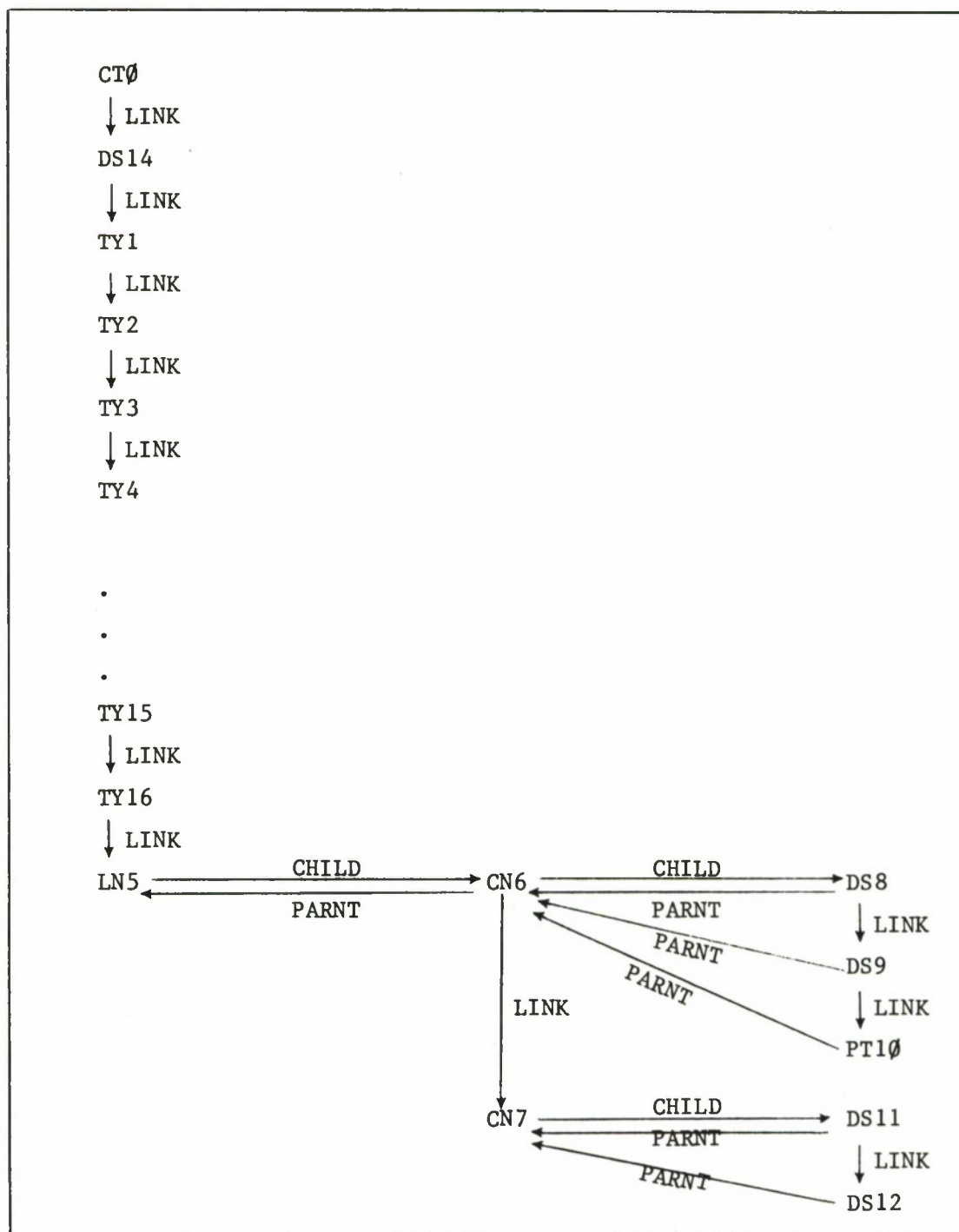


Figure 12. Equipment Table Hierarchy

Information in the Equipment Table is used by the Scenario Interpreter and by the Exec and is available to a scenario by means of certain scenario instruction types. The scenario may examine information, or in limited cases, change information in an ET entry. A scenario may access its own ET entry, or, through the relationships described above, access the ET entry of a relative, a relative's relative, and so on (in the direction of the arrows only). This capability of a scenario becomes increasingly useful as the equipment being emulated becomes increasingly complex.

The set of Registers of the current scenario associated with a particular device is pointed to by the first word (ETRØ) in the ET entry for that device. The first word of an ET entry is pointed to by the relationship pointers described above. Using instruction types h and then g and p (as defined in Volume 2 Table XVII), a scenario A running on device LN5 (as shown in Figure 12) can access the ET entry and Registers of scenario B running on device CN6, and then gain access to the ET entry and Registers of scenario C running on DS8, and so on. An example of this method of communication among devices is shown in the scenario segments in Figure 13.\* In this case LN5 running with scenario A establishes the linkage to DS8 running with scenario C. Scenario A checks Register 9 of scenario C to determine when DS8 is ready to send a query. When scenario A senses that R9 = 1, it then performs a specified function (function 1) and resets R9 to zero. This zero indicator is put into R9 of scenario C, which senses the indicator and proceeds to send the query. Meanwhile CN6 running with scenario B is engaged in performing function 2, which may or may not be involved in communication with LN 5 or DS8.

---

\* The scenario library SCENLIB, shown in Figure 35 in Appendix VI, establishes the macros used in this example.

Also, using instruction type h, and then instruction types Y or n, scenario A can examine the bit indicators (ETIND) of the ET entry of device CN6 and then DS8, etc. There are other scenario instructions which access the Equipment Table contents, and can be used in numerous ways to enhance scenario abilities and efficiency. A complete presentation of scenario instructions is given in Volume 2, Table XVII.

The technique of utilizing the Equipment Table to examine or pass information among devices can be useful, for example, when emulating a polled network. Assume, for instance, that CN6 was a controller and DS8 and DS9 were polled terminals. Then by making use of the cross references in the Equipment Table, the scenario for CN6 could poll the scenarios for DS8 and DS9 by examining indicator bytes or Registers to determine which devices were active, ready to send, or ready to receive. The individual terminal scenarios could send their queries and examine responses when indicated by the controller scenario.



LN5 SCENARIO A (SCA)	CN6 SCENARIO B (SCB)	DS8 SCENARIO C (SCC)
L CONT function 1 : LDR Ø R9 R9 SET TO ZERO PTR R9 9 R12 PUT CONTENTS OF CURRENT R9 INTO R9 OF SET OF REGISTERS POINTED TO BY R12 (DS8) :		

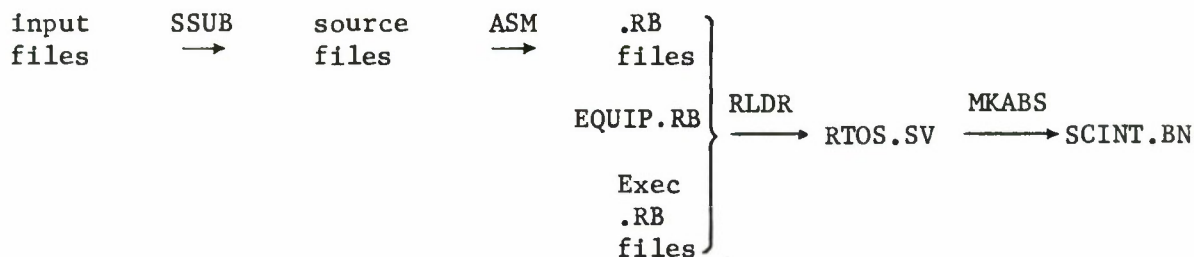
Figure 13. Example of Device Communication Through Scenarios (Concluded)

## SECTION VI

### REAL-TIME EMULATOR SYSTEM GENERATION

#### INTRODUCTION

The generation of the real-time emulator system is a four-step process which can be represented as follows:



The four steps are execution of the Macro Processor (SSUB), execution of the NOVA assembler (ASM), execution of the NOVA relocatable loader (RLDR), and execution of the DOS command MKABS. The first two steps must be performed separately for each assembly module which is to be changed (including the Equipment Table which is not considered a part of the Scenario Interpreter). The last two steps must be performed once each whenever one or more assembly modules (including those of the Exec) have been changed. In creating the Executive from the various source files, there is some flexibility available in defining buffer sizes, storage requirements, and parity checking on SUT terminals. These options are described in detail in Volume 6 of this series in the User Information Section.

#### SSUB

For purposes of this discussion the general form of the command to execute the Macro Processor is assumed to be:

SSUB      input-file      source-file      macro-libraries



The input-file names, source-file names, and the macro libraries needed for the Scenario Interpreter are given in Table VII. The implementation uses of the Macro Processor are also discussed in Section III.

To execute the Macro Processor, type on the control TTY;

```
SSUB II ININT RTOSLIB LIB LIB1,
```

or

```
SSUB EQ EQUIP,
```

where II and EQ are the input files; ININT and EQUIP are the output files; RTOSLIB, LIB and LIB1 are libraries; and, represents the carriage-return key. The macro libraries must be in the form of the output files produced by the macro library generator (MACDEF), the file LIB.ML, for instance. Unlike the last three steps, the output file (ININT or EQUIP, above) must be absent from the DOS file directory before executing SSUB.

If one of the three macro libraries must be changed, it must be read into the NOVA using LIB, for instance, as the input file name.

Typing

```
MACDEF LIB,
```

on the control TTY will execute the macro library generator which will generate the macro library LIB.ML.

#### ASM

An output file from the Macro Processor (Source File) must next be processed by the Data General assembler by typing, for instance:

```
ASM/L/X    $LPT/L    ININT,
```

The output file produced is a relocatable, binary file, ININT.RB in this case. Because the switches /L/X and the line printer \$LPT are specified, an assembly listing including the source file and cross reference list will be produced on the line printer.

TABLE VII  
Input File Names for Emulator System

<u>Input File Name</u>	<u>Source File Name</u>	<u>Macro Library Names</u>
EQ	EQUIP	--
SI	SCINT	RTOSLIB, LIB, LIB1
CI	CMINT	RTOSLIB, LIB, LIB1
II	ININT	RTOSLIB, LIB, LIB1
FC	FETCH	RTOSLIB, LIB, LIB1
TP	TESTP	RTOSLIB, LIB, LIB1
S1	SUBR1	RTOSLIB, LIB, LIB1
S2	SUBR2	RTOSLIB, LIB, LIB1
AF	ALF	RTOSLIB, LIB, LIB1
ERROR	ERMSG	RTOSLIB, LIB, LIB1
FTC	FTCHG	RTOSLIB, LIB, LIB1
DW	DUMPW	RTOSLIB, LIB, LIB1
DH	DUMPH	RTOSLIB, LIB, LIB1
IS	ISCEN	--

## RLDR

Table VIII lists the assembly modules needed by the Data General relocatable loader to generate the real-time software for each of the two versions of the emulator. The files used by RLDR are those with the .RB suffixes. A list of the module names (excluding the suffix) must be given to RLDR. These can be typed from the list in Table VIII, if desired; however, the system tapes for each of the emulator versions contain a file called LOADLIST which is a list of the file names needed for each version. To execute RLDR, type on the control TTY:

```
RLDR/Z      MAP/L      @LOADLIST@,
```

The output file produced by RLDR is in a form suitable for execution under control of DOS. Although the real-time emulator cannot be executed under DOS, the step is a necessary preliminary to producing the required file. The output file is named RTOS.SV since RTOS is the first file in the list in LOADLIST. Since MAP/L is specified the core map produced by RLDR will be placed in a DOS disk file called MAP. It can be listed by typing:

```
PRINT MAP,   or   PRINTL MAP,
```

The MAP file should be saved on tape with the other files for future reference. The file RTOS.SV should also be saved since octal patches, if needed, can be made to it, with the MAP file for guidance. The fourth step must then be performed with a new or patched RTOS.SV.

## MKABS

The DOS command MKABS produces a file which can be executed independently of DOS. The command is executed by typing:

```
MKABS/Z      RTOS      SCINT.BN      INIT/S,
```

The octal equivalent of INIT (obtained from the MAP file) is the

TABLE VIII

## Inputs to Relocatable Loader

	Assembly Module Name	Lab System	Field-Test System
Exec	*RTOS	X	X
	*RTIN	X	X
	LPT	X	X
	MTA	X	X
	TTY1	X	
	DCM	X	
	DCMT	X	
	ASYN		X
	SCMGT	X	X
	PAGE	X	X
	DSK	X	X
	DMP	X	X
ET	*EQUIP	X	X
Scenario Interpreter	SCINT	X	X
	CMINT	X	X
	ININT	X	X
	FETCH	X	X
	TESTP	X	X
	SUBR1	X	X
	SUBR2	X	X
	ALF	X	X
	ERMSG	X	X
	*FTCHG	X	X
	DUMPW	X	X
	DUMPH	X	X
	ISCEN	X	X

\* Different versions needed

value to be used in the command. MKABS uses RTOS.SV as the input file and produces SCINT.BN as the output file. SCINT.BN is the real-time emulator program, containing the Exec, the Equipment Table, and the Scenario Interpreter. It may be executed, by means of the DOS program EXEC, by typing:

```
EXEC SCINT,
```

A more convenient method of executing SCINT.BN, however, is discussed under Operating Instructions for the Scenario Interpreter.

#### Disk Requirements

After a system is generated, it is not necessary to maintain all the binary and source program files on disk. These files should be saved on tape, and disk space freed to allow space for additional macro libraries and scenarios. Table IX indicates the disk requirements of the files which should be retained on disk during emulator operation.

Table IX  
Disk Requirements for Emulator System

File	Size Bytes/Pages	Comments
DOS,etc	101221/210	Includes basic support software after @REMAL@ has been executed. Includes SYS.DR, MAP.DR, EDIT.SV,XFER.SV,SYS.LB,RLDR.SV,OEDIT.SV PRINTL.SV,REMAL,BLDR.SV,EXEC.SV,ASM.SV
MACDEF.SV	14976/30	Macro Processor. See MTR 2677 Volume 3.
SSUB.SV	20736/41	Macro Processor. See MTR 2677 Volume 3.
SCENLIB.ML	242/1	Lower-case scenario instruction op-codes. See MTR 2677, Volume 2, Table XIV and related text.
CVT.SV	31488/62	Scenario Assembler. See MTR 2677, Volume 4.
SUTTAB	384/1	Scenario Assembler. See MTR 2677, Volume 4.
DEVTAB	1792/4	Scenario Assembler. See MTR 2677, Volume 4.
RTOS.SV	32512/64	Real-Time Emulator. See MTR 2677, Volumes 5 and 6.
SCINT.BN	33514/66	Real-Time Emulator. See MTR 2677, Volumes 5 and 6.
P	30/1	Real-Time Emulator. See MTR 2677, Volumes 5 and 6.
C	3/1	Real-Time Emulator. See MTR 2677, Volumes 5 and 6.
LOADLIST*	130/1	Real-Time Emulator. See MTR 2677, Volumes 5 and 6.
DATAR.SV	29056/57	Data Reduction Program. See MTR 2677, Volume 7.
SUMRY.SV	27904/55	Data Reduction Program. See MTR 2677, Volume 7.
TLIST.SV	27264/54	Data Reduction Program. See MTR 2677, Volume 7.
CTABS	1664/4	Data Reduction Program. See MTR 2677, Volume 7.
ERFILE	420/1	Data Reduction Program. See MTR 2677, Volume 7.
TREL.SV	26240/52	Data Reduction Program. See MTR 2677, Volume 7.
MASTR.SV	17024/34	Data Reduction Program. See MTR 2677, Volume 7.
MAP	3752/8	Core map of RTOS.SV and, thus, of SCINT.BN
NOTES	1926/4	Text description of system. Should be updated when changes made in public or private copy.
FILECH.BN	4806/10	Verifies file validity on disk. See Reference 3.
MTLIST.BN	3606/8	Physical tape dump for MT1. See Reference 4.
Total	380690/758	

\* When used, also need EQUIP.RB and .RB files for Scenario Interpreter and Exec.



## SECTION VII

### REAL-TIME EMULATOR

#### INTRODUCTION

The Scenario Interpreter is the real-time, emulator application program which operates in conjunction with the Real-Time Exec, a multi-tasking, application-oriented executive program. The Scenario Interpreter executes commands used to exert gross control over the run, executes scenarios which describe the actions to be taken in emulating terminal and operator functions, and records real-time events on a log tape. The Scenario Interpreter and the Real-Time Exec perform all the functions of the real-time emulator run.

#### SYSTEM FLOW

As shown in Figure 14 the real-time emulator system as well as the internal scenarios to be used must reside on disk before a run can be initiated. The Scenario Interpreter program (running under the Real-Time Executive) is then started by input from the control teletype. Once the emulation has begun, the teletype may be used for both output messages and input commands for the run. The events of the emulation are recorded on the log tape during the run, and this tape is used at the completion of the run for analytical purposes. If any dumps of the emulator system are requested during the real-time run, they will be printed on the line printer during the run.

#### OPERATING INSTRUCTIONS

External control over a real-time emulator run is exerted primarily through the control TTY. The run is started under DOS conventions. Once started, emulator conventions apply. In existing Equipment Tables, the control TTY is defined as device CTO. Device CTO is made to look



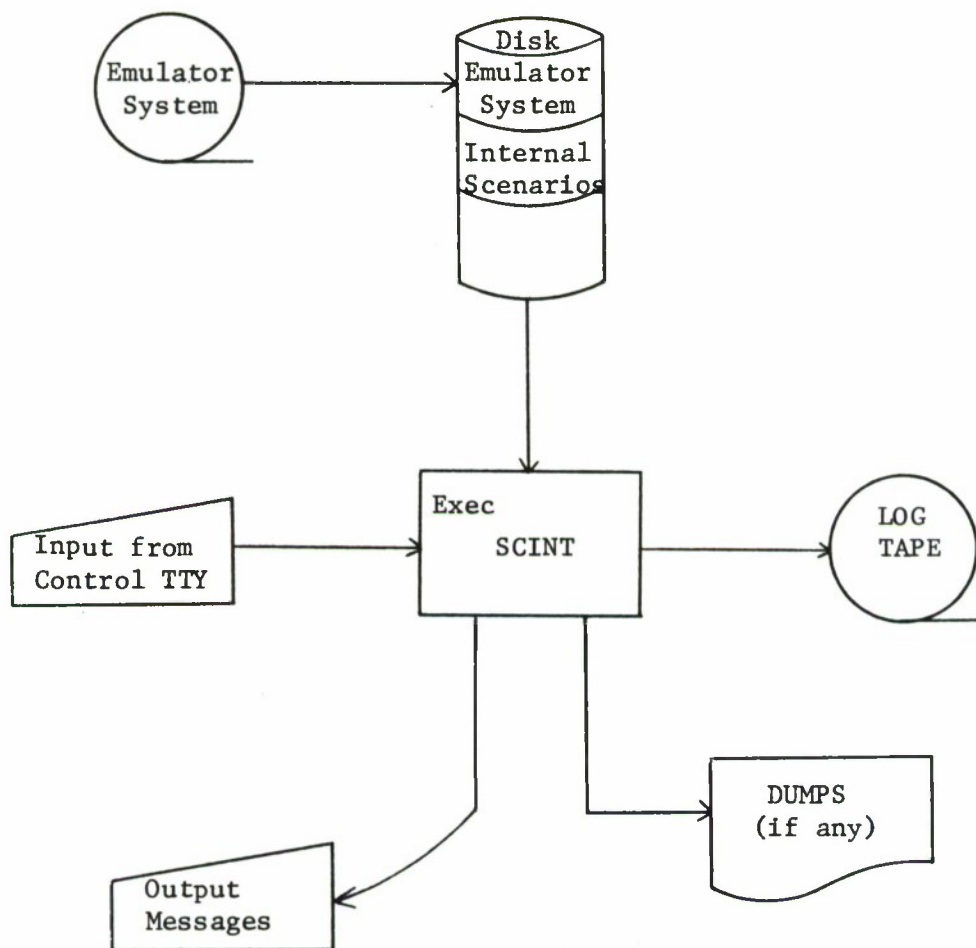


Figure 14. System Flow for Real-Time Emulator

as much like other (emulated) devices as possible. CTO can be used as an emulated device if desired although responses must be supplied by the user, of course. Unlike other devices, CTO is operated in echo-plex mode so that keystrokes will cause printing on the TTY. Unlike DOS, the Exec does not echo back a carriage-return and a line feed when the carriage return key is depressed. Therefore, the symbol `↵` is used to denote depression of the carriage-return key and echo back of both carriage-return and line-feed under DOS. Under Exec control, both keys must be depressed and they are represented below by CR/LF. It is assumed that the list of EOM characters pointed to by ETEOM in the ET entry for CTO includes LF (12<sub>8</sub>), CANCEL (30<sub>8</sub>), and BREAK (5).

#### Startup

If not already mounted, a scratch tape is needed on tape drive 0. The real-time run is most conveniently started by typing on the control TTY:

@P@  
`↵`

This input causes DOS to treat the file P as a list of DOS commands.

The file P contains:

```
RELEASE MTO;  
TYPE C;  
EXEC SCINT;
```

This set of commands causes the contents of file C (containing WAIT`↵`) to be typed on the control TTY. The log tape is then rewound if it was left other than at the load point by a previous real-time run or by an aborted Data Reduction run. While the tape is rewinding, the real-time emulator program (SCINT.BN) is called and initialization is begun. All further control TTY inputs must follow Exec conventions.

## Control TTY Inputs

### Run ID

After the word 'WAIT' is typed on CTO, the user must wait for the message 'ENTER RUN ID' to be typed on CTO before taking any further action. The user must then enter a character string, terminated by CR/LF, which will be written on the log tape as the run identification. The run identification consists of all characters typed up to but not including the first control character (those with octal values less than 40) or the first 20<sub>10</sub> non-control characters. If an error is made in entering the run ID, simultaneous depression of the control and X keys (Control-X) will cancel the input and the user can start again. Almost immediately after entry of the run ID, the emulator will write the run ID and the other two history records on the log tape in one burst and then type "READY" on CTO. The emulator is now ready to accept commands so as to start emulation.

### Commands

The emulator will remain in the idle state until a command is entered from CTO or from another emulator module or until an (unsolicited) response is received from the SUT, from CTO, or from another emulator module. Even then, the emulator will return to the idle state until one or more START commands are executed by the emulator. Commands are described in Volume 2 of this series. Commands from CTO (or another emulator module) must be preceded by an ASCII left-bracket character (Control-K). With a single START command, the user can execute a control scenario, if he desires, which can automatically START other devices and execute other commands (by means of the type-C scenario instruction) and any of the scenario instructions defined in Volume 2.

### CANCEL Input

Any CTO input can be cancelled by depressing Control-X. The input will not be logged, and CR/LF will be typed as an acknowledgement.

### BREAK Output

If the first (or any odd) character of a CTO input message is a BREAK character (input by depressing Control-E), the input is considered a BREAK input whose purpose is to BREAK or stop output on CTO of error messages (see ERROR command) and the monitor output of queries and responses (see MONITOR command). Error messages, queries, and responses already queued for typing, will be typed, but no more will be queued until another ERROR or MONITOR command causes them to be queued again.

### Responses

A CTO input not in any of the above classes is considered a response. If no scenario is operating for CTO, they will be treated as unsolicited. If a scenario is operating and is waiting for a solicited response, the response will be processed immediately. Otherwise, the response will be queued until the scenario requests it or until the scenario terminates.

### Shutdown

The real-time run is terminated by execution of a QUIT command from CTO, another emulator module, or a scenario. If the run does not terminate immediately, the emulator is so busy that the QUIT command (which is purposely given the lowest possible priority) is never executed because of a continuing string of higher priority tasks. One or more devices must be STOPped for the QUIT command to be executed. When the QUIT command is executed, two lines of emulator statistics are typed on CTO and the NOVA halts. By depressing Continue on the panel, DOS will be brought back in core and executed. DOS will type 'DOS REV XX.' and it will halt. Depressing Continue again will cause 'R' to be typed, and DOS is again in control. If desired, the Data Reduction program can be executed for the run just completed or any DOS function can be performed.

## ERROR MESSAGES

The high-order digit of the printed error message number has been used to classify the error messages generated by the Scenario Interpreter as to seriousness. The most serious errors correspond to the highest digit. The ten error message classes are given in Table X. General comments are also included as to the kinds of errors associated with each class and the system action following detection of the error.

Table XI lists and explains all the error messages generated by the Scenario Interpreter. Each three-digit number shown is a part of the message. The message itself represents the only use in the Scenario Interpreter of the three-digit numbers. Elsewhere, error messages are referenced only by the two low-order digits, and the table is in order based on these digits. The convention (6)40 has been used to indicate the internal and external message numbers. The table gives the meaning and cause of each error message as well as the subroutines and modules which generate the message.

Table X

Error Message Classes for Scenario Interpreter

<u>Class</u>	<u>Meaning</u>
9	Not used. Reserved for severe errors which would abort real-time run.
8	System errors. Bring to attention of system programmer. Action terminated for device and device made inactive. (Same as if end of top-level scenario reached).
7	Relatively serious problem. May be system error or user error. Action terminated as for class 8.
6	Relatively serious user error, probably in a scenario. Action terminated as for class 8 unless able to proceed.
5	Error encountered in attempt to free a block of allocable core memory. Probably a system error although improper use of a type-F scenario instruction or previous improper action with Registers could cause it. System attempts to continue with emulation of device.
4	User error. Improper use of a command. Command not executed. Action continues as for class 5.
3	Unable to execute command. May be a problem of synchronization between devices. Action continues as for class 5.
2	Unable to execute command. Erroneous command operator or operand. Action continues as for class 5.
1	Usually an indication of an action taken although an error may be present also.
0	Not an error. Indication of action taken.



Table XI

## Error Messages for Scenario Interpreter

<u>Message</u>	<u>Meaning</u>
800 STACK OVERFLOW	System error. Attempt to PUSH a value into stack portion of RS when stack full. (Subroutine POSH0, POSH1, POSH2, or POSH3).
801 STACK UNDERFLOW	System error. Attempt to POP a value from stack portion of RS when stack empty (Subroutine PUP0, PUP1, PUP2, or PUP3).
502 NO RS TO FREE	System error. Attempt to free RS when STACK=0. (Subroutine FRRS).
503 ILLEGAL FREE ADDRESS	Probably a system error. Attempt to free RS or buffer whose address not in allocable core. (Subroutine FRRS or FRBF).
504 NO BUFFER TO FREE	Probably a system error. Attempt to free a non-existent buffer, i.e., pointer = 0 (Subroutine FRBF).
406 TOO FEW REGS FOR SUBSCENARIO CALL	Register RGCAL not allocated in current set so that execution of a SUB command is ruled to be invalid. (Subroutines CMINT or ALRG).
507 NO REGS TO FREE	The set of Registers pointed to may have been freed previously or the contents of the Register may have been altered erroneously by a scenario. Otherwise, a system error. (Subroutine FRRG).
210 COMMAND NOT IMPLEMENTED	Specified command (MOD or TRANSFER) has not been implemented. (Subroutine CMINT).
211 INCORRECT COMMAND OPERATOR	Erroneous command operator. (Subroutine CMINT).
312 EQUIPMENT UNAVAILABLE	Attempt to START a device whose status is other than 'I' or 'S'. (Subroutine CMINT).



Table XI (Continued)

## Error Messages for Scenario Interpreter

<u>Message</u>	<u>Meaning</u>
613 OUT-OF-RANGE REG #	Attempt to access Register not allocated in current set (module FETCH) or in another set (module ININT - type g or p scenario instruction).
114 DEVICE STOPPED	End of top-level scenario reached by normal operation or simulated due to serious error. (Module FETCH).
215 VALUE NEEDED FOR COMMAND	Numeric (decimal) value missing from SCALE command or numeric portion of equipment name missing from MONITOR, RESTART, START, STATUS, or STOP command. (Subroutine CMINT or FNENT).
216 UNKNOWN DEVICE NAME IN COMMAND	Unable to find equipment name specified in MONITOR, RESTART, START, STATUS, or STOP command in Equipment Table. (Subroutine FNENT).
217 INCORRECT SCENARIO NAME	Unable to find scenario name specified in START or SUB command in Scenario Directory (Subroutine CMINT).
020 ACTION TAKEN	Indicates successful execution of DUMP, ERROR, MONITOR, RESTART, SCALE, START, STOP, or SUB command (Subroutine CMINT).
121 SUB COMMAND LEGAL ONLY FROM SCENARIO	No rational way to execute a SUB command from one device for another since they operate asynchronously (Subroutine CMINT).
422 INVALID SUB- SCENARIO COMMAND REFERENCE	Attempt to execute a SUB command with no scenario specified when no uncompleted subscenario exists for device (RGCAL = $\emptyset$ ) or when Register RGCAL does not point to a valid set of Registers (C(RGR $\emptyset$ ) $\neq$ RGR $\emptyset$ ). (Subroutine CMINT).
223 ONLY "ON" OR "OFF" LEGAL	First operand of LOG command specifies 'ALL' and second operand specifies neither 'ON' nor 'OFF' (Subroutine CMINT).

Table XI (Continued)

## Error Messages for Scenario Interpreter

<u>Message</u>	<u>Meaning</u>
224 ONLY "A", "N", OR "U" LEGAL	First operand of LOG command specifies 'THIS' OR equipment name and second operand specifies none of 'A', 'N', or 'U'. (Subroutine CMINT).
125 LOG ACTION COMPLETE	LOG command has processed as much as it can of the third operand. Each component of this operand is processed separately and program has reached illegal component or end of command. Rather than attempting in an iterative program to separate the cases of missing third operand, error in nth component but first n-1 of them were processed, or all components were correct, a combination message is used which is intended to cause the user to verify that there was no error in the third operand. Note that for this type of SUB command, the SUBSCENARIO form is invalid and no character (such as a blank) may follow 'SUB' in the command instruction or the program will assume a scenario is specified.
826 STATI INCORRECT	System error. Instruction Interpreter attempt- ing to emulate device whose status (STATI) is neither 'A' nor 'T'. (Module FETCH.)
327 DEVICE INACTIVE OR STOPPED	Attempt to STOP a device whose status (STATI) is 'I', 'T', 'S', or 'U'. (Subroutine CMINT.)
330 DEVICE NOT STOPPED	Attempt to RESTART a device whose STATUS (STATI) is neither 'T' nor 'S'. (Subroutine CMINT.)
631 QUERY BUFFER OVERFILL	Attempt to fill query buffer beyond end by scenario instruction of type 5, \, or @. Note that if an error message intervenes after generation of query buffer, but before filling it, the error message buffer will displace the query buffer and the error message buffer will then be filled by the instruction. (Module ININT.)

Table XI (Continued)

## Error Messages for Scenario Interpreter

<u>Message</u>	<u>Meaning</u>
732 NO QUERY BUFFER TO FILL	This message will only appear if there is no query buffer (or error message buffer) associated with the device and a scenario instruction of type 5, \, or @ is executed. This condition will only occur prior to generation of the first buffer or following execution of a type-E scenario instruction and before generation of next query buffer or of next error message buffer which is not the result of a type-E instruction.
333 DEVICE STOPPED BY TYPE-7 INSTR	A RESTART command is not legal for the device since it was STOPped by a type-7 scenario instruction rather than by a STOP command so that there is no current task which can be RESTARTed. See Miscellaneous Notes section. (Subroutine CMINT).
634 OTHER REG SET DOES NOT EXIST	Attempt to execute a scenario instruction of type g or p when the other set of Registers does not exist (pointer = $\emptyset$ ). (Module ININT).
035 TTY OUTPUT SUPPRESSED	A BREAK input was recognized and executed. (Module SCINT.)
336 ASSEMBLY ERROR IN SCEN	First byte of internal scenario is non-zero. Scenario needs to be reassembled after correction of errors before it will be acceptable for use with START or SUB command. (Subroutine CMINT).
337 EQUIPMENT TYPE MISMATCH	Scenario may not be used with specified device (START command) or with current device (SUB command) because scenario is not a universal scenario and the second byte of the internal scenario fails to match TERMT in the ET entry for the device. (Subroutine CMINT).

Table XI (Concluded)

Error Messages for Scenario Interpreter

<u>Message</u>	<u>Meaning</u>
640 BEHIND SCHEDULE	A type-W scenario instruction was executed after the specified time had passed. The amount of time by which the task is behind schedule, in milliseconds, is contained in the start transmission time fields of the buffer. Processing continues for device. (Module ININT.)
641 WAIT INSTR IGNORED	Type-W scenario instruction may not specify a time in excess of approximately 4.62 hours because of conversion problems. Instruction ignored and processing continues for the device. (Module ININT.)

## DEVICE STATUS

Figure 15 shows all the valid state (STATI) transitions which can occur for a device. These transitions occur as the following functions are performed:

- I→A occurs when a START command is successfully executed for the device.
- A→I occurs when the end of the top-level scenario (RGRET = 0 for the current set of Registers) is reached for the device.
- A→T occurs when a STOP command is successfully executed for the device.
- A→W occurs when a time delay type of scenario instruction (type D, W, or d) is executed.
- A→S occurs for the current device when a type-7 scenario instruction transfers control of the task to another device.
- W→A occurs upon the expiration of a time delay caused by execution of a scenario instruction of type D, W, or d.
- W→T occurs when a device is STOPped while executing a scenario instruction of type D, W, or d.
- S→A occurs when a STOPped device is STARTed or RESTARTed after the transition from T to S has taken place or for the new device during execution of a type-7 scenario instruction.
- T→S occurs for a STOPped device after completion of execution of the current scenario instruction or upon receipt of a response following execution of a scenario instruction of type R or I
- T→A occurs when a RESTART command is executed for a STOPped device before the T to S transition has taken place.

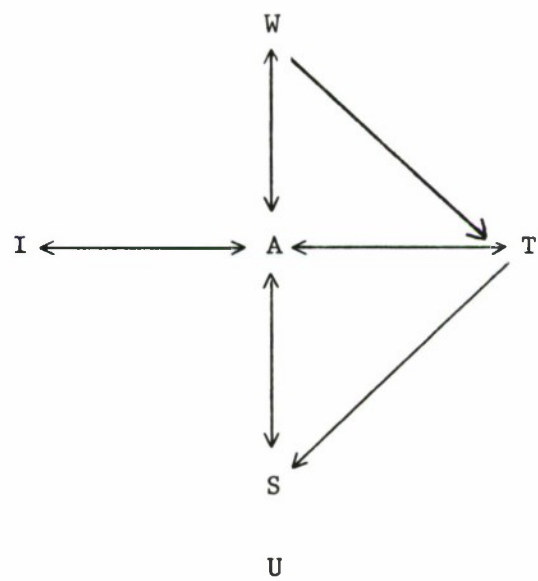


Figure 15. State Transition Diagram



U        device is unavailable and status cannot be changed by the emulator (can only be changed in non-real-time by reassembly of the ET or with the octal editor).

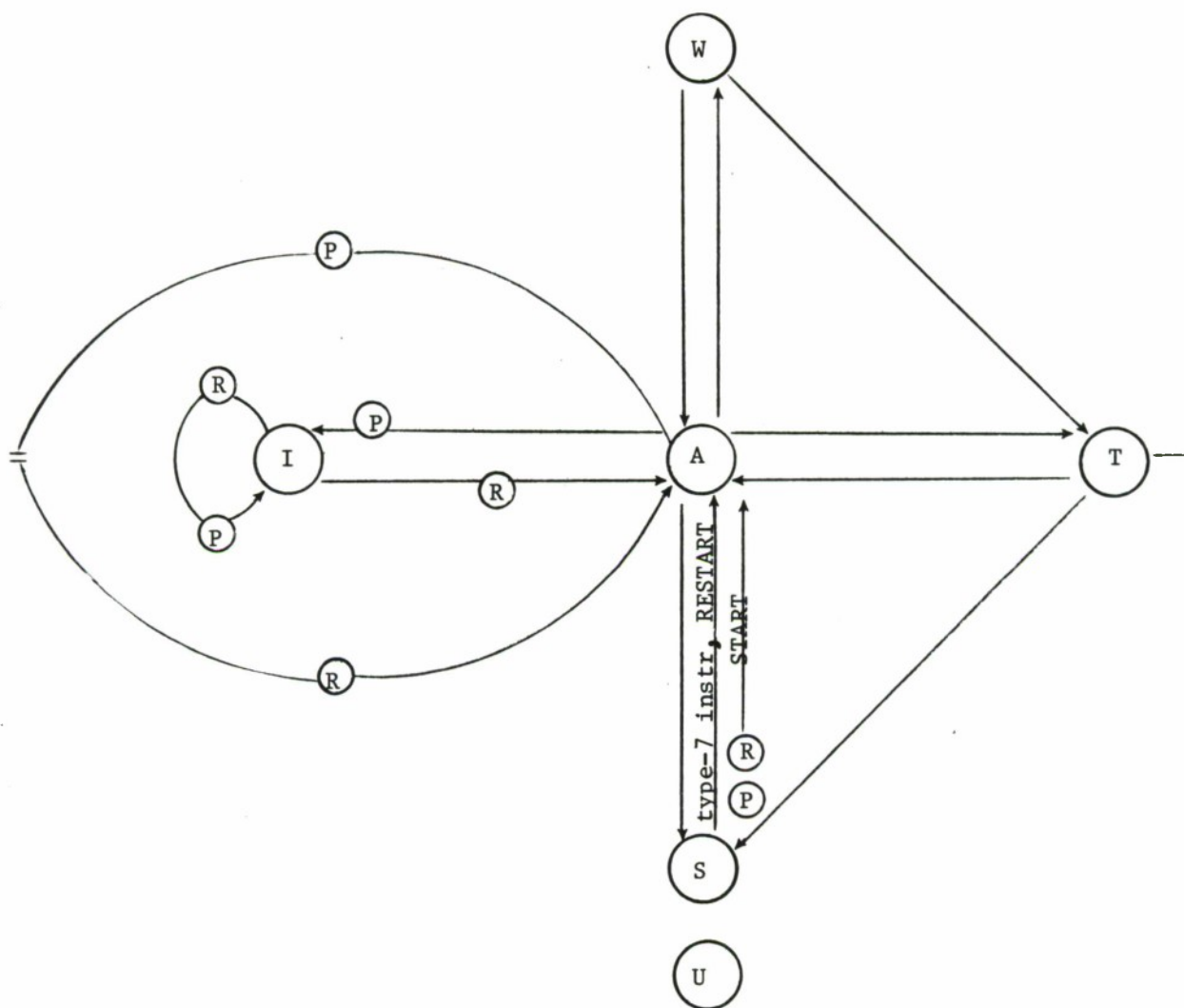
#### RING COUNTERS

There is a pair of ring counters in each ET entry (for each emulated device). They are used to sequence number tasks of types 6 (unsolicited responses), 7 (solicited responses), and 8a (newly STARTed devices), so that only one task of these types at a time (per device) can proceed past a certain point in the Input Processor (types 6 and 7) or the Instruction Interpreter (type 8a) so as to preserve reentrancy. The ring counter RRING (the response ring counter) is used to count and sequence number such tasks. The subroutine CHEKR is used to maintain RRING. CHEKR fetches RRING and uses it to sequence number the task (by setting RSEQU), steps RRING, and stores the updated value. CHEKR then compares RSEQU with PRING (the processing ring counter). If they are equal, the task is allowed to proceed. Otherwise, the task remains in CHEKR until PRING equals RSEQU. Thus, a queue of such tasks is maintained for each device, when necessary, and the tasks are released one at a time in the order in which they reached CHEKR.

The processing ring counter (PRING) is maintained by the subroutine STEPR. STEPR is called when task types 6 (unsolicited responses), 7b (type R or I scenario instruction executed), or 7c (end top-level scenario) terminate and when certain tasks of type 8a are generated (when a STOPped device is STARTed, the STOPped task must first be terminated). The only function performed by STEPR is to step PRING so that the next sequence numbered task may proceed.

These steps are shown in Figure 16 which is a modification of the state diagram in Figure 15. In Figure 16, when a device is STARTed, its status (STATI) changes from I to A. RRING is also





- Ⓡ RRING used and stepped by CHEKR
- Ⓟ PRING stepped by STEPR

Figure 16. Ring Counter Changes

stepped and the new task may be queued. When the end of a top-level scenario is reached for a device, its status changes from A to I and PRING is stepped.

If a STOPped device is STARTed (not RESTARTed), its status changes from S to A. When the STOPped task is terminated, PRING is stepped for the old task. RRING is then stepped for the new task (which may be queued.)

The loop around the I status indicates no change in status but the fact that if the device is inactive, receipt of an unsolicited response first causes RRING to be stepped and then PRING. Unsolicited responses are queued since a change in device status while the response is queued may cause a change in the type of response. The final determination as to the type of response is made when the response leaves the queue.

Similarly the loop around the A status indicates no change in status but the execution of a scenario instruction of type R or I which causes PRING to be stepped followed by a new task which steps RRING. Had one or more responses already been queued for the device, the stepping of PRING would allow the first of these to advance.

The discussion also indicates possible problems regarding use of the type-7 scenario instruction. For a type-7 instruction to be valid, the device to which control of the task is transferred must be STOPped. Thus, for this new device there already exists a suspended Scenario Interpreter task. If a task which has been generated for one device is allowed to terminate for a second device, PRING will not get stepped at the end of the task for the old device but for the new device. Thus, since the ring counters provide for 256<sub>10</sub> sequence numbers, the old device would have to accumulate a total of 255 queued responses (which would tie up 255 Exec clock blocks) before any further activity could occur for the old device. The new

device should be able to resume activity when a new task is generated for it, but the original STOPped task would be destroyed without its allocable core being freed when the task which executed the type-7 instruction terminated. The first problem is the more serious one, of course, but the latter ties up system resources for the duration of the run. Therefore, a task which is started for one device should be terminated for the same device to avoid these problems.

#### RESPONSE HANDLING AND LOGGING

The determination of whether logging is enabled or not for a particular device and a particular buffer type is made at the time the buffer is allocated. Changing the setting of the logging indicators, with the LOG command, has no affect on logging of buffers which have already been allocated. In the present implementation, if logging is enabled in a given case, a long buffer (one with a long header) is allocated and all long buffers are logged. For all long buffers, the log processing bit in BFINd is set at time of allocation. For either long or short buffers, one of the other five processing bits is set (based on buffer type) at time of allocation. When a task is done with a buffer or when it needs the buffer pointer space in the RS for a new buffer to be allocated, it resets the appropriate processing bit and attempts to free the buffer. If all six processing bits are reset, the free attempt is successful.

Unlike the other four types of buffers, response buffers are not automatically logged in all cases. Every long response buffer must be logged by one means or another or it will not be freed and the space will not be available for reallocation during the rest of the run. A separate response queue is maintained for each emulated device so that only one main task can be active at a time to process a single response. When a response and its associated task leave the queue, the determination is made as to whether the response is

solicited (or unsolicited) depending essentially on whether the device is active (or inactive). If the device is inactive when the response leaves the queue, the response will be logged automatically as unsolicited, if logging is enabled, and the task is terminated.

If the device is active at the time the response leaves the queue, the response will be logged automatically as solicited if Response Indicator 2 in ETIND is set and logging is enabled. The indicator must be set by executing a scenario instruction of the form = 2 prior to the time the response leaves the queue. A long response buffer can also be logged by executing a type-8 scenario instruction, which specifies whether the response is solicited or unsolicited. The use of both techniques will cause the buffer to be logged two or more times, once automatically and once for each type-8 instruction executed. Since there is no apparent advantage in logging a response more than once and the solicited response indicator (bit 0 in BFIND) is initially reset, execution of a type-8 instruction to log a response as unsolicited does not reset the solicited response indicator. Therefore, once the indicator has been set by either means, any further type-8 instructions will cause logging as solicited regardless of the value of the first operand.

When a device is active, all responses received will be queued until one is requested by the scenario by means of executing a scenario instruction of type R or I. When such an instruction is executed, the main task for the device is terminated at the end of execution of that instruction. Further execution of the scenario is done by the task associated with the next queued response which starts execution with the scenario instruction following the R or I instruction. If any responses are queued for a device when the end of the top-level scenario is reached or when a STOPped device is STARTed (not RESTARTed), the responses will be logged automatically as unsolicited. In addition, when either event occurs, all indicators in ETIND are reset except



for the Command Indicator and the Monitor Indicator. Therefore, if responses are to be logged automatically as solicited, each scenario STARTed (not RESTARTed or executed by a SUB command) must set Response Indicator 2.

## DIGITAL I/O

Digital I/O devices are installed on the field-test system but not on the lab system. With the field-test system connected directly to a SUT (without use of modems), the emulator must emulate the actions of modems as well as devices and operators. For each device, the SUT must believe it is communicating with the modem at its end of a communications channel. To provide more direct and complete control over the modem control lines (those not used for data transfer) than that provided by most line adapters, the emulator uses digital input devices to read the control signals set and reset by the SUT and digital output devices to set and reset the control signals read by the SUT.

The field-test system to be discussed is that containing 16 asynchronous communications channels and 8 synchronous channels. The discussion is largely concerned with emulation of asynchronous devices, with comments as to the extensions for synchronous devices.

The digital I/O design was done by Data General. The intended software design had to be modified to interface with the hardware as delivered.

A digital output device contains the capability of setting  $32_{10}$  digital outputs. Since a single NOVA instruction can set only  $16_{10}$  outputs, the outputs associated with one device address are separated into A and B groups. Since the outputs must be continuous rather than momentary, a register is associated with each of the two groups of an output device. Thus a NOVA digital output instruction loads either the A or the B register and the SUT reads (senses) the bits in those

registers. Loading a register corresponds to the simultaneous setting of some outputs to 1 and resetting of others to 0. Since Data General provided no means of reading an output register, the emulator software has to maintain a record of the status of each set of 16 outputs, in the word pointed to by ETDOA. (Each such word contains the current settings of outputs associated with 2 to 16 emulated devices, as should be clear later.) When one or more digital outputs must be set or reset for an emulated device, the software has to fetch the word pointed to by ETDOA and either reset the appropriate bits by masking or set them by ORing. The updated word then has to be stored back in memory and loaded into the appropriate register.

The system contains four digital output devices with (octal) addresses of 64, 65, 66, and 67. The outputs for a single digital output device are numbered from 0 to 31 decimal (0 to 15 in the A register, 16 to 31 in the B register). The system contains 128 digital outputs. Devices 64 and 65 are reserved for synchronous emulation, and 66 and 67 are used for asynchronous emulation.

The digital input hardware is similar to that for digital output but simpler. A digital input device allows reading (sensing)  $32_{10}$  inputs. The inputs are grouped in A and B groups although a group is simply a group of lines in the emulator hardware since, in this case, the inputs read are in registers in the SUT. When one or more digital inputs must be read and tested for an emulated device, the appropriate digital input device and group (containing inputs associated with 2 to 16 emulated devices) must be read, and the appropriate inputs tested.

The system contains two digital input devices with (octal) addresses of 70 and 71. The inputs for a single digital input device are numbered from 0 to 31 decimal (0 to 15 in the A group, 16 to 31 in the B group). The system contains 64 digital inputs. Device 70 is reserved for synchronous emulation, and 71 is used for asynchronous emulation.

Figure 17 shows the types of connections between the NOVA rack and the SUT, by way of the interface rack. On the left are the connection points, and on the right is shown the type of path connecting each pair of adjacent points. The jumpers between the A and B barrier strips are intended to be the primary means of changing configurations. For asynchronous devices, there are 16 A barrier strips and 16 B strips, one of each per device. Up to 10 separate connections can be made from an A barrier strip to 10 or less of the 24 connection points on a B barrier strip.

The relationships within the interface rack should be clarified by Figure 18. A single cable carries all 32 inputs or outputs (both A and B groups) of a single digital I/O device between the NOVA rack and the interface rack. A single section of the interface rack accommodates 16 emulated asynchronous devices. The normal wiring needed for emulating Bell 103A modems is shown in the figure. Only the digital I/O wiring is shown. For each emulated device, two digital inputs and four digital outputs are shown although only one of the inputs is used. Digital input device 71 is adequate for the needs of all 16 emulated devices. Digital output devices 66 and 67 are needed to provide four outputs per emulated device. In the diagram, the outputs are labeled from 0 through 3 and the inputs from 0 through 1. These are the addresses to be used by scenarios.

The purpose of the ETDID and ETDOD fields in an ET entry is to describe the relationship between the fixed digital I/O addresses used by scenarios (the same for all emulated devices) and the hardware addresses which are different for each emulated device. ETDID and ETDOD as well as the four types of digital I/O scenario instructions allow up to eight digital inputs and eight digital outputs to be associated with each emulated device. Since only one NOVA instruction is used to read digital inputs or to set and reset digital outputs and to conserve space in the ET, all the inputs (or outputs) for an



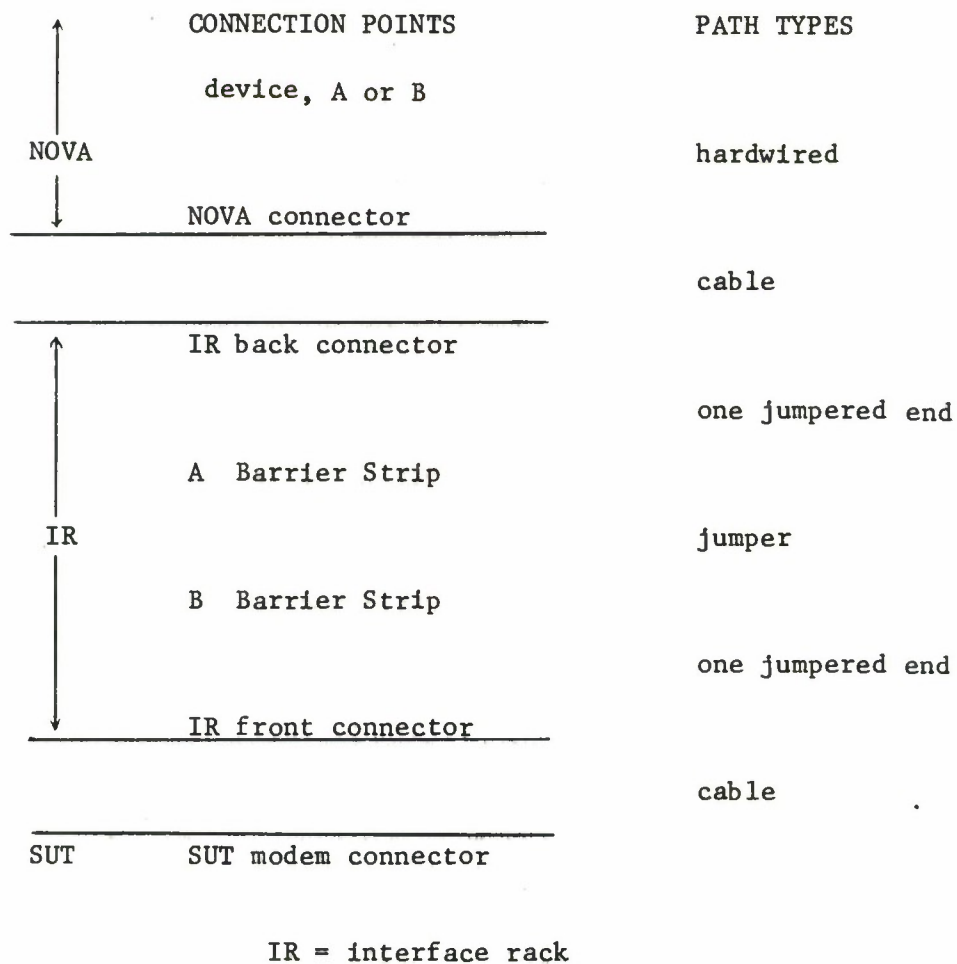


Figure 17. Digital I/O Connections

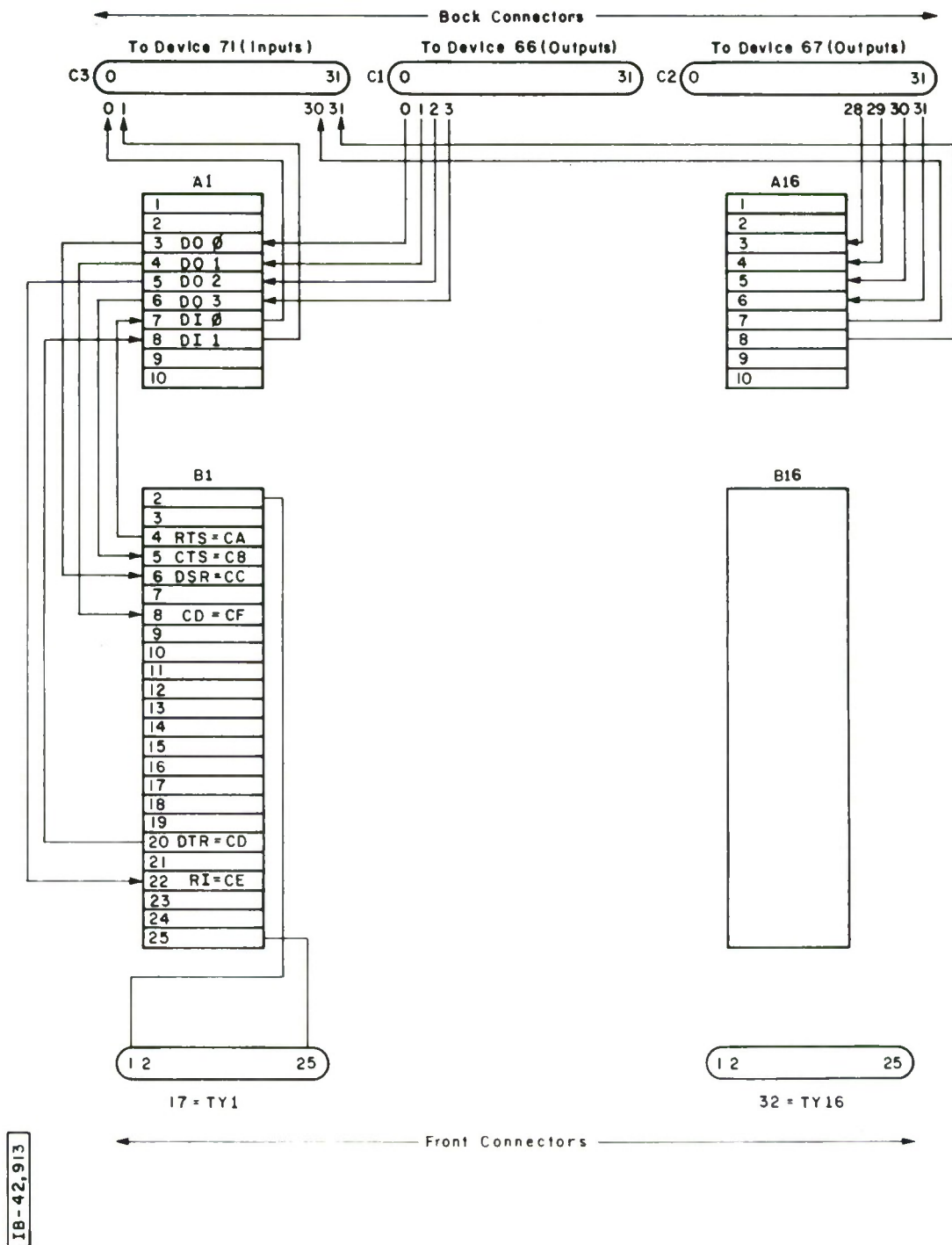


Figure 18 NORMAL INTERFACE RACK WIRING FOR ASYNCHRONOUS DEVICES

emulated device must have the same digital I/O device address, be in the same group (A or B), and be adjacent to one another.

ETDID and ETDOD have the same format (CCCBSSSSOODDDDDD in binary) and specify the digital I/O device address (DDDDDD), the number of the left-most input or output (BSSSS, where the value of the high-order (B) bit separates the A group from the B group), and the number of consecutive inputs or outputs minus one (CCC). If ETDID (or ETDOD) is zero, there are no inputs (or outputs) associated with the emulated device. From Figure 18 it can be seen that:

for device TY1:

ETDID: CCC = 1, BSSSS = 0, DDDDDD = 71

ETDOD: CCC = 3, BSSSS = 0, DDDDDD = 66

for device TY2:

ETDID: CCC = 1, BSSSS = 2, DDDDDD = 71

ETDOD: CCC = 3, BSSSS = 4, DDDDDD = 66

for device TY16:

ETDID: CCC = 1, BSSSS = 30., DDDDDD = 71

ETDOD: CCC = 3, BSSSS = 28., DDDDDD = 67

where a decimal point following a number indicates a decimal number, otherwise octal.

In Figure 18, the six digital input and output connections on an A barrier strip are connected to six points on a B barrier strip which in turn are connected to six pins on a front connector which is cabled to the SUT. These correspondences are shown in Figure 19. The codes are standard pin or signal codes. Figure 20 contains synonyms for the five scenario instruction op-codes used for digital I/O as well as correspondences between the digital I/O addresses used by a scenario and the two-letter signal codes. These equivalences can be made by use of the Macro Processor.

<u>Scenario I/O Address</u>	<u>Pin Number</u>	<u>Code</u>	<u>Function</u>
DO-0	6	CC	Data Set Ready (DSR)
DO-1	8	CF	Carrier Detect (CD)
DO-2	22	CE	Ring Indicator (RI)
DO-3	5	CB	Clear to Send (CTS)
DI-0	4	CA	Request to Send (RTS)
DI-1	20	CD	Data Terminal Ready (DTR)

Figure 19. Normal Asynchronous Correspondence

DON = ;  
DOF = :  
BDN = 9  
BDF = q  
ADY = d  
CC = 0  
CF = 1  
CE = 2  
CB = 3  
CA = 0  
CD = 1

Figure 20. Macro Definitions  
for Digital I/O

DON CE  
L CDLOOP  
BDN CDON CD  
ADY 250  
J CDLOOP  
L CDON  
ADY 500  
DON CC  
DOF CE  
ADY 4500  
DON CB CF

Figure 21. HANDSHAKE Scenario

Figure 21 contains the HANDSHAKE scenario which causes the emulator to exchange the modem control signals necessary prior to data transmission. The scenario first turns on (sets) the Ring Indicator (CE). At the label CDLOOP, a branch is taken to the label CDON if Data Terminal Ready (CD) is on. Otherwise, a 250-ms delay is taken followed by a branch to CDLOOP to test CD again. When CD has been turned on by the SUT (at CDON), a 500-ms delay is taken, Data Set Ready (CC) is turned on, and Ring Indicator is turned back off. A  $4\frac{1}{2}$ -second delay is then taken and Clear to Send (CB) and Carrier Detect (CF) are both turned on.

In Figure 18, connection points 1, 2, 9, and 10 are not used for digital I/O. Points 1 and 2 are received and transmitted data, and 9 and 10 are for clock signals for synchronous emulation. If more than two digital inputs or four digital outputs are needed for an emulated device or if secondary data transmission paths are needed, two A barrier strips must be connected to the same B barrier strip. This technique is necessary for synchronous emulation. From the standpoint of digital I/O, two adjacent A barrier strips will have to be used so that the digital inputs and digital outputs for the emulated device form consecutive sets. ETDID can then be changed to describe up to four inputs, and ETDOD can be changed to describe up to eight outputs.

#### STORAGE REQUIREMENTS

The core storage requirements for both the Scenario Interpreter and the Real-Time Exec are presented in Tables XII and XIII respectively. The data for the Real-Time Exec are based on the 64-line field test system, while the information for the Scenario Interpreter applies to both lab and field test systems.

Table XII

## Core Storage Requirements for Scenario Interpreter

<u>Assembly Module</u>	<u>Program, Words</u>	<u>Major Tables, Words</u>	<u>Total, Words</u>
SCINT	417	-	417
CMINT	668	-	668
ININT	996	64	1060
FETCH	464	64	528
TESTP	162	-	162
SUBR1	310	-	310
SUBR2	292	-	292
ALF	317	45	362
ERMSG	195	491	686
FTCHG	192*	-	192*
DUMPW	171	-	171
DUMPH	185	-	185
ISCEN	-	7	7
	<hr/>	<hr/>	<hr/>
	4369	671	5040

\* For field test system

Table XIII

Core Storage Requirements for Real-Time Exec

Name	Words
RTOS	2686
RTIN	672
MTA	758
LPT	98
SCMGT	442
PAGE	385
DSK	64
DMP	164
ASYNC	2916
	<hr/>
TOTAL	8185



## MISCELLANEOUS NOTES

(1) Assume devices A and B are both STARTed and then device B is STOPped by a STOP command. Further assume that the scenario for device A executes a type-7 scenario instruction to transfer control to device B at time T and that the scenario for device B transfers control back to A at time T'. An attempt to RESTART device A between times T and T' is not legal since device A has no task associated with it (its original task is associated with device B) even though its status (STATI) is 'S'. Error message #33 is generated in this case. Device B may not be RESTARTed during the interval since its status is not 'S', although it may be RESTARTed after the STOP command and prior to T, and after T'.

(2) The Scenario Directory is ordered the same as the DOS file directory. (LIST/L \*.IS) will produce a list on the printer of internal scenarios and their order in the DOS file directory.) By design, the Scenario Interpreter will find the first entry in the Scenario Directory whose n-character name matches the first n-characters of a scenario name in a command. Thus, if TEST precedes TESTA in the directory, a command specifying TESTA will find TEST in the directory. Similarly, M can prevent access to Ml, MATCH, etc. Implementation was done in this manner since there is no guarantee as to which of many characters may follow the last character of a scenario name. In particular, a user may declare any ASCII character as an EOM character, which would follow a scenario name.

To avoid problems of selection of an unintended scenario because of such subset names, various techniques are available. No subsetting will occur if all scenario names contain the same number of characters. In particular, if all scenario names are ten characters or more in length, no problems will occur because the DOS file directory contains only the first ten characters of a file name. Another solution is to end each scenario name with a character which is used nowhere else in

a scenario name (the ASCII \$ sign appears a likely candidate). If subset names occur, they will cause no problems if the longer names precede the shorter ones in the DOS file directory. The final solution, of course, is not to form scenario names by appending one or more characters to previous scenario names.

(3) Commands entered at the control TTY must be preceded by a left bracket (control-K):

[START DS14 Y

Command instructions punched in cards should be in the form:

CçSTART DS14 Y

The cents sign is the keypunch equivalent of the left bracket. (In the case of the scenario instruction, the cents sign is not needed for identification, but the first character in the literal is skipped over.)

(4) Partial core dumps on the printer will result from:

- a. use of the DUMP command
- b. use of the Structure Dump (?) instruction

The dump routines used to implement these functions are not re-entrant since interleaved usage by several tasks of the same printer seems unuseful. The continuity of the dump is necessary to identify the device (and scenario) causing it. The dump functions are for diagnostic purposes and should be used with care to avoid reentrancy violations.

#### PANIC CODES AND ACTIONS

If during the normal operation of the emulator, certain abnormal conditions occur, the Real-Time Exec will abort the run. Before aborting the run, however, the system saves the contents of accumulators ACO-AC3 in locations 12, 13, 14, and 15, respectively, disables

interrupts, prints out a panic code on the control teletype, and halts. The panic codes are described in Table XIV.

The user can obtain a full core dump of the system at this point by depressing the "CONTINUE" switch on the NOVA console. If only a partial dump is desired, the word count and starting address of the desired area can be entered into accumulators 0 and 1, respectively, before depressing the "CONTINUE" switch. When the dump is completed, the system will automatically try to write the magnetic tape buffers to tape, write an end-of-file on the tape and then try to make a normal emulator exit, printing out the run statistics. An example of a panic message and termination is given in Figure 22.

The run statistics that are printed on the control teletype at the end of an emulator run are: the maximum number of task control blocks that were in use at any one time (TCB MAX XXX), the maximum number of tasks that existed on the task pending queue at any one time, the number of available core blocks that exist at exit time, and the total number of core words available at exit.

Table XIV

RTOS Panic Codes

<u>Error Code</u>	<u>Meaning</u>
1	System error. Two tasks are illegally trying to remove core space from the free chain at the same time.
2	System error. Two tasks are illegally trying to return core space to the free chain at the same time.
3	System error. A task issuing a .FREE supervisor call has illegally given a block size of zero length. Usually means the core chain or Scenario Interpreter data structures are in error.
5	System error. A task issuing a .FREE supervisor call has illegally tried to free a block with a starting address the same as a block already in the free chain. Usually means Scenario Interpreter data structures are in error.
6	System error. A task issuing a .FREE supervisor call has illegally tried to free a block which overlaps the front part of a block already in the free chain. Usually means core chain or Scenario Interpreter data structures are in error.
7	System error. A task issuing a .FREE supervisor call has illegally tried to free a block which overlaps the end part of a block already in the free chain. Usually means core chain or Scenario Interpreter data structures are in error.
8	System error. A task exiting from either a .ALOC or .FREE supervisor call has found the core chain busy indicator illegally set.
9	System error. A task exiting from either a .ALOC or .FREE supervisor call has found that the link word of its TCB is illegally set. Usually means that the queue stack is in error.
10	System error. A task issuing a .FORK supervisor call has illegally given a value of zero for the new task's stack address. Usually Scenario Interpreter error.
11	System error. A task issuing any supervisor call other than .ALOC or .FREE has a zero value for its stack address. Usually Scenario Interpreter error.

Table XIV (Continued)

RTOS Panic Codes

<u>Error Code</u>	<u>Meaning</u>
12	System error. The number of clock blocks reserved at system generation have been used up by tasks issuing .WAIT supervisor calls. User is either trying to emulate too many lines with space for clock blocks or is running in loopback mode at a high baud rate.
13	Hardware error. An undefined device has caused an interrupt. Location 14 (accumulator 2) contains the device number of the offending device.
14	System error. A response having an odd number of characters has been terminated without padding out the right byte of the last word. Usually indicates response handling logic is in error when adding a new device to system.
15	System error. The word count in a query buffer is greater than 32,768, which is outside the address space of the NOVA 800. Usually means the Scenario Interpreter data structures are in error.
17	System error. The interrupt dismissal routine was called with an illegal interrupt data block address. Usually means an executive error.
18	System error. The interrupt data block address was equal to zero for a device that was trying to perform an end of operation at the non-interrupt level because the queue for the device was not available at time of interrupt.
19	System error. The initial word count for the text portion of a query buffer is equal to zero. Usually means scenario is in error or Scenario Interpreter data structures are in error.
20	System error. Lab system only. On exiting from the DCM handler the bit time indicator had been reset illegally. This panic condition was part of original Data General software.
21	System error. Lab system only. The system was unable to service all DCM lines in 5 bit times. Usually means core chain became too long. Part of original Data General software.
25	Hardware error. The magnetic tape controller indicated an error when a status instruction was executed upon a



Table XIV (Concluded)

RTOS Panic Codes

<u>Error Code</u>	<u>Meaning</u>
	tape interrupt. Location 12 (accumulator 0) contains the status of the tape drive. The explanation of the status is given in Reference 5.
26	System error. The magnetic tape handler received a non-error interrupt and did not have a record of having written a tape buffer. Usually means the tape device unit control block has been destroyed.
27	Hardware error. In reading the magnetic tape status before writing, either bit 1, 2, 3, or 5 has been set indicating some type of tape unit trouble. From experience panic code 25 usually occurs before this condition.
28	System error. A task issuing a .FTCH supervisor call has passed a scenario program counter which is larger than the scenario itself. Usually means that the internal scenario on disk has been destroyed or the scenario management routine has an error.
29	Hardware error. The disk controller indicated an error when a status instruction was executed upon a disk interrupt. Location 12 (accumulator 0) contains the status of the disk controller. The explanation of the disk status is given in Reference 5.

Note: The above panic conditions were inserted during the debugging and development phase of the emulator software. From experience the only ones that a user may usually encounter are 12, 13, 21, and 25. Any of the others occurring usually means a new problem uncovered and should be reported to the system programmers.



OP0  
WAIT  
ENTER RUN ID

1  
READY

PANIC: ERROR CODE=21  
HIT CONTINUE FOR FULL CORE DUMP

TCE MAX 000003 TP0 MAX 000003  
CORE LINKS 000002 CORE AVAIL 027363  
DOS REV 05.

R

Figure 22. Example of Panic Message

## SECTION VIII

### DATA REDUCTION PROGRAM

#### INTRODUCTION

The Data Reduction program (DATAR) processes log tape data gathered during an emulator test run. The program produces scenario trace data and various statistics on the performance and utilization of both the emulator and the SUT. A complete description of the design and implementation of the program can be found in Volume 7 of this series. DATAR runs under Data General Corporation's standard Disk Operating System (DOS), Revision 5.

DATAR may be used to produce several kinds of summary and detailed listings from the log tape, and thus it allows the user to obtain a quick summary of activity during the run on an individual basis or as an entire system. DATAR also gives detailed information in the form of record-by-record listings that include information such as readable real-time clock (RRTC) times, various timing calculations, and the text message.

After the tape file is processed by DATAR, the user may save the test data on master log tapes (to consolidate tapes or to put similar runs on one tape). The master (or original) log tape may be used for later analysis on the NOVA 800 or on a larger machine with more sophisticated data reduction and analysis capabilities.

#### SYSTEM FLOW

Figure 23 depicts the system flow of DATAR programs. The log tape, with data gathered from a single emulation run or a series of runs, is mounted and readied on the system tape drive, transport 0, prior to any user input requests. The log tape provides the input to DATAR.

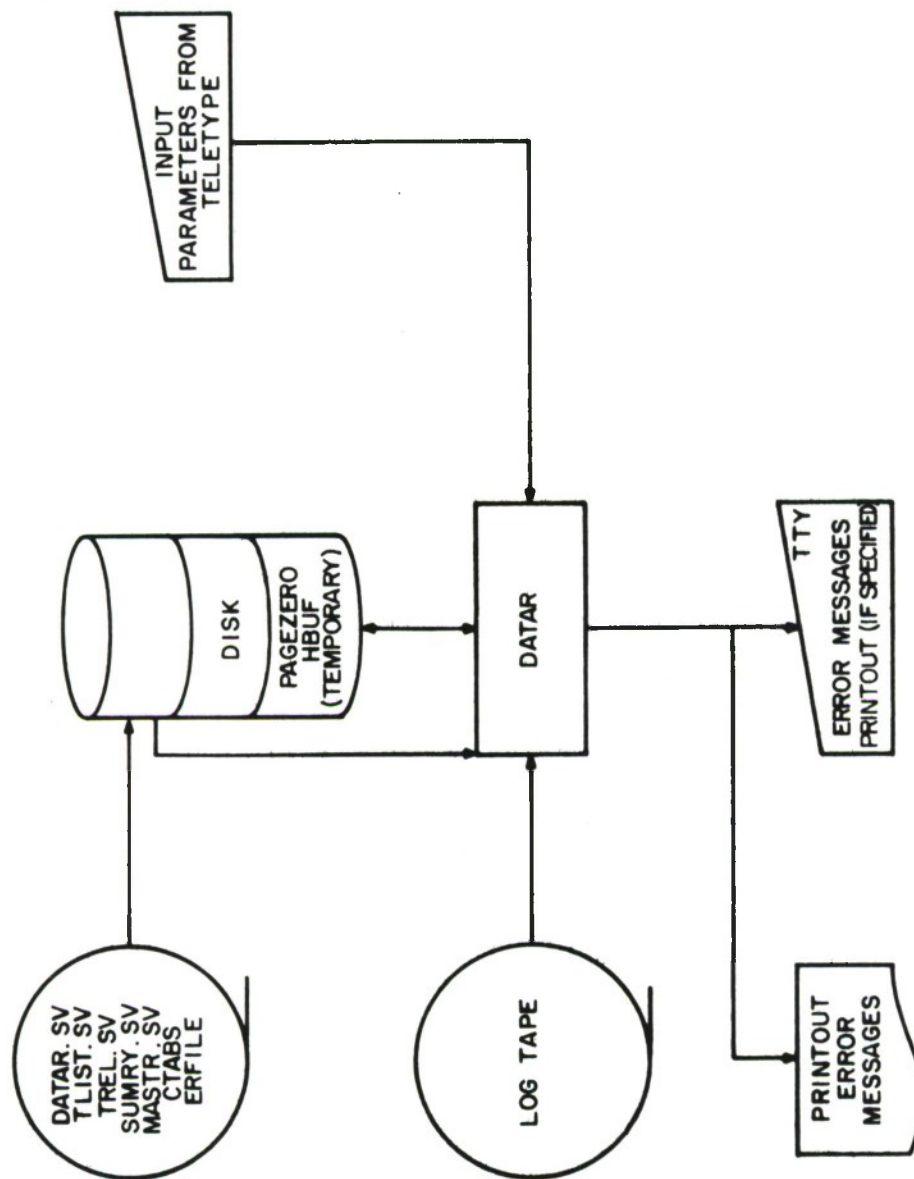


Figure 23 GENERAL SYSTEM FLOW OF DATA REDUCTION PROGRAM

DATAR is called by entering an input message on the system teletype. There are two forms of input messages which result in two modes of operation, interactive (conversational) or switch. The interactive mode requires the user to specify input arguments by responding to a series of interactive requests output by DATAR. The switch mode, where a switch is the character / (slash) followed immediately by an alphabetic character, uses switches to modify input groups and specify input arguments.

On entry, the Command Interpreter (CI) residing in DATAR.SV is loaded from disk and uses the input arguments to determine the type of output to be produced. The user may obtain a brief summary, a detailed summary, an octal tape listing, or a listing with actual RRTC times, with time differences (intervals), or with relative times. The output device, (line printer or teletype) is also determined from the input message. DATAR output is printed at the specified device, and error messages are output to the teletype and, if in use, the line printer.

DATAR requires the conversion tables (CTABS) and the error message file (ERFILE) to be disk resident for all types of output. If an octal listing is desired, the CI begins printout on the output device. However, if a summary or another type of listing is desired, the CI saves some information on disk in two temporary files, PAGEZERO and HBUF, and calls one of the save files (SUMRY.SV, TLIST.SV, or TREL.SV) into execution to do the processing. Error messages are directed to the teletype and the output device. Note that a CONTROL-A interrupt stops all programs and returns to DOS without deleting the temporary disk files, PAGEZERO and HBUF.

## OPERATING PROCEDURES

### Input Message

DATAR is called by entry of a user input request starting with

the program name DATAR. The two valid messages are:

1. DATAR [out-device]
2. DATAR/  $\begin{pmatrix} B \\ D \\ L \end{pmatrix}$  [/sub-options] [id] [RECORDS/types]  
[out-device]

where  $\emptyset$  indicates a space.

Both messages result in the disk operating system (DOS) loading the save file DATAR.SV and passing control to the CI portion of DATAR. The ordering of the input groups is important and should be adhered to as illustrated above.

#### Command Interpreter

The CI operates in two modes, interactive (conversational) and switch. The interactive mode is invoked by message type 1 above. The switch mode requires a more complex input message (type-2) but minimal user interaction. Also, the switch mode is easier to enter and is processed by the CI in less time.

#### Interactive Mode

The interactive mode operates in the following manner. DATAR types an interactive request that includes all valid responses as shown in Table XV. The user must reply with either the full word response or the corresponding integer. Based upon the user response, DATAR either types another request or determines that the required input parameters have been obtained and passes control to processing. A user reply of COMBINATION (or 7) to request number 5 or of COMBINATION (or 8) to request 7 causes the CI to type requests 6 or 8, respectively. In either case, a 1 to 5 or 6 digit integer must be entered using the specified digits from the preceding request. Also, a user reply of LIST to request 9 causes the CI to type a list of the numbers and names of all devices defined in the Equipment Table. Following the list, the CI reissues request 9. The user may respond with numbers or names, but repetitions are ignored. A list of requested devices

Table XV

## Interactive Requests and Responses for DATAR

Request Number	Text
1	ENTER SUT RUN NAME.
2	ENTER OPTION: BRIEF(1), DETAILED(2), OR LIST(3).
3	ENTER YES(1), OR NO(Ø) FOR PLOT.
4	ENTER SUB-OPTION: INTERVAL(1), SPECIFIC(2), ORDERED(3).
5	ENTER SUB-OPTION: INTERVAL(1), SPECIFIC(2), ORDERED(3), ACTUAL(4), OCTAL(5), RELATIVE(6), OR COMBINATION(7).
6	ENTER COMBINATION AS 1 TO 5 DIGIT INTEGER USING 2 TO 6 ABOVE.
7	ENTER RECORD KEY: ALL(1), HISTORY(2), SCENARIO(3), QUERY(4), RESPONSE(5), COMMAND(6), ERROR(7), OR COMBINATION(8).
8	ENTER COMBINATION AS 1 TO 6 DIGIT INTEGER USING 2 TO 7 ABOVE.
9	ENTER DESIRED DEVICE NUMBERS OR NAMES SEPARATED BY BLANKS OR LIST.
10	ENTER YES(1), OR NO(Ø), FOR START, STOP SPECIFICATION.
11	TO TERMINATE, ENTER END. ENTER LOGICAL OR PHYSICAL RECORD START, STOP PRECEDED BY L OR P.



is printed in the order defined by the Equipment Table. Figure 24 illustrates the various interactive paths to obtain the desired output.

The output device to be used must be specified in the original message. The optional input group, Out-device, has a value of \$TTO for the system teletype or \$LPT for the system line printer (the default output device).

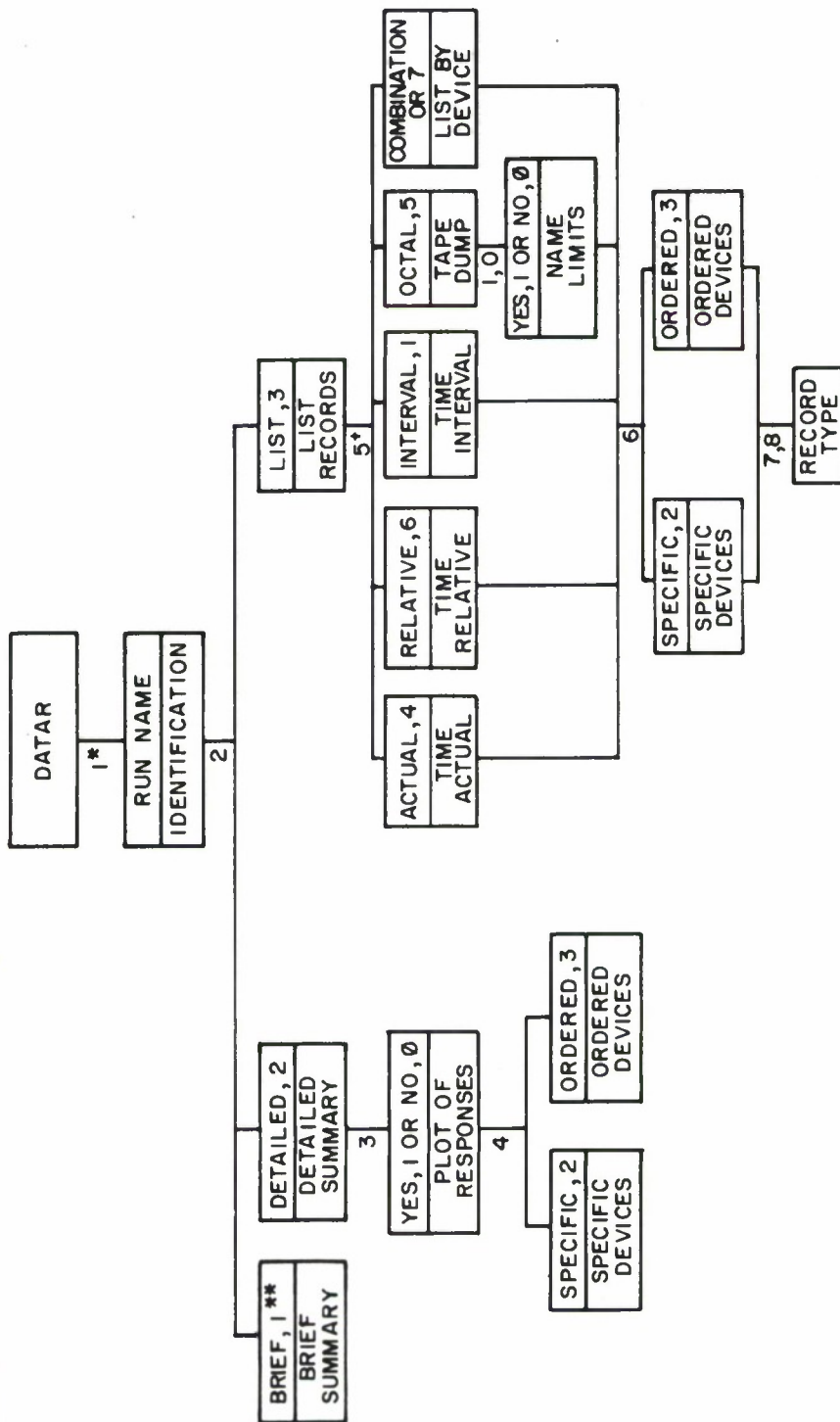
#### Switch Mode

The message which invokes the switch mode is given in general form by message type 2 above. One of the three switches (/B, /D, or /L) must accompany the program name DATAR, otherwise the interactive mode is entered. All switch letters were chosen to relate to the function performed and to simplify mnemonic identification.

The input group  $\text{DATAR/D}^{\text{B}}[\text{suboption(s)}]^{\text{L}}$  allows various combinations of option and suboption switches. One of the option switches B, D or L is required; if more than one is given, precedence is given first to B, then D. The option switches, listed in Table XVI, determine the type of output to be generated: brief summary, detailed summary, or listing.

The suboption switches are also listed in Table XVI. The suboption switches are meaningless for the B option. For option D, only O, S, and P are meaningful. For the L option, all are meaningful except P. The suboption switches specify the type of data to be included in the output option. They also determine if the data are to be given sequentially or on an individual device basis. If the data are to be given by device, the suboption switches tell DATAR whether all or user specified devices are to be examined.

The optional input group id specifies the run identification. It is the first  $n$  ( $1 \leq n \leq 20$ ) characters of the run identification given at the start of a real-time emulator run. If id is not given, DATAR uses data from the first run on the tape.



\* THE INTEGER n SPECIFIES REQUEST NUMBER FROM TABLE XV  
 \*\* THE GROUPS (RESPONSE WORD, NUMBER) ARE TAKEN FROM TABLE XV  
 + A RESPONSE OF 2 OR 3 GIVES ACTUAL TIMES BY DEVICE

Figure 24 INTERACTIVE TREE DIAGRAM FOR DATAR

Table XVI

## Option and Suboption Switches for DATAR

Switch	Function
<u>Options</u>	
/B	Brief Summary
/D	Detailed Summary
/L	Listing
<u>Suboptions</u>	
/S	Examine only user specified devices
/O	Examined all devices in order of E.T.
/P	Histograms of Response Distributions
/N	Name records for octal dump
/I	Print time intervals rather than actual times
/R	Print Relative times rather than actual times
/T	Octal format tape dump

The optional input group RECORDS/type(s) specifies the type(s) of logical records to be included in the output. Valid switches are given in Table XVII. Any combination of values is allowed. Omission of this group implies all types. The B and D options ignore this group.

The optional input group Out-device is defined above under interactive mode.

The option and suboption switches may be combined as shown in Table XVIII and Figure 25. Table XVIII presents all meaningful input requests with a brief description of the output. (The optional input groups are not listed.) Figure 25 also illustrates the meaningful switch combinations.

### Summaries

There are two types of summaries, brief and detailed. The brief summary examines all records for all devices, listing error messages and gathering general statistics. The detailed summary gives similar statistics but does so by device. Note that the input group RECORDS is meaningless since both summaries examine all types of records.

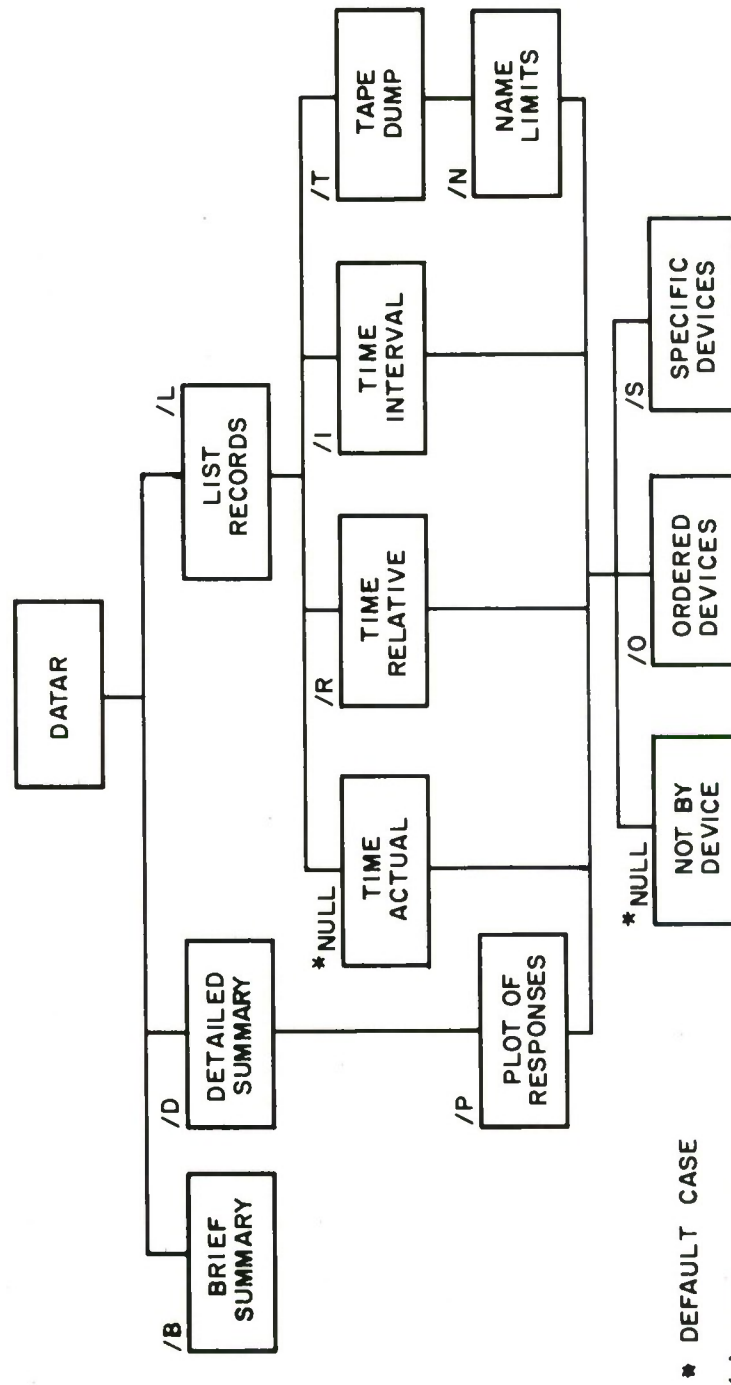
#### Brief Summary

The brief summary ignores suboption selections. The format of the brief summary output is illustrated in Appendix IV, Figure 26. The summary data in this figure is taken from the file with the run identification of "RUN FT7". A list of all error messages with associated device names precedes the summary data.

Various RRTC times are given in the following units: elapsed time is expressed in seconds to the nearest 100,000 th, response times in seconds to the nearest hundredth, total emulator CPU time in tens of microseconds, and percent emulator CPU to the nearest hundredth.

GENERAL SWITCH-MODE INPUT MESSAGE

DATAR ( /B ) ( /D ) ( /L ) [ /I /P /S ] [ /R /N /O ] [ /T ] [ /H /R ] [ /S /C ] [ /Q /E ] [ \$LPT ] [ \$TTO ]



- \* DEFAULT CASE
- ( ) REQUIRED GROUP
- [ ] OPTIONAL GROUP

Figure 25 SWITCH TREE DIAGRAM FOR DATAR

Table XVII

## Record Type Switches

Logical Record	Symbol	Switch
HISTORY	H	/H
RESPONSE	R	/R
QUERY	Q	/Q
SCENARIO INSTRUCTION	S	/S
COMMAND	C	/C
ERROR	E	/E

Table XVIII

## Switch Combinations and Valid Inputs

Input Message	Action Taken
1. DATAR/B	BRIEF summary of all data preceded by a list of error messages.
2. DATAR/D [/P] or DATAR/D/O [/P]	DETAILED summary for each active device in the order established by the Equipment Table. A plot of response times is available as an option (/P).
3. DATAR/D/S [/P]	Same as above except only those devices specified by the user (upon request) are examined.
4. DATAR/L/I	LIST all records in sequence written. Include transmission time intervals, processing (task) time intervals, and response times.
5. DATAR/L/I/O	Same as above except list separately for each active device.
6. DATAR/L/I/S	Same as above except devices must be specified by user.



Table XVIII (Concluded)  
Switch Combinations and Valid Inputs

Input Message	Action Taken
7. DATAR/L	LIST all records in sequence written. Include internal scenario address and actual clock times for start transmission and start/end task.
8. DATAR/L/O	Same as above except list separately for each active device.
9. DATAR/L/S	Same as above except devices must be specified by user.
10. DATAR/L/R/O	LIST separately for each active device all records in sequence written. Include internal scenario address and start/end transmission times relative to LOGON and test start time.
11. DATAR/L/R/S or DATAR/L/R	Same as above except devices must be specified by user.
12. DATAR/L/T [/N]	LIST all records in sequence written in octal tape dump format. Naming of starting and stopping logical (or physical) record numbers is available as an option (/N).
13. DATAR/L/T/O [/N]	Same as above except list separately for each active device.
14. DATAR/L/T/S [/N]	Same as above except devices must be specified by user.

The logical and physical record counts are given by the counts following the headings MESSAGES and RECORDS, respectively. The headings UN-R and UNSOLICITED specify unsolicited responses. The TERMINAL-MAX heading is used to name the terminal associated with the maximum response. The asterisk (\*) following a scenario instruction type denotes a lower case character or a non-printable special character.

#### Detailed Summary

The detailed summary allows a device specification suboption as well as a special histogram output. The format of the detailed summary is illustrated in Appendix IV Figure 27. A list of all requested devices to be examined is printed prior to summary data, and consists of either all devices defined in the Equipment Table or only those devices specified by the user.

The name of the file used in Figure 27 is "RUN FT7". A detailed summary is given for each active, requested device, and the name of the device is given as a terminal identification. Unsolicited responses are counted as record types. Also, the average and maximum RRTC response times are given in seconds, to the nearest hundredth. As in the brief summary, an asterisk (\*) is used to identify non-printable lower case and special characters which are used as scenario instruction types.

If requested, a histogram of response distribution is printed for each active device following the summary data. Figure 28, Appendix IV illustrates the format of the histogram. As can be seen, the name of the device is given at the top of each page and is followed, on the first page, by a list of all quarter-second response intervals which have a positive count and percentage. The count gives the actual number of responses which fall within the specified interval. The percentage is calculated by dividing the count by the total number of responses. All responses less than 0.25 seconds are

included in the first interval, while all responses greater than 15.00 seconds are shown in the 15.00 second interval. If there are no intervals with a positive count, then a histogram is not generated.

Following the summary data (and histogram if requested) for the last active device, the program lists all requested devices which were found to be inactive during the run.

### Listings

There are basically four types of listings: octal tape, actual times, time intervals, and relative times. All these suboptions allow record selection based on device and/or record type. If the user decides to obtain the listing by device, then all devices defined in the Equipment Table must be requested or the desired device names and/or numbers must be specified in response to the interactive request number 9. A list of all requested devices will precede any data and a list of requested but inactive devices will terminate the listing.

The types of logical records to be listed may be selected by using the RECORDS input group. In the switch mode, all records are listed if the RECORDS group is omitted. The heading MESSAGE on each listing page refers to the logical record number of the first non-history record on the page.

### Octal Tape

The octal tape dump listing is used to print the contents of each logical record in octal byte format. The user may name the starting and stopping logical (or physical) record number by using the /N option. If starting and stopping numbers are given, the program skips all logical (or physical) records up to the start. It produces its octal output in logical record format and stops at the given logical (or physical) record number. Figure 29 in Appendix IV illustrates an octal tape listing output format. The user requested

that all devices in the Equipment Table be examined and named the starting and stopping logical record numbers as 101 to 110.

As can be seen, the output for each active device gives the range limits and device name prior to the data. After a range is completed, the user may specify another range of limits or continue to the next active device. Note that there may not be records within the range associated with the given device (CTØ in Figure 29). The character P represents the physical record boundary.

#### Actual Times

The actual time listing contains the actual RRTC start of transmission and the start and end of task processing times. The values are taken directly from the record and listed in tens of microseconds. The actual time listing is the default suboption in the switch mode. Figure 30 in Appendix IV illustrates the format of the actual time listing.

As shown by the example in Figure 30, the user requests an actual time listing of Query and Response records ordered sequentially and output on the system teletype. The name of the run is "6-14 4:30 PM." For each record, the type of record is given followed by transmission start, task start, and task end times. The heading SCEN ADDR gives the location of the start of the scenario instruction relative to the beginning of the scenario, if any.

#### Time Intervals

The time interval listing contains differences between the RRTC times. This listing also calculates response times as the difference between the start of transmission for a solicited Response and the end of transmission from the preceding query associated with the same device. Figure 31 in Appendix IV presents the format of the time interval listing.



The example in Figure 31 shows a time interval listing of "RUN2" in which the user chooses to specify the devices to be examined. For each active device, the terminal identification is given, followed by all the data associated with the particular device. For each record, the record type is given as well as the difference between the end and start of transmission time, the difference between the end and start of task processing time, and the cumulative emulator CPU time, all in tens of microseconds. The response times are given in seconds to the nearest hundredth.

#### Relative Times

The relative time listings are by device with user specification of devices being the default case. Figure 32 in Appendix IV illustrates the format of a relative time listing.

In the example shown in Figure 32, the user requests that all devices defined in the Equipment Table be examined. Both the run-start time and the user start time (UST) are given in tens of microseconds. The run-start time is the start of transmission of the first non-history record in the file. The UST is the start of transmission of the first Query or solicited Response associated with the device. A value of BELOW is given for UST if a Query or solicited Response is not the first record type in the file for the particular device. For each record, the record type is given in addition to the start and end of transmission minus the UST, the start and end of transmission minus the end of transmission time of the previous Query, and the location of the scenario instruction (as SCEN ADDR) relative to the beginning of the scenario, if any.

#### ERRORS

There are several error conditions recognized by the various programs. Table XIX lists all error conditions and messages that may

Table XIX

## DATAR Error Message File (ERFILE)

Number	Message	Cause or Corrective Action
1	Invalid option	Submit valid option.
2	Invalid termination option	Submit valid option.
3	Invalid sub-option or key (record)	Submit valid option.
4	Invalid device specification	Submit valid device name or bad device address logged.
5	Disk file accessing error (read/write)	Error from DOS, disk file may be missing.
6	End-of-file (on tape)	End of run.
7	Invalid tape identification	Log tape file incorrectly logged.
8	Unrecognizable message type	Bad record type logged.
9	Zero length record found	Two successive records with zero word length.
10	Illegal program call	Overlay problem, maybe disk file is missing.
11	Command instruction missing	C-type record with null text.
12	DISK SPACE exhausted	Not enough disk for temporary files or overlay.
13	Invalid device table format	Equipment Table not second record in file.
14	Tape read error	Tape drive problems, may not be mounted properly.



occur, during execution of DATAR.

The general format of the error message is:

RECORD m, WORD n: error message text

where m specifies the physical record that contains the erroneous logical record and n specifies the first word of the logical record relative to the start of the in-core buffer containing the record. Many of the conditions allow the user to start over or submit another choice. However, some (such as tape and disk errors) are unrecoverable. The cause of error condition and/or corrective action for each error is also given in Table XIX.

#### SAVING TEST DATA

After analyzing the test data with DATAR, the user may wish to save the data for future analysis on the NOVA 800 or some larger computer. A program (MASTR) has been written to transfer data from a log tape to a master log tape (to consolidate tapes or to get comparable runs on one tape). The master tape (or original tape) may then be used as input to DATAR to analyze the run again or compare a series of runs manually. In addition, more sophisticated statistical methods may be employed to produce more meaningful statistics for comparing and evaluating an SUT.

#### Program Description

In general, the MASTR program (written for a one tape drive system) reads the data from the input log tape, temporarily stores it in a file on disk, waits for the output (master) tape to be mounted, writes the data from disk onto the tape as the last sequential file, and terminates the file with two end-of-file (EOF) marks. If disk storage is insufficient to complete the transfer in one pass, the program continues through as many passes as necessary, each time notifying the user that an additional pass is required. Obviously, a

multiple pass transfer requires input and output tapes to be mounted and dismounted several times.

### Input Message

MASTR requires two user supplied input parameters: a run identification (used to locate the test run) and the amount of available disk space (used as temporary storage). The two commands that activate the tape transfer program are:

1. MASTR ↵
2. MASTR id ds ↵

(Although message 1 appears more concise, note that requests to supply values for the input groups id and ds will be issued by the program.) The first input group, id, specifies the first  $n$  ( $1 \leq n \leq 20$ ) characters of the run identification as found in the Identification-History record, the first logical record logged. The run identification, which was entered at the start of emulation, is required to allow access to different runs on multiple run tapes.

The second group, ds, is the number of unused disk blocks available for temporary storage. The program uses  $ds-2$  blocks to protect the used portions of disk. The number of unused blocks is given by the DOS command DISK. This number can be increased by deleting disk files no longer in use. A good approximation for the number of blocks required for a single pass transfer is the number of physical records used for the run (obtained from the record count in a brief (/B) summary) plus five (two for the unused blocks and three for disk file linkage words). This number must be multiplied by the ratio of physical record size to disk block size, which presently is 1.

### Operation

The MASTR program is called by one of the input messages described above. It obtains the run identification and disk size from the input message (#2), or as responses to the program commands ENTER RUN

IDENTIFICATION and ENTER AMOUNT DISK LEFT. The program then issues the command:

MOUNT INPUT TAPE, STRIKE CARRIAGE RETURN

and waits for a carriage return. Upon receipt of the carriage return, MASTR locates the first file (on the input tape) that contains the specified run identification as the first n characters in the History-Identification record.

The program uses the disk size and physical record size to calculate the number of tape records that can be written in the temporary disk file, MITCHTEMP. MASTR reads the tape until disk space is exhausted or an EOF mark is encountered. If disk space is insufficient the message:

NOT ENOUGH DISK.

REMOUNT INPUT TAPE AFTER OUTPUT TAPE IS WRITTEN.

notifies the user that one or more additional passes are necessary to complete the transfer. This implies remounting the input tape after the first segment is transferred to the master tape.

After the disk file is written, MASTR issues the command:

MOUNT OUTPUT TAPE, STRIKE CARRIAGE RETURN

and waits for the carriage return. Upon receipt, the program locates the double EOF mark on the master and writes all the data from the disk file onto the output tape, overwriting the second EOF of the preceding run. If an additional pass is necessary, the program requests that the input tape be mounted and continues the loop until the transfer is completed. Upon completion, the message:

LOG TAPE TRANSFER COMPLETE

is output and two EOF marks are written. The first EOF terminates the file while the second indicates that the file is the last one on the

tape. Note that a tape intended to be a master must be initialized by the DOS command INIT/F MT0 prior to the transfer operation. The command writes two EOF marks at the beginning of the tape.

The program does not check the run identification of each file on the output file. Therefore, files may be written with duplicate file names. However, only the first file with a duplicate file name is accessible.

### Errors

The MASTR program checks for various error conditions. If an error exists, a message is output and the transfer terminates by returning to DOS. Table XX lists the error conditions, messages, and suggested corrective action. Remember that files on a master tape are only as unique as the run identification given at the start of the emulation test.

Table XX

## MASTR Error Message File

ERROR MESSAGE	ERROR CONDITION	CORRECTIVE ACTION
1. NOT ENOUGH DISK	Space too small for one physical tape record.	Delete some files and specify larger number.
2. ERROR LOCATING INPUT FILE	Invalid run id, illegal format, tape read error.	Check id, format, read errors by using DATAR/B with and without run id.
3. DISK ERROR	Trouble writing/reading file MITCHTEMP.	Ensure disk accessibility. Try again.
4. INPUT TAPE READ ERROR	Tape equipment or parity problem.	Check channel number, unit ready, etc. Otherwise, fatal parity error.
5. OUTPUT TAPE WRITE ERROR	Tape equipment or parity problem.	No double EOF or check channel number, unit ready, write lockout, etc. Otherwise, fatal parity error.
6. EOF WRITE ERROR	Tape equipment or parity problem.	No double EOF or check channel number, unit ready, write lockout, etc. Otherwise, fatal parity error.
7. ERROR LOCATING OUTPUT FILE	No second EOF, tape equipment or parity problem.	Check equipment, initialize tape if never done before.



## SECTION IX

### EXECUTION TIMES

#### REAL-TIME INSTRUCTIONS

Because of the variety of scenario instructions available to the user, it may be possible in some instances to accomplish the same task using more than one method, or combination of scenario instructions. In these cases, execution timing for scenario instructions may be a consideration in determining maximum scenario efficiency.

Table XXI gives the current best estimates of real-time emulator execution times. The times given represent the total cost (Scenario Interpreter as well as Real-Time Exec execution time) in microseconds of emulator CPU time for executing each function once. The functions timed include two miscellaneous functions (logging and the receipt of an unsolicited response) followed by the scenario instruction types given in the same order as Table XVI of Volume 2 of this series followed by the command types in alphabetical order.

The data were obtained by making a very large number of short runs on the field-test emulator. In most cases, a run consisted of executing a single scenario for a single device. After performing its task, the scenario executed a QUIT command. The data reduction brief summary operation was used to obtain the CPU time.

The general technique used was to execute the desired function 1000 times in a loop, as in the case below of one of the scenarios used to test the add instruction:

1	3	A 12
2	3	C [ LOG ALL OFF ALL
3	22	1 1000 R9
4	28	L LOOP



Table XXI  
Execution Times  
Real-Time Scenario Instruction  
(in microseconds)

Function	Execution Time	Footnotes
Miscellaneous Functions		
Logging	1778 + 6.6b	
Receipt of Unsolicited Response	2325 + 231r	10, 26
Control Instructions		
R	4397 + 477i	10, 26, 27
R''	3312 + 220r	10, 26, 27
Q	1721 + 255q	10, 26, 27
I	3845 + 440i	10, 26, 27
O	953 + 213q	26, 27
;	679	27
:	679	13, 27
C	—	10, 14, 27
E	3137 + 54e	10, 15, 27
E	1333	10, 16, 27
D	1305	1, 3
W	1458	1, 3, 13
d	1236	1, 3
e	1136	5, 27
X	7490	7, 27
7	761	3
8	2703	1, 17, 27

Table XXI (Continued)  
Real-Time Scenario Instruction Execution Times

Function	Execution Time	Footnotes
Arithmetic and Logical Instructions		
+	691	1, 27
-	691	1, 13, 27
*	768	1, 27
/	788	1, 6, 27
&	708	3
Assembler Directive Instructions		
L	-	18
a	-	18
blank	-	18
t	-	18
i	-	18
Branch and Comparison Instructions		
J	631	3
B	689	1, 2, 3, 13
U	689	1, 2, 3, 13
>	689	1, 2, 3, 13
<	689	1, 2, 3
G	689	1, 2, 3
H	689	1, 2, 3
M	657 + 39m	3, 19
S	677 + 39m + 48n	1, 9, 27
Y	719	2, 3

Table XXI (Continued)  
Real-Time Scenario Instruction Execution Times

Function	Execution Time	Footnotes
<b>Branch and Comparison Instructions (continued)</b>		
n	719	2, 3, 13
9	721	2, 3
q	722	2, 3, 13
K	682	3, 20
3	682	3, 4, 20
P	1016 + 43.5p	3
<b>Core Memory Allocation Instructions</b>		
A	1361	3, 10
F	7858	10, 21, 27
<b>Move Instructions</b>		
l	668	3
g	687	27
p	687	1, 27
5	621 + 43.6t	27
T	640	27
=	717	27
Z	715	13, 27
V	663 + 21.6c	13, 27
6	647 + 21.6c	27
\	708	27
@	748	27
r	680	27
c	666	4, 27
h	684	27

Table XXI (Continued)  
Real-Time Scenario Instruction Execution Times

Function	Execution Time	Footnotes
Diagnostic Instruction		
?	—	
Commands		
DUMP	—	
ERROR	3950	
LOG	5355	12, 22
MONITOR	7204	12
QUIT	2060	23
RESTART	12,571	12, 24
SCALE	5046	25
START	14,540	8, 10, 11, 12
STATUS	4681	12
STOP	12,571	12, 24
SUB	7490	7, 10, 11
SUB	7858	10, 11, 21

Table XXI (Continued)  
Real-Time Scenario Instruction Execution Times

Nomenclature

- b = length of MESBF (variable or text) portion of buffer to be logged, in bytes
- c = number of bytes for which longitudinal redundancy check (LRC) byte is calculated
- e = length of error message in type-E scenario instruction, in bytes
- i = length of query and length of response, in bytes
- m = number of bytes successfully matched
- n = number of bytes unsuccessfully matched
- p = number of bytes parity checked
- q = length of query transmitted, in bytes (those up to, but not including, the first NULL (zero) byte in a query buffer)
- r = length of response received, in bytes
- t = number of bytes transferred from (contained in) a type-5 scenario instruction to a query buffer

Table XXI (Continued)  
Real-Time Scenario Instruction Execution Times

Footnotes

- (1) Add 6.4 microseconds for each field of type 10 or 11 which contains a Register number.
- (2) Add 6.4 microseconds if the branch is not taken.
- (3) Add 14 microseconds if instruction starts at an even byte.
- (4) Add 26.8 microseconds if initial value of RGRPT points to an odd byte.
- (5) The time includes the time for the type-e instruction plus the additional time for the following (executed) instruction over what it would be if executed normally rather than by the execute instruction. The normal execution time of the following instruction is excluded. Increased time over most other instructions is spent in scenario management code in the Exec. The type-e instruction causes two changes in the scenario associated with the device. The time given includes the time to free each core page when control passes to the other but no time to read pages from disk since the core pages were not overlaid. If one or both core pages were in use by other devices, the freeing time would be less, but if all core pages were in use, the type-e instruction could require disk reads to be done.
- (6) Execution time varies by 11.4 microseconds from minimum to maximum, depending upon values used.
- (7) Includes time to execute the SUB command with scenario specified and the type-X scenario instruction. Includes time to allocate set of Registers but not the time to free them.
- (8) Includes the time to start the scenario for the named device and to terminate that scenario by execution of end-of-scenario.
- (9) Add 13 microseconds if branch not taken. If substrings of the instruction string occur in the response, the number of comparisons may be relatively large. For instance, if the response ABCABACABABCABABACABABABC is searched for the string ABABABA, then  $m = 27$  and  $n = 19$  and the execution time is 2642 microseconds.



Table XXI (Continued)  
Real-Time Scenario Instruction Execution Times

Footnotes

- (10) Execution time will vary depending upon the number of blocks in the free chain which have to be examined to find a large enough block to allocate and/or to find the proper place in the chain to place a freed block.
- (11) Execution time will vary depending upon the number of Scenario Directory entries which have to be examined before the named one is found.
- (12) Execution time will vary depending upon the number of Equipment Table entries which have to be examined before the named one is found, and this number may be different depending upon whether hierarchical equipment names are used in the command or not.
- (13) Time estimated based on measured time for a similar instruction.
- (14) Time varies widely with command type. The time to execute the type-C instruction is included in the command execution time, except for the QUIT command.
- (15) Time with error-message logging enabled. Time includes logging time.
- (16) Time with error-message logging disabled.
- (17) Includes time to log the response. If response logging is disabled, the instruction is equivalent to a NOP.
- (18) Assembler directives are not executed in real-time and are not even included in the internal scenario.
- (19) Add 24 microseconds if the branch is taken. If an m-character compare is made, the first four characters match, but the fifth one does not, the execution time should be  $657 + 4(39) + 24 = 837$  microseconds.
- (20) Add 10 microseconds if branch not taken.
- (21) Includes time to execute the SUB command with scenario specified and the type-F scenario instruction.

Table XXI (Concluded)

Real-Time Scenario Instruction Execution Times

Footnotes

- (22) Time for LOG ALL OFF ALL
- (23) Time through the time the record is logged. Certain termination activities are performed after logging.
- (24) Includes time to execute RESTART of a named device and time to execute STOP THIS for the named device.
- (25) Time will vary depending upon number of digits in scale factor to be converted. Conversion time is 31 microseconds per decimal digit.
- (26) Using an asynchronous line adapter at 10 characters per second.
- (27) Add 12.8 microseconds if instruction starts at an odd byte.

5	28	+ R9 34 R11
6	34	+ R8 1 R8
7	40	U LOOP R9 R8
8	47	C [ LOG ALL ON C
9	63	C [ QUIT

Instruction 5 is the one being timed. Instructions 6 and 7 are for loop control and instruction 3 controls the iteration count. Instructions 2 and 8 turn logging off and then back on to capture the final CPU time value. Such scenarios were run two or more times each to check the degree of reproducibility.

A second, base scenario was then prepared, identical to the above except that instruction 5 was eliminated. The second scenario was then run two or more times, and the most representative CPU time value was chosen for each of the two scenarios. The difference between these values divided by the iteration count gives the function execution time.

The contents of the two scenarios were varied depending upon the function being timed. In the case of several of the commands, more than one scenario had to be run concurrently. The iteration count was reduced to 100 for the miscellaneous functions, for some of the commands, and for the query instructions. In any case, an appropriate base scenario was always constructed and run so that the difference in CPU times would isolate the function or functions being timed (a few of the functions cannot be executed multiple times independently of other functions).

The measured results were given general reasonableness checks and were also evaluated by comparing differences between measured results for different functions (primarily the scenario instructions) and differences obtained from NOVA instruction counts for the same functions. No attempt was made to verify the absolute values given

in Table XXI because of the complexity of the emulator system. The relative comparisons checked reasonably well, although certain differences have not yet been explained. The data in Table XXI cannot be regarded as precise. The presence of a zero in the units position cannot be regarded as indicating low precision nor can the presence of a decimal place be regarded as indicating high precision in all cases. In the latter case, the increments given in the table proper for those functions whose execution times vary with string length, the increments given were obtained by computations on the measured results, although these increments checked rather well in those cases in which instruction counts were made. The increments given in the footnotes are generally more precise since most of them are based on instruction counts (assuming that the CPU clock is accurate).

The relative comparisons made for approximately 15 scenario instruction types indicate precision varying from 0 to 35 microseconds. No formal comparisons were made for the commands although it appears possible that much larger discrepancies may be present. In particular, from scanning the code for the LOG command and the MONITOR command, it does not seem reasonable that the latter should require nearly 2 milliseconds more than the former. It should also be noted that a typical command generally provides many more options than a typical instruction and, therefore, will result in a much greater range of execution times. It was not possible to time and report each option of each function. In addition, as the footnotes show, a number of run-dependent factors can significantly affect the timing results.

Several factors are present which would make it very costly to attempt to resolve the discrepancies noted above. At least 700 runs were made to obtain the current data. Most of these lasted several seconds in real-time, but some lasted a minute or two. The results had to be listed, recorded, and analyzed. Most of the scenarios were run two or more times each since the results frequently showed some

variation in total emulator CPU time. It was felt that replicated runs should agree within possibly 10 to 30 microseconds based on early experience with the simpler instructions. In the case of some of the commands and query instructions, the total variation was sometimes 200 or 300 microseconds. In the case of a common base scenario run a number of times over a two-month period, the total variation was 3200 microseconds (for a  $1\frac{1}{2}$  second run - 0.2%). It seems likely that these variations are the result of clock frequency variations, possibly the result of temperature differences. The clocks involved were the CPU clock, the Readable Real-Time Clock used for timing measurements, the "Real-Time Clock" used for response timeouts and which places a continuous overhead on the DVM, and the line-adaptor clocks in the case of query instructions. In addition, it is known that the timing characteristics of the magnetic tape drive have a rather coarse control, and the tape drive had to be used in all runs to record at least the first and last event of the run.

A cause of greater variation in execution times is the fact that the NOVA computer has only very superficial byte-manipulation ability. The Exec uses 12.8 microseconds more to fetch the two-byte scenario instruction length field (for any instruction) from two adjacent words (when the instruction starts at an odd byte) than when it starts at an even byte. The Scenario Interpreter uses 26.8 microseconds more to fetch a two-byte operand (contained in certain instructions) when the instruction starts at an even byte than when it starts at an odd byte. The effect of these differences is that to achieve the best results one needs to examine the starting byte of each instruction in a scenario (or at least those within the loop) and make adjustments in case of differences between a base scenario and a timing scenario. One may also need to modify both scenarios by adding one or more instructions or changing their positions to cause cancellation of the even-odd effects. The nature and magnitude of this problem were only realized after a number of runs were made, instruction counts were made for



portions of certain instructions, and relative comparisons were made. Making such even-odd corrections for a large number of scenarios would be quite time consuming.

In the case of the START and SUB commands, the present implementation reads at least the first two bytes of the Scenario name from the command for each Scenario Directory (SD) entry encountered. If 30 entries have to be compared and the scenario name starts at an odd byte in the command, the execution time for the command is 800 microseconds more than if the scenario name started at an even byte. (To force the even byte case, an odd number of blanks must occur after "START" and before the scenario name if the device name contains one digit.) To control this situation, the length and content of the SD as well as the location of the scenario name within a command must be controlled.

In the case of commands which contain device names, a further variation can arise. A total of 26.8 to 80.4 microseconds more will be used if the device name or "THIS" starts at an odd byte in a command. A total of 31 microseconds is used to convert each digit (after the two initial characters) in a device name. The execution time will further vary depending upon the number of Equipment Table (ET) entries which have to be searched. The number of entries searched will depend upon the ordering and linking of the ET entries and whether or not hierarchical equipment names are used in commands.

If the above factors are handled properly, one may be able to obtain relatively accurate results for the tests run. Certain additional factors need to be considered before applying the results. Of necessity, the tests were run under conditions whereby there was little competition for resources within the emulator. As the number of active, emulated devices increases, allocable core memory becomes splintered and those functions which must allocate and/or free core memory will use up more emulator CPU time. When a block is to be



freed, each link in the free chain which must be examined, uses up 7 microseconds of CPU time, and approximately the same amount of time is needed during allocation. If an average of 25 links needs to be examined, the cost is 175 microseconds for each allocate or free operation. Every command executed requires the allocation of a command buffer, freeing of the command buffer, and allocation of an error-message buffer for the response to the command and may also require the freeing of a previous error-message or query buffer. In addition, 6 or 7 instructions (see footnote 10) and one of the miscellaneous functions allocate and/or free core memory. There is no dynamic measure of the length of the free chain, but the timing tests probably only caused a free chain of five or ten links. Very little logging was done (from 2 to 6 records per run), but each record logged requires one allocation and one free operation (for a Register Stack).

Scenario management can also have a significant effect on individual execution times. If an instruction spans a scenario page boundary, it must be buffered and a new scenario page becomes the active page for the device. The cost of the latter operation is in the vicinity of 200 microseconds. If the new page must be read from disk, the cost is greater. When the number of active scenario pages in core approaches the number of core pages allocated, a disk read may be required for each scenario instruction fetched from a new page. The emulator is designed to cope with this situation to handle peak loading problems. If an emulator module operates in this mode more than a relatively small fraction of the time, it is overloaded and its load should be reduced.

The data given in Table XXI ignores the effect of any error conditions. The only error messages allowed for are the normal responses to commands.

The "Real-Time Clock", used for response timeouts, provides a continuous overhead estimated at between 0.2% (2000 microseconds per

second of elapsed time) and 0.3%. The effect of this overhead has been ignored in Table XXI in those cases in which emulator %CPU time was near 100% since the effect on a 700-microsecond instruction is only 1 or 2 microseconds. In those cases in which the % CPU time was lower (primarily some of the commands, the query instructions, the delay and wait instructions, and the miscellaneous functions), the emulator CPU times for the base scenario and the timing scenario were corrected for this overhead based on elapsed time, generally using a conservative 0.2% factor. This overhead is present throughout the elapsed time of a run, regardless of the amount of emulator activity.

#### NON-REAL TIME PROGRAMS

It is difficult to give anything but intelligent estimates as to the running times of the non-real-time programs. This is because of the many variables involved which determine execution times for each of the programs. Presented here is a sample problem for each program, with key characteristics defined, and approximate running times given. The times are based on an average derived from several runs of each program, and may vary within a 5 second range.

#### SSUB

The example shown in Appendix VI, Figure 33 shows a scenario called 34FORTN with macro calls not yet expanded. Figure 34 in Appendix VI shows the same scenario, now called FORTN, with macros expanded. The libraries which contain the macro definitions are given in Figure 35. The table below summarizes the key characteristics pertinent to the macro processing of this example. In this case, the macro processor takes about 20 seconds to complete execution.

number of libraries	2
number of macros in libraries	16
length of file without macros expanded	1172

length of file with macros expanded	3956
number of macro substitutions	185

#### MACDEF

The program used to generate macro libraries is MACDEF. The execution time of this program depends on characteristics summarized in the table below for the example shown in Appendix VI, Figure 35, the KAPLIB library.

number of definitions	3
length of input file	205
length of output file (.ML)	203

Execution time to create KAPLIB.ML from KAPLIB is 4 seconds.

#### CVT

The scenario assembler program may convert the FORTN scenario (Figure 34) into an internal scenario by using any of its three printing options. Average times for execution are 35 seconds for assembly with no listings (CVT/N option), 55 seconds for assembly with partial listings (CVT/P option), and 3 minutes 10 seconds for assembly with complete listings (CVT option). These times, of course, reflect to some degree, the speed of the printer. The table below summarizes the key characteristics pertinent to the Assembly of the example.

label definitions	22
other label references	24
queries	25
arithmetic instructions	133
search instructions	22
commands	3
assembler directives	3
other instructions	<u>159</u>
Total instructions	391
length, in bytes, of internal scenario	2435

## DATAR

The data reduction program processes the log tape written during an emulation run and can produce many combinations of listings and summaries. Execution times for all combinations are too cumbersome to be presented here. The table below describes the key characteristics pertinent to the data reduction of a log tape from a sample emulation of the Fortran Cost scenarios presented in Figures 33-35.

number of physical records	43
number of logical records	376
number of internal scenarios in directory	70
number of devices in ET	26
number of active devices	2
number of queries	123
number of responses	226
number of scenario instructions	1
number of lines of output for relative-time listing	660

Using such a log tape, the data reduction program produces a brief summary in 20 seconds and a relative-time listing for a single emulated device in an average of 4 minutes. These times are for processing of a file which is the first file on a log tape. If more than one emulation file is on a tape (perhaps a tape created by the MASTR program) the DATAR program rewinds to the beginning of tape and re-searches for the correct run every time it begins a new device listing for the run. This, of course, may consume considerably more time.

## MASTR

The execution time of the MASTR program depends on several factors, as described in the table below:

number of physical records in run disk space available file number of MASTR tape
--

Also included in the complete execution time is the length of time it takes the user to dismount the original log tape and mount the MASTR tape, for as many times as is needed to complete the transfer. Therefore it is unrealistic to give any meaningful timing estimates.

#### REFERENCES

1. Data General Corporation, Disk Operating System User's Manual, 093-000048-03, Southboro, Massachusetts, 1971.
2. Data General Corporation, NOVA Editing Routines, 093-000018-02, Southboro, Massachusetts, 1971.
3. Data General Corporation, File Check Program, 093-000071-00, Southboro, Massachusetts, 1971.
4. Data General Corporation, Tape Dump Program, 093-000059-01, Southboro, Massachusetts, 1971.
5. Data General Corporation, How to Use the NOVA Computers, Southboro, Massachusetts, 1971.



## APPENDIX I

### Conversion Codes for IBM 2741

Because some of the 2741 control characters do not have a direct counterpart in the ASCII character set, an exact mapping was not possible. Table XXII is a list of the 2741 control characters, and their position in the ASCII table. This same mapping was used in the 2741 conversion code tables used for the on-site model of the emulator.

Table XXIII represents the conversion codes used by the Scenario Assembler for 2741 EBCDIC odd parity code, with the parity bit as the right-most bit. The "lab" conversion is used on the fixed-site model of the Emulator when emulating an IBM 2741 terminal using Data General's software driven data communications multiplexor. The "field" conversion reverses the order of the bits, and is used on the on-site model of the Emulator when emulating an IBM 2741 terminal using Digital Computer Controls asynchronous line adapters.

Table XXII

## Control Characters for IBM 2741 Terminal

2741		ASCII	
Octal	Character	Octal	Character
037	EOT = control D	004	EOT = end-of-transmission
135	BS = backspace	010	BS = backspace
172	HT = horizontal tab	011	HT = horizontal tab
073	LF = line feed	012	LF = line feed
130	RES = restore	014	FF = form feed
133	NL = new line	015	CR = carriage return
034	UC = upper case	016	SO = shift out
174	LC = lower case	017	SI = shift in
031	PN = punch on	022	DC2 = device control 2
032	RS = reader stop	023	DC3 = device control 3
171	PF = punch off	024	DC4 = device control 4
136	IL = idle	026	SYN = synchronous idle
075	EOB = end-of-block	027	ETB = end-of-block
076	PRE = prefix	033	ESC = escape

Table XXIII  
Conversion Code Table used for IBM 2741 Terminal

ASCII CHARACTER	ASCII CODE	2741* LAB CODE	2741 FIELD CODE	ASCII CHARACTER	ASCII CODE	2741* LAB CODE	2741 FIELD CODE
NUL	000			SP	040	U 001	100
SOH	001			!	041	U 127	165
STX	002			"	042	U 026	064
ETX	003			#	043	L 026	064
EOT	004	C 037	174	\$	044	L 127	165
ENQ	005			%	045	U 013	150
ACK	006			&	046	L 141	103
BEL	007			'	047	U 015	130
BS	010	C 135	135	(	050	U 023	144
HT	011	C 172	057	)	051	U 025	124
LF	012	C 073	156	*	052	U 020	004
VT	013			+	053	U 141	103
FF	014	C 130	015	,	054	L 067	166
CR	015	C 133	155	-	055	L 100	001
SO	016	C 034	034	.	056	L 166	067
SI	017	C 174	037	/	057	L 043	142
DLE	020			0	060	L 025	124
DC1	021			1	061	L 002	040
DC2	022	C 031	114	2	062	L 004	020
DC3	023	C 032	054	3	063	L 007	160
DC4	024	C 171	117	4	064	L 010	010
NAK	025			5	065	L 013	150
SYN	026	C 136	075	6	066	L 015	130
ETB	027	C 075	136	7	067	L 016	070
CAN	030			8	070	L 020	004
EM	031			9	071	L 023	144
SUB	032			:	072	U 010	010
ESC	033	C 076	076	;	073	U 007	160
FS	034			<	074	U 004	020
GS	035			=	075	U 002	040
RS	036			>	076	U 016	070
VS	037			?	077	U 043	142
				@	100	L 040	002

\* C = control  
U = upper case  
L = lower case

Table XXIII  
Conversion Code Table used for IBM 2741 Terminal (Concluded)

<u>ASCII CHARACTER</u>	<u>ASCII CODE</u>	<u>2741 LAB CODE</u>	<u>2741 FIELD CODE</u>	<u>ASCII CHARACTER</u>	<u>ASCII CODE</u>	<u>2741 LAB CODE</u>	<u>2741 FIELD CODE</u>
A	101	U 142	043	a	141	L 142	043
B	102	U 144	023	b	142	L 144	023
C	103	U 147	163	c	143	L 147	163
D	104	U 150	013	d	144	L 150	013
E	105	U 153	153	e	145	L 153	153
F	106	U 155	133	f	146	L 155	133
G	107	U 156	073	g	147	L 156	073
H	110	U 160	007	h	150	L 160	007
I	111	U 163	147	i	151	L 163	147
J	112	U 103	141	j	152	L 103	141
K	113	U 105	121	k	153	L 105	121
L	114	U 106	061	l	154	L 106	061
M	115	U 111	111	m	155	L 111	111
N	116	U 112	051	n	156	L 112	051
O	117	U 114	031	o	157	L 114	031
P	120	U 117	171	p	160	L 117	171
Q	121	U 121	105	q	161	L 121	105
R	122	U 122	045	r	162	L 122	045
S	123	U 045	122	s	163	L 045	122
T	124	U 046	062	t	164	L 046	062
U	125	U 051	112	u	165	L 051	112
V	126	U 052	052	v	166	L 052	052
W	127	U 054	032	w	167	L 054	032
X	130	U 057	172	x	170	L 057	172
Y	131	U 061	106	y	171	L 061	106
Z	132	U 062	046	z	172	L 062	046
[	133	U 040	002	{	173		
\	134	U 166	067		174		
]	135			}	175		
↑	136	U 067	166	~	176		
←	137	U 100	001	DEL	177	L 177	177
•	140						

## APPENDIX II

### Sample Listings from Scenario Assembler

TEST

CONVERSION CODE = 1  
END-OF-MESSAGE CODE = 1

1	3	A	9
2	3	L	FL3
3	3	R	!!
4	6	S	FL3 CANDE
5	16	Q	MITRE/EMULATE
6	33	L	FL4
7	33	R	!!
8	36	S	FL4 LOGGED
9	47	A	6
10	52	+	13 0 R8
11	58	S	FILES
12	66	0	R8
13	70	0	
14	73	L	FL5
15	73	R	!!
16	76	S	FL5 #
17	82	Q	REMOVE
18	92	L	FL7
19	92	R	!!
20	95	S	FL7 #
21	101	Q	BYE
22	108	L	FL8
23	108	R	!!
24	111	S	FL8 ET
25	118	C	QUIT



TEST.IS

INDICATOR 0

CONVERSION CODE = 1

END-OF-MESSAGE CODE = 1

ALLOCATE 9

1 A 9

2 L FL3

3 R 11

3 1 0 3  
5 1 122 0

R

4 S FL3 CANDE

6 1 0 12  
8 1 123 0  
10 1 3 103  
12 1 101 116  
14 1 104 105

S  
C  
AN  
DE

5 0 MITRE/EMULATE

16 1 0 21  
18 1 121 115  
20 1 111 124  
22 1 122 105  
24 1 57 105  
26 1 115 125  
28 1 114 101  
30 1 124 105  
32 1 15 0

QM  
IT  
RE  
/E  
MU  
LA  
TE

6 L FL4

7 R 11

33 1 0 3  
35 1 122 0

R

8 S FL4 LOGGED

36 1 0 13  
38 1 123 0  
40 1 41 114  
42 1 117 107  
44 1 107 105  
46 1 104 0

S  
IL  
OG  
GE  
D

9 A 6

47 1 0 5  
49 1 101 0  
51 1 6 0

A

10 + .13 0 RB

52 I 0 6  
54 I 53 15  
56 I 0 10

+

11 5 FILES

58 I 0 10  
60 I 65 106  
62 I 111 114  
64 I 105 123

5F  
IL  
ES

12 0 R8

66 I 0 4  
68 I 134 10

0

13 0

70 I 0 3  
72 I 117 0

0

14 L FL5

15 R II

73 I 0 3  
75 I 122 0

R

16 S FL5 #

76 I 0 6  
78 I 123 0  
80 I 111 43

S  
IN

17 0 REMOVE

82 I 0 12  
84 I 121 122  
86 I 105 115  
88 I 117 125  
90 I 105 15

QR  
EM  
OV  
E

18 L FL7

19 R II

92 I 0 3  
94 I 122 0

R

20 S FL7 #

95 I 0 6  
97 I 123 0  
99 I 134 43

S  
ON

21 0 BYE

101 I 0 7  
103 I 121 102  
105 I 131 105

QB  
YE

107	1	15	0	
22	L	FL8		
23	R	11		
108	1	0	3	
110	1	122	0	R
24	S	FL8 ET		
111	1	0	7	
113	1	123	0	S
115	1	154	105	LE
117	1	124	0	T
25	C	QUIT		
118	1	0	10	
120	1	103	133	CI
122	1	121	125	QU
124	1	111	124	IT

SYMBOL TABLE  
NUMBER OF ENTRIES

5

LENGTH	LABEL	IS ADDRESS	ES LINE NO.
3	FL3	3	2
3	FL4	33	6
3	FL5	73	14
3	FL7	92	18
3	FL8	108	22

### APPENDIX III

Listing of EQUIP.RB

0001 EQUIP

```

      000101      ,TITL EQUIP
      000111      ,ENT E0000,E0,E1,ETREC
      000123      ,ENT ETEND
      000124      ,ENT ETENT
      000125      ,ENT E2
      000127      ,ENT ETLEN
      000128      ,DUSR A=101
      000129      ,DUSR I=111
      000130      ,DUSR S=123
      000131      ,DUSR T=124
      000132      ,DUSR U=125
      000133      ,DUSR W=127
      000134      ,DUSR E=105
      000135      ,DUSR Z=132
      000136      ,DUSR N=116
      000137      ,DUSR O=117
      000138      ,DUSR RT1=135,
      000139      ,DUSR BL1=7,
      000140      ,DUSR BL2=6,
      000141      ,DUSR PT1=0
      000142      ,DUSR PT2=N
      000143      ,DUSR DDDLINE=3+16,+4
      000144      ,DUSR IBM2848=3+16,+4
      000145      ,DUSR IBM2260=3+16,+4
      000146      ,DUSR IBM1053=5
      000147      ,DUSR D2000=6
      000148      ,DUSR IBM2741=7
      000149      ,DUSR I2741=3+16,+4
      000150      ,DUSR ZASC1=1+16,+1
      000151      ,DUSR ZASC6=1+16,+6
      000152      ,DUSR EASC2=2+16,+2
      000153      ,DUSR EASC5=2+16,+5
      000154      ,TXTH 5
      000155      ,ZREL
      000156      ,NREL
      000157      E0END=E0000
      000158      20000+"E
      000159      E9999=E0000+4
      000160      "H
      000161      ,+1
      000162      E0000:
      000163      E0:
      000164      0
      000165      "C+256,+*T
      000166      0,
      000167      0,
      000168      0,
      000169      E1
      000170      0
      000171      110,
      000172      0
      000173      EDM2
      000174      0
      000175      0
      000176      0
      000177      0
      000178      0+256,+37
      000179      ZASC6+256,+I
      000180      11+256,+10
      000181      0+256,+0
      000182      0+256,+106
      000183      ,ETRO
      000184      ,ETYP
      000185      ,ETIO
      000186      ,CHILD
      000187      ,LINK
      000188      ,PARNT
      000189      ,ETRAT
      000190      ,ETQBP
      000191      ,ETEDH
      000192      ,ETRSP
      000193      ,ETPAD
      000194      ,RRING,PRING
      000195      ,ETLGA,ETLGN
      000196      ,TERMT,STATI
      000197      ,PORTO,PORTI
      000198      ,SPRTO,SPRTI
      000199      ,SUTAD,ETIND
      000200      000025 ETEND:
      000201      00001020105 ETREC:
      000202      000021001046
      000203      000031000110
      000204      000041000005
      000205      0000051000000
      000206      000061041324
      000207      000071000000
      000208      000101000000
      000209      0001110000321
      000210      000121000000
      000211      000131000156
      000212      000141000000
      000213      0001510010571
      000214      000161000000
      000215      000171000000
      000216      000201000000
      000217      000211000037
      000218      000221013111
      000219      000231004410
      000220      000241000000
      000221      000251000200

```



0002 EQUIP  
 00026'004132  
 00027'000000  
 00030'000000  
 00031'000000

EWEND:  
 E1:

00032'000000  
 00033'042123  
 00034'000010  
 00035'000000  
 00036'000057  
 00037'000000  
 00040'000150  
 00041'000000  
 00042'001005  
 00043'000000  
 00044'000000  
 00045'000000  
 00046'000037  
 00047'021111  
 00050'024450  
 00051'000401  
 00052'007400  
 00053'004105  
 00054'000000  
 00055'000000  
 00056'000000

E2:

00057'000000  
 00060'052131  
 00061'000001  
 00062'000000  
 00063'000104  
 00064'000000  
 00065'000207  
 00066'000000  
 00067'001073  
 00070'000000  
 00071'000000  
 00072'000000  
 00073'000037  
 00074'032111  
 00075'021442  
 00076'000401  
 00077'017000  
 00100'003517  
 00101'020071  
 00102'000060  
 00103'001107

E3:

00104'000000  
 00105'052131  
 00106'000002  
 00107'000000  
 00110'000131  
 00111'000000  
 00112'000207  
 00113'000000  
 00114'001101

0,\*256,\*Z  
 1,-1\*102+007+0  
 1,-1\*102+007+0  
 0

0  
 "D\*256,\*"S  
 14.  
 0  
 E2  
 0  
 0110.  
 0  
 EOM3  
 0  
 0  
 0  
 0\*256,\*37  
 EASC2\*256,\*I  
 51\*256,\*50  
 1\*256,\*1  
 15,\*256,\*008  
 0,\*256,\*E  
 1,-1\*102+007+0  
 1,-1\*102+007+0  
 0

0  
 "T\*256,\*"Y  
 1.  
 0  
 E3  
 0  
 RT1  
 0  
 EOM4  
 0  
 0  
 0  
 0\*256,\*37  
 I2741\*256,\*I  
 43\*256,\*42  
 1\*256,\*1  
 30,\*256,\*008  
 BL1\*256,\*PT1  
 2,-1\*102+00,07+71  
 4,-1\*102+00,07+66  
 0060A

0  
 "T\*256,\*"Y  
 2.  
 0  
 E4  
 0  
 RT1  
 0  
 EOM5

!BYTEL, PARTY  
 !ETDIO  
 !ETDOO  
 !ETDOA

!ETR0  
 !ETYP  
 !ETID  
 !CHILD  
 !LINK  
 !PARNT  
 !ETRAT  
 !ETQBP  
 !ETEO  
 !ETRSP  
 !ETPAD  
 !RRING,PRING  
 !ETLGA, ETLGN  
 !TERMT, STATI  
 !PORTO, PORTI  
 !SPRTO, SPRTI  
 !SUTAD, ETIND  
 !BYTEL, PARTY  
 !ETDIO  
 !ETDOO  
 !ETDOA

!ETR0  
 !ETYP  
 !ETID  
 !CHILD  
 !LINK  
 !PARNT  
 !ETRAT  
 !ETQBP  
 !ETEO  
 !ETRSP  
 !ETPAD  
 !RRING,PRING  
 !ETLGA, ETLGN  
 !TERMT, STATI  
 !PORTO, PORTI  
 !SPRTO, SPRTI  
 !SUTAD, ETIND  
 !BYTEL, PARTY  
 !ETDIO  
 !ETDOO  
 !ETDOA

!ETR0  
 !ETYP  
 !ETID  
 !CHILD  
 !LINK  
 !PARNT  
 !ETRAT  
 !ETQBP  
 !ETEO

## 0003 EQUIP

001151000000  
001161000000  
001171000000  
001201000037  
001211032111  
001221021442  
001231001002  
001241017400  
001251003517  
001261021071  
001271062066  
001301001107

E41

001311000000  
001321052131  
001331000003  
001341000000  
001351000156  
001361000000  
001371000207  
001401000000  
001411001051  
001421000000  
001431000000  
001441000000  
001451000037  
001461032111  
001471021442  
001501001403  
001511020000  
001521003517  
001531022071  
001541064066  
001551001107

E4A1

001561000000  
001571052131  
001601000004  
001611000000  
001621000203  
001631000000  
001641000207  
001651000000  
001661001051  
001671000000  
001701000000  
001711000000  
001721000037  
001731032111  
001741021442  
001751002004  
001761016400  
001771003517  
002001023071  
002011066066  
002021001107

E131

002031000000  
002041052131

0  
0  
0  
0+256,+37  
I2741+256,+I  
43+256,+42  
2+256,+2  
31,+256,+088  
BL1+256,+PT1  
2,-1+182+02,B7+71  
4,-1+182+04,B7+66  
0066A

0  
"T+256,+Y  
3,  
0  
E4A  
0  
RT1  
0  
EOM1  
0  
0  
0  
0+256,+37  
I2741+256,+I  
43+256,+42  
3+256,+3  
32,+256,+088  
BL1+256,+PT1  
2,-1+182+04,B7+71  
4,-1+182+08,B7+66  
0066A

ETRSP  
ETPAD  
IRRING,PRING  
ETLGA,ETLGN  
ITERMT,STATI  
PORTO,PORTI  
SPRTO,SPRTI  
SUTAD,ETIND  
BYTEL,PARTY  
ETDIO  
ETDOO  
ETDOA

ETRO  
ETYPE  
ETIO  
CHIL0  
LINK  
PARNT  
ETRAT  
ETQBP  
ETEOM  
ETRSP  
ETPAD  
IRRING,PRING  
ETLGA,ETLGN  
ITERMT,STATI  
PORTO,PORTI  
SPRTO,SPRTI  
SUTAD,ETIND  
BYTEL,PARTY  
ETDIO  
ETDOO  
ETDOA

ETRO  
ETYPE  
ETIO  
CHIL0  
LINK  
PARNT  
ETRAT  
ETQBP  
ETEOM  
ETRSP  
ETPAD  
IRRING,PRING  
ETLGA,ETLGN  
ITERMT,STATI  
PORTO,PORTI  
SPRTO,SPRTI  
SUTAD,ETIND  
BYTEL,PARTY  
ETDIO  
ETDOO  
ETDOA

ETRO  
ETYPE

0004 EQUIP  
 00205'0000005  
 00206'0000000  
 00207'000230'  
 00210'0000000  
 00211'000207  
 00212'0000000  
 00213'001051'  
 00214'0000000  
 00215'0000000  
 00216'0000000  
 00217'000037  
 00220'032111  
 00221'021442  
 00222'002405  
 00223'020400  
 00224'003517  
 00225'024071  
 00226'070066  
 00227'001110'

E14:

00230'0000000  
 00231'052131  
 00232'0000006  
 00233'0000000  
 00234'000255'  
 00235'0000000  
 00236'000207  
 00237'0000000  
 00240'001051'  
 00241'0000000  
 00242'0000000  
 00243'0000000  
 00244'000037  
 00245'032111  
 00246'021442  
 00247'003006  
 00250'021000  
 00251'003517  
 00252'025071  
 00253'072066  
 00254'001110'

E15:

00255'0000000  
 00256'052131  
 00257'0000007  
 00260'0000000  
 00261'000302'  
 00262'0000000  
 00263'000207  
 00264'0000000  
 00265'001051'  
 00266'0000000  
 00267'0000000  
 00270'0000000  
 00271'000037  
 00272'032111  
 00273'021442  
 00274'003407  
 00275'021400

5,  
 0  
 E14  
 0  
 RT1  
 0  
 EOM1  
 0  
 0  
 0  
 0+256,+37  
 12741+256,+1  
 43+256,+42  
 5+256,+5  
 33,+256,+088  
 8L1+256,+PT1  
 2,-1+182+08,87+71  
 4,-1+182+16,87+66  
 00668

0  
 "T+256,+"Y  
 0,  
 0  
 E15  
 0  
 RT1  
 0  
 EOM1  
 0  
 0  
 0  
 0+256,+37  
 12741+256,+1  
 43+256,+42  
 6+256,+6  
 34,+256,+088  
 8L1+256,+PT1  
 2,-1+182+10,87+71  
 4,-1+182+20,87+66  
 00668

0  
 "T+256,+"Y  
 7,  
 0  
 E16  
 0  
 RT1  
 0  
 EOM1  
 0  
 0  
 0  
 0+256,+37  
 12741+256,+1  
 43+256,+42  
 7+256,+7  
 35,+256,+088

JETIO  
 ICHILO  
 I LINK  
 I PARNT  
 IETRAT  
 IETQBP  
 IETEOM  
 IETRSP  
 IETPAD  
 IRRING,PRING  
 IETLGA,ETLGN  
 ITERM,STATI  
 IPORTO,PORTI  
 ISPRTO,SPRTI  
 ISUTAD,ETINO  
 IBYTEL,PARTY  
 IETDIO  
 IETOOD  
 IETDOA

JETRU  
 IETTYPE  
 IETIO  
 ICHILO  
 I LINK  
 I PARNT  
 IETRAT  
 IETQBP  
 IETEOM  
 IETRSP  
 IETPAD  
 IRRING,PRING  
 IETLGA,ETLGN  
 ITERM,STATI  
 IPORTO,PORTI  
 ISPRTO,SPRTI  
 ISUTAO,ETINO  
 IBYTEL,PARTY  
 IETDIO  
 IETOOD  
 IETDOA

JETRU  
 IETTYPE  
 IETIO  
 ICHILO  
 I LINK  
 I PARNT  
 IETRAT  
 IETQBP  
 IETEOM  
 IETRSP  
 IETPAD  
 IRRING,PRING  
 IETLGA,ETLGN  
 ITERM,STATI  
 IPORTO,PORTI  
 ISPRTO,SPRTI  
 ISUTAO,ETINO

0005 EQUIP  
00276'003517  
00277'026071  
00300'074066  
00301'001110'

E16:

00302'000000  
00303'052131  
00304'000010  
00305'000000  
00306'000327'  
00307'000000  
00310'000207  
00311'000000  
00312'001051'  
00313'000000  
00314'000000  
00315'000000  
00316'000037  
00317'032111  
00320'021442  
00321'004010  
00322'022000  
00323'003517  
00324'027071  
00325'076066  
00326'001110'

E17:

00327'000000  
00330'052131  
00331'000011  
00332'000000  
00333'000354'  
00334'000000  
00335'000207  
00336'000000  
00337'001051'  
00340'000000  
00341'000000  
00342'000000  
00343'000037  
00344'032111  
00345'022444  
00346'000401  
00347'022400  
00350'003517  
00351'030071  
00352'060067  
00353'001111'

E18:

00354'000000  
00355'052131  
00356'000012  
00357'000000  
00360'000401'  
00361'000000  
00362'000207  
00363'000000  
00364'001051'  
00365'000000

BL1\*256,+PT1  
2,-1\*182+12,87+71  
4,-1\*182+24,87+66  
DD66B

0  
"T\*256,+\*Y  
8,  
0  
E17  
0  
RT1  
0  
EDM1  
0  
0  
0  
0\*256,+37  
I2741\*256,+I  
43\*256,+42  
8,\*256,+8,  
36,\*256,+0BB  
BL1\*256,+PT1  
2,-1\*182+14,87+71  
4,-1\*182+28,87+66  
DD66B

IBYTEL, PARTY  
JETDIO  
JETDOD  
JETDDA

JETRO  
JETYPE  
JETID  
JCHILD  
JLINK  
JPART  
JETRAT  
JETQBP  
JETEDM  
JETRSP  
JETPAD  
JRRING,PRING  
JETLGA,ETLGN  
JTERMT,STATI  
JPORTO,PORTI  
JSPRTO,SPRTI  
JSUTAD,ETIND  
IBYTEL, PARTY  
JETDIO  
JETDOD  
JETDDA

0  
"T\*256,+\*Y  
9,  
0  
E18  
0  
RT1  
0  
EOM1  
0  
0  
0  
0\*256,+37  
I2741\*256,+I  
45\*256,+44  
1\*256,+1  
37,\*256,+0BB  
BL1\*256,+PT1  
2,-1\*182+16,87+71  
4,-1\*182+00,87+67  
DD67A

JETRO  
JETYPE  
JETID  
JCHILD  
JLINK  
JPART  
JETRAT  
JETQBP  
JETEDM  
JETRSP  
JETPAD  
JRRING,PRING  
JETLGA,ETLGN  
JTERMT,STATI  
JPORTO,PORTI  
JSPRTO,SPRTI  
JSUTAD,ETIND  
IBYTEL, PARTY  
JETDIO  
JETDOD  
JETDDA

0  
"T\*256,+\*Y  
10,  
0  
E19  
0  
RT1  
0  
EDM1  
0

JETRO  
JETYPE  
JETID  
JCHILD  
JLINK  
JPART  
JETRAT  
JETQBP  
JETEDM  
JETRSP

0000 EQUIP  
 00366'000000  
 00367'000000  
 00370'000037  
 00371'032111  
 00372'022444  
 00373'001002  
 00374'023000  
 00375'003517  
 00376'031071  
 00377'062067  
 00400'001111'

E19:

00401'000000  
 00402'052131  
 00403'000013  
 00404'000000  
 00405'000426'  
 00406'000000  
 00407'000207  
 00410'000000  
 00411'001051'  
 00412'000000  
 00413'000000  
 00414'000000  
 00415'000037  
 00416'032111  
 00417'022444  
 00420'001403  
 00421'023400  
 00422'003517  
 00423'032071  
 00424'064067  
 00425'001111'

E20:

00426'000000  
 00427'052131  
 00430'000014  
 00431'000000  
 00432'000453'  
 00433'000000  
 00434'000207  
 00435'000000  
 00436'001051'  
 00437'000000  
 00440'000000  
 00441'000000  
 00442'000037  
 00443'032111  
 00444'022444  
 00445'002004  
 00446'024000  
 00447'003517  
 00450'033071  
 00451'066067  
 00452'001111'

E21:

00453'000000  
 00454'052131  
 00455'000015

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 0  
 0+256,+37  
 I2741+256,+I  
 45+256,+44  
 2+256,+2  
 38,+256,+088  
 BL1+256,+PT1  
 2,-1+182+18,87+71  
 4,-1+182+04,87+67  
 0067A

JETPAD  
 JRRING,PRING  
 JETLGA,ETLGN  
 JTERM,STATI  
 JPORTO,PORTI  
 JSPRTO,SPRTI  
 JSUTAO,ETIND  
 JBYTEL,PARTY  
 JETOIO  
 JETODD  
 JETDDA

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 "T+256,+Y  
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 E20  
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 RT1  
 0  
 EDM1  
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 0  
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 I2741+256,+I  
 45+256,+44  
 3+256,+3  
 39,+256,+088  
 BL1+256,+PT1  
 2,-1+182+20,87+71  
 4,-1+182+08,87+67  
 0067A

JETRU  
 JETYPE  
 JETID  
 JCHILU  
 JLINK  
 JPARNT  
 JETRAT  
 JETQBP  
 JETEOM  
 JETRSP  
 JETPAO  
 JRRING,PRING  
 JETLGA,ETLGN  
 JTERM,STATI  
 JPORTO,PORTI  
 JSPRTO,SPRTI  
 JSUTAD,ETIND  
 JBYTEL,PARTY  
 JETOIO  
 JETDOD  
 JETDUA

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 "T+256,+Y  
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 E21  
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 RT1  
 0  
 EDM1  
 0  
 0  
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 0+256,+37  
 I2741+256,+I  
 45+256,+44  
 4+256,+4  
 40,+256,+088  
 BL1+256,+PT1  
 2,-1+182+22,87+71  
 4,-1+182+12,87+67  
 DD67A

JETRU  
 JETYPE  
 JETID  
 JCHILU  
 JLINK  
 JPARNT  
 JETRAT  
 JETQBP  
 JETEOM  
 JETRSP  
 JETPAO  
 JRRING,PRING  
 JETLGA,ETLGN  
 JTERM,STATI  
 JPORTO,PORTI  
 JSPRTO,SPRTI  
 JSUTAD,ETIND  
 JBYTEL,PARTY  
 JETOIO  
 JETDOD  
 JETDUA

0  
 "T+256,+Y  
 13,

JETRU  
 JETYPE  
 JETID



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00460'000000  
00461'000207  
00462'000000  
00463'001051!  
00464'000000  
00465'000000  
00466'000000  
00467'000037  
00470'032111  
00471'022444  
00472'002405  
00473'024400  
00474'003517  
00475'034071  
00476'070067  
00477'001112!

E22:

00500'000000  
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00503'000000  
00504'000525'  
00505'000000  
00506'000207  
00507'000000  
00510'001051'  
00511'000000  
00512'000000  
00513'000000  
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00517'003006  
00520'025000  
00521'003517  
00522'035071  
00523'072067  
00524'001112'

E23:

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00526'052131  
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00530'000000  
00531'000552  
00532'000000  
00533'000207  
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00536'000000  
00537'000000  
00540'000000  
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00542'032111  
00543'022444  
00544'003407  
00545'025400  
00546'003517

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E22  
0  
RT1  
0  
EDM1  
0  
0  
0  
0+256,+37  
I2741+256,+1  
45+256,+44  
5+256,+5  
41,+256,+088  
BL1+256,+PT1  
2,-1+182+24,B7+71  
4,-1+182+16,B7+67  
00678

```

0
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14,
0
E23
0
RT1
0
EOM1
0
0
0
0*256,+37
I2741*256,+I
45*256,+44
6*256,+6
42,*256,+088
BL1*256,+PT1
2,*1*182+26,B7+71
4,*1*182+20,B7+67
D067B

```

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"T+256,+\*Y  
15,  
0  
E24  
0  
RT1  
0  
EOM1  
0  
0  
0  
0+256,+37  
12741+256,+I  
45+256,+44  
7+256,+7  
43,+256,+088  
8L1+256,+PT1

```

,CHILD
,LINK
,PARNT
,ETRAT
,ETQBP
,ETEQM
,ETRSP
,ETPAD
,RRING,PRING
,ETLGA,ETLGN
,ITERM,STATI
,PORTO,PORTI
,SPRTO,SPRTI
,SUTAO,ETINO
,BYTEL,PARTY
,ETDID
,ETDDO
,ETODD

```

```

,ETRO
,ETYPE
,ETID
,CHILD
,LINK
,PARNT
,ETRAT
,ETQBP
,ETEOM
,ETRSP
,ETPAO
,RRING,PRING
,ETLGA,ETLGN
,TERMT,STATI
,PORTD,PORTI
,SPRTD,SPRTI
,SUTAD,ETIND
,BYTEL,PARTY
,ETOID
,ETDOD
,ETODA

```

```

,ETRB
,ETYPE
,ETID
,CHILD
,LINK
,PARNT

,ETRAT

,ETQBP
,ETEOM
,ETRSP
,ETPAD
,RRING,PRING
,ETLGA,ETLGN
,TERMT, STATI
,PORTD, PORTI
,SPRTD, SPRTI

,ISUTAO, ETIND
,BYTEL, PARTY

```



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005471036071	2,-1*182+28,87+71		
005501074067	4,-1*182+24,87+67	JETDUD	
0055110011121	DD678	JETDOA	
E241			
005521000000	0	JETRO	
005531052131	"T*256,+ "Y	JETYPE	
005541000020	16,	JETID	
005551000000	0	JCHILD	
0055610005771	E5	JLINK	
005571000000	0	JPARNT	
005601000207	RT1		JETRAT
005611000000	0	JETQBP	
0056210010511	EDM1	JETEOM	
005631000000	0	JETRSP	
005641000000	0	JETPAD	
005651000000	0	JRRING,PRING	
005661000037	0*256,+37	JETLGA, ETLGN	
005671032111	12741+256,+I	JTERMT, STATI	
005701022444	45*256,+44	JPORTD, PORTI	
005711004010	8,+256,+8,	JSPRTD, SPRTI	
005721026000	44,+256,+088		JUTAD, ETIND
005731003517	8L1+256,+PT1	JBYTEL, PARTY	
005741037071	2,-1*182+30,87+71	JETDID	
005751076067	4,-1*182+28,87+67	JETDOD	
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005811000005	5,	JETID	
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005831000000	0	JLINK	
005841000000	0	JPARNT	
005851004540	2400,		JETRAT
005861000000	0	JETQBP	
0058710010511	EOM1	JETEOM	
005881000000	0	JETRSP	
005891000000	0	JETPAD	
005901000000	0	JRRING,PRING	
005911000037	0*256,+37	JETLGA, ETLGN	
005921032111	DDDLIN*256,+I	JTERMT, STATI	
005931015031	32*256,+31	JPORTD, PORTI	
005941000000	0*256,+0	JSPRTD, SPRTI	
005951025400	43,+256,+088		JUTAD, ETIND
005961004116	8L2+256,+PT2	JBYTEL, PARTY	
005971000000	1,-1*182+087+0	JETDID	
005981000000	1,-1*182+087+0	JETDOD	
005991000000	0	JETDOA	
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006031041516	"C*256,+ "N	JETYPE	
006041000000	6,	JETID	
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0060610000511	E7	JLINK	
0060710005771	E5	JPARNT	
006081004540	2400,		JETRAT
006091000000	0	JETQBP	
0061010010511	EDM1	JETEOM	
006111000000	0	JETRSP	
006121000000	0	JETPAD	

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 00644'047000  
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 00647'000000  
 00650'000000

E7:

00651'000000  
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 00653'000007  
 00654'000775'  
 00655'000000  
 00656'000577'  
 00657'004540  
 00660'000000  
 00661'001051'  
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 00664'000000  
 00665'000037  
 00666'032125  
 00667'015031  
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E8:

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 00717'004116  
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 00722'000000

E9:

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 00726'000000

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 32+256,+31  
 0+256,+0  
 116+256,+088  
 BL2+256,+PT2  
 1,-1+182+087+0  
 1,-1+182+087+0  
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 "C+256,+ "N  
 7,  
 E11  
 0  
 E5  
 2400,  
 0  
 EDM1  
 0  
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 0  
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 IBM2848+256,+U  
 32+256,+31  
 0+256,+0  
 250+256,+088  
 BL2+256,+PT2  
 1,-1+182+087+0  
 1,-1+182+087+0  
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IRRING,PRING  
 IETLGA,ETLGN  
 ITERM,STATI  
 IPORTO,PORTI  
 ISPRTO,SPRTI  
 ISUTAO,ETIND  
 IBYTEL,PARTY  
 IETOIO  
 IETODO  
 IETDOA

IETRO  
 IETYPE  
 IETID  
 ICHILD  
 ILINK  
 IPARNT  
 IETRAT  
 IETQBP  
 IETEOM  
 IETRSP  
 IETPAO  
 IRRING,PRING  
 IETLGA,ETLGN  
 ITERM,STATI  
 IPDRTO,PORTI  
 ISPRTO,SPRTI  
 ISUTAO,ETIND  
 IBYTEL,PARTY  
 IETDIO  
 IETDDO  
 IETDDA

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 IETYPE  
 IETID  
 ICHILD  
 ILINK  
 IPARNT  
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 IETQBP  
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 IETLGA,ETLGN  
 ITERM,STATI  
 IPORTO,PORTI  
 ISPRTO,SPRTI  
 ISUTAD,ETIND  
 IBYTEL,PARTY  
 IETOIO  
 IETDOD  
 IETDOA

IETRO  
 IETYPE  
 IETIO  
 ICHILD

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 00731'0004540  
 00732'000000  
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 00734'000000  
 00735'000000  
 00736'000000  
 00737'000037  
 00740'032111  
 00741'0015031  
 00742'000000  
 00743'120400  
 00744'004116  
 00745'000000  
 00746'000000  
 00747'000000

E10  
 E6  
 2400.  
 0  
 EDM1  
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 0  
 0  
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 IBM2260\*256,+I  
 32\*256,+31  
 0\*256,+0  
 241\*256,+088  
 BL2\*256,+PT2  
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 /ETEOM  
 /ETRSP  
 /ETPAD  
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 /ETLGA,ETLGN  
 /TERMT,STATI  
 /PORTO,PORTI  
 /SPRTO,SPRTI  
 /SUTAD,ETIND  
 /BYTEL,PARTY  
 /ETDID  
 /ETDOD  
 /ETDOA

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 00766'0015000  
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 00770'121000  
 00771'004116  
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 00773'000000  
 00774'000000

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 "P\*256,+"T  
 10.  
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 E6  
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 EDM1  
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 32\*256,+0  
 0\*256,+0  
 242\*256,+088  
 BL2\*256,+PT2  
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 1,-1\*182+087+0  
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 /PARNT  
 /ETRAT  
 /ETQBP  
 /ETEOM  
 /ETRSP  
 /ETPAD  
 /RRING,PRING  
 /ETLGA,ETLGN  
 /TERMT,STATI  
 /PORTO,PORTI  
 /SPRTO,SPRTI  
 /SUTAD,ETIND  
 /BYTEL,PARTY  
 /ETDID  
 /ETDOD  
 /ETDOA

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 00777'000013  
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 01002'000651'  
 01003'0004540  
 01004'000000  
 01005'001051'  
 01006'000000  
 01007'000000  
 01010'000000  
 01011'000037  
 01012'032125  
 01013'0015031  
 01014'000000  
 01015'122000  
 01016'004116  
 01017'000000

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 "D\*256,+"S  
 11.  
 0  
 E12  
 E7  
 2400.  
 0  
 EOM1  
 0  
 0  
 0  
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 IBM2260\*256,+U  
 32\*256,+31  
 0\*256,+0  
 244\*256,+088  
 BL2\*256,+PT2  
 1,-1\*182+087+0

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 /ETID  
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 /LINK  
 /PARNT  
 /ETRAT  
 /ETQBP  
 /ETEOM  
 /ETRSP  
 /ETPAD  
 /RRING,PRING  
 /ETLGA,ETLGN  
 /TERMT,STATI  
 /PORTO,PORTI  
 /SPRTO,SPRTI  
 /SUTAO,ETIND  
 /BYTEL,PARTY  
 /ETDID

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01021'000000	0	/ETDOA	
01022'000000	0	/ETRU	
01023'042123	"D*256,+ "S	/ETYPE	
01024'000014	12,	/ETID	
01025'000000	0	/CHIL0	
01026'000000	0	/LINK	
01027'000051'	E7	/PARNT	
01030'004540	2400,		/ETRAT
01031'000000	0	/ETQBP	
01032'001051'	EOM1	/ETEOM	
01033'000000	0	/ETRSP	
01034'000000	0	/ETPAD	
01035'000000	0	/RRING, PRING	
01036'000037	0+256,+37	/ETLGA, ETLGN	
01037'032125	IBM2260+256,+U	/TERM, STATI	
01040'015031	32+256,+31	/PORTO, PORTI	
01041'000000	0+256,+0	/SPRTO, SPRTI	
01042'122400	245+256,+088	/SUTAD, ETIND	
01043'004110	8L2+256,+PT2	/BYTEL, PARTY	
01044'000000	1,-1*1B2+0B7+0	/ETDID	
01045'000000	1,-1*1B2+0B7+0	/ETDOD	
01046'000000	0	/ETDOA	
	E9999:		
000025	LEN	=E0END-E0000	
01047'000032	ETENT:	E9999-E0000/LEN	
01050'000025	ETLEN:	LEN	
01051'000037	EOM1:	37	
01052'177777		-1	
01053'177777		-1	
01054'177777		-1	
01055'177777		-1	
01056'177777		-1	
01057'000012	EOM2:	12	
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01061'000030		30	
01062'177777		-1	
01063'177777		-1	
01064'177777		-1	
01065'177777	EOM3:	-1	
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01067'177777		-1	
01070'177777		-1	
01071'177777		-1	
01072'177777		-1	
01073'000037	EOM4:	37	
01074'177777		-1	
01075'177777		-1	
01076'177777		-1	
01077'177777		-1	
01100'177777		-1	
01101'000037	EOM5:	37	
01102'000043		43	
01103'177777		-1	
01104'177777		-1	
01105'177777		-1	
01106'177777		-1	
01107'000000	D066A:	0	

```
0012  EQUIP
01110'000000 D066B1 0
01111'000000 D067A1 0
01112'000000 D067B1 0
      .END
```

0013	EQUIP
0066A	001107'
0066B	001110'
0067A	001111'
0067B	001112'
E0	000005'
E0000	000005'
E0END	000032'
E1	000032'
E10	000750'
E11	000775'
E12	001022'
E13	000203'
E14	000230'
E15	000255'
E16	000302'
E17	000327'
E18	000354'
E19	000401'
E2	000057'
E20	000426'
E21	000453'
E22	000500'
E23	000525'
E24	000552'
E3	000104'
E4	000131'
E4A	000156'
E5	000577'
E6	000624'
E7	000651'
E8	000676'
E9	000723'
E9999	001047'
EOM1	001051'
EOM2	001057'
EOM3	001065'
EOM4	001073'
EOM5	001101'
ETEND	000000'
ETENT	001047'
ETLEN	001050'
ETREC	000001'
LEN	000025'



APPENDIX IV  
DATAR Listings

USER INPUT: DATAR/B)

BRIEF SUMMARY OF RUN FT7

PAGE 1

DEV ERROR MESSAGE:

CTW 020 ACTION TAKEN  
TY15 020 ACTION TAKEN  
TY10 114 DEVICE STOPPED  
TY15 020 ACTION TAKEN  
CTW 020 ACTION TAKEN  
TY10 020 ACTION TAKEN  
TY10 020 ACTION TAKEN  
TY10 040 BEHIND SCHEDULE

BRIEF SUMMARY OF RUN FT7

PAGE 2

TERMINALS: 3

ELAPSED TIME: 29.79358 MESSAGES: 135 RECORDS: 14

CHARACTERS: TOTAL: 172 R: 86 Q: 86 UN-R: 0

RECORD TYPES: M: 3 S: 110 Q: 5  
R: 5 C: 7 E: 0 UNSOLICITED: 0

TIMES: AVG RESP: 6.66 MAX RESP: 30.82 TERMINAL-MAX: TY10  
PERCENT CPU: 1.89 TOTAL CPU: 56569

SCENARIO INSTRUCTIONS USED:

B:	4	B:	2	O:	23	I:	3	I:	4
M:	1	C:	4	D:	22	J:	21	O:	2
R:	6	W:	1	D:	8	L:	2	Q:	3
R:	4								

COMMANDS ISSUED:

QUIT: 1 1 START: 1 3 SUB: 1 3  
END-OF-FILE

Figure 26. Brief Summary Output Format

USER INPUT:      DATED: 01/53

DEVICES REQUESTED ARE:      PAGE    1

1    CTA  
3    TY1  
5    TY3  
17    TY15  
14    TY16  
19    CUB

---

DETAILED SUMMARY OF RUN #12      PAGE    2

TERMINAL IDENTIFICATION:    CTA

REC'D TYPE:    RI    3    SI    4    OI    8  
                 RI    0    CI    3    EI    2    UNSOLICITED:    0

TIME:    0000    HRS    00    MIN    00

COMMANDS ISSUED:

START    1    1    STANT    1    2

---

DETAILED SUMMARY OF RUN #12      PAGE    3

TERMINAL IDENTIFICATION:    TY15

REC'D TYPE:    RI    3    SI    70    OI    2  
                 RI    3    CI    2    EI    2    UNSOLICITED:    0

TIME:    0000    HRS    00    MIN    00

SCENARIO INSTRUCTIONS USED:

RI    4    0    1    17    1    1    1    0    1    1  
CI    2    0    1    20    1    10    0    1    2    3  
OI    1    0    1    2    0    1    4    0    1    3

COMMANDS ISSUED:

START    1    1    SUB    1    1

---

DETAILED SUMMARY OF RUN #12      PAGE    4

TERMINAL IDENTIFICATION:    TY16

REC'D TYPE:    RI    3    SI    34    OI    3  
                 RI    2    CI    2    EI    4    UNSOLICITED:    0

TIME:    0000    HRS    00    MIN    00

SCENARIO INSTRUCTIONS USED:

RI    2    0    1    0    1    2    1    1    3    0    1    2  
OI    2    0    1    0    1    3    0    1    1    0    1    7

COMMANDS ISSUED:

SUB    1    2

---

FOLLOWING DEVICES ARE INACTIVE:      PAGE    5

3    TY1  
5    TY3  
19    CUB  
END-OF-FILE

Figure 27. Detailed Summary Output Format

RESPONSES FOR DATA 4-2 1  
 INTERVAL COUNT PERCENTAGE

0.001 TO 0.750	1	24	54.55
0.750 TO 1.500	1	14	31.11
1.500 TO 2.250	1	1	2.22
2.250 TO 3.000	1	1	2.22
3.000 TO 3.750	1	1	2.22
3.750 TO 4.500	1	4	9.09
4.500 TO 5.250	1	4	9.09
5.250 TO 6.000	1	4	9.09
6.000 TO 6.750	1	1	2.22
6.750 TO 7.500	1	2	4.44
7.500 TO 8.250	1	2	4.44
8.250 TO 9.000	1	2	4.44
9.000 TO 9.750	1	1	2.22
9.750 TO 10.500	1	1	2.22
10.500 TO 11.250	1	1	2.22
11.250 TO 12.000	1	1	2.22
12.000 TO 12.750	1	1	2.22
12.750 TO 13.500	1	1	2.22
13.500 TO 14.250	1	1	2.22

PAGE 4

PLT OF RESPONSE PERCENTAGES FOR DATA 4-2 1

PAGE 5

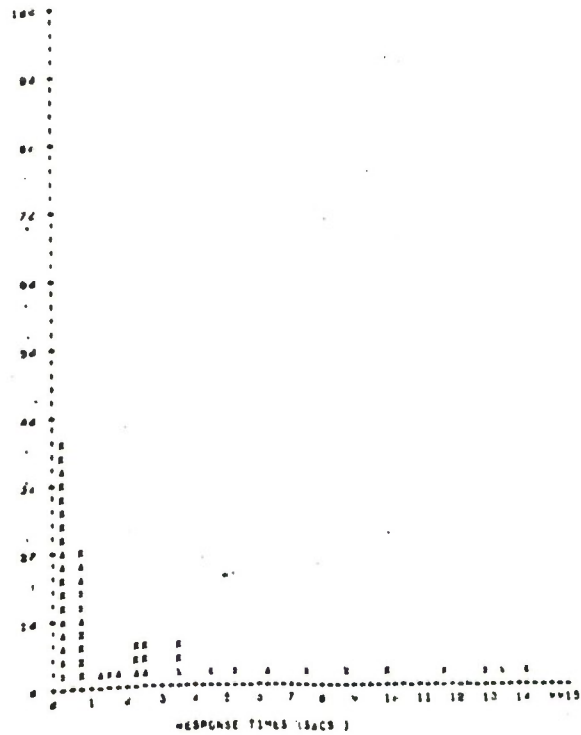


Figure 28. Histogram Output Format

```

USER INPUT:  DATAR/L/T/O/N)
DEVICES REQUESTED ARE I
PAGE 1

1 CT0
2 DS14
3 TY15
4 TY16

LOGICAL RECORDS 1-1 TO 114
TERMINAL: CT0
P (None for CT0)

LOGICAL RECORDS 1-1 TO 114
TERMINAL: TY15

103
242 002 001 027 000 122 000 056 272 144 000 000 071 300 000 000
072 037 000 000 072 300 000 040 000 000 052 263 055 070 000 230
020 034 121 051 153 122 001 001 174 020 013 133 037 000

104
040 001 000 022 000 123 000 000 000 000 000 000 000 000 000
073 057 000 000 074 031 000 000 000 052 263 055 070 000 230
000 000 102 011

105
040 001 000 024 000 123 000 000 000 000 000 000 000 000 000
074 122 000 000 075 151 000 000 000 052 263 055 070 000 242
000 007 040 000 003 011 012 000

106
040 001 000 023 000 123 000 000 000 000 000 000 000 000 000
075 244 000 000 076 330 000 000 000 052 263 055 070 000 251
000 005 104 000 012 000

107
044 004 000 033 000 121 000 000 100 053 000 002 277 303 000 000
077 200 000 002 300 104 000 000 000 052 263 055 070 000 250
020 034 045 040 142 112 150 142 122 150 001 122 153 045 117 114
112 045 153 133 037 000

108
040 001 000 034 000 123 000 000 000 000 000 000 000 000 000
077 030 000 002 301 105 000 000 000 052 263 055 070 000 250
000 032 121 020 034 045 040 142 112 150 142 122 150 001 122 153
045 117 114 112 045 153 133 037 000

LOGICAL RECORDS 1-1 TO 114
TERMINAL: TY16

101
044 004 000 027 000 121 000 050 233 343 000 000 033 335 000 050
233 104 000 000 034 127 000 000 000 000 052 310 055 027 000 010
020 034 121 051 153 122 001 001 174 020 013 133 037 000

102
040 001 000 030 000 123 000 000 000 000 000 000 000 000 050
232 250 000 000 035 023 000 000 000 052 310 055 027 000 010
000 020 122 020 034 121 051 153 122 001 001 174 020 013 133 037 000

109
242 002 000 033 000 122 000 000 130 311 000 002 330 007 000 062
330 000 000 002 337 145 000 000 000 052 310 055 027 000 030
020 034 045 040 142 112 150 142 122 150 001 122 153 045 117 114
112 045 153 133 037 000

110
040 001 000 023 000 123 000 000 000 000 000 000 000 000 000
330 303 000 002 340 070 000 000 000 052 310 055 027 000 030
000 005 070 001 000 000

FOLLOWING DEVICES ARE INACTIVE:
PAGE 2

2 DS14
END-OF-FILE

```

Figure 29. Octal Tape Output Format

USER INPUT:      DATAR/L RECORDS/Q/R SITO)

TIME-ACTUAL LIST OF 6-14 4:30PM MESSAGE 36 PAGE 1

REC	TRANS	TASK	TASK	SCEN	SCENARIO	DEV	
TY	START	START	END	ADDR	NAME	ID	TEXT
D	6056430	6156291	6156437	12	PL2	TY1	#X
F	6111630	6512453	6512462	22	PL2	TY1	#OW+43929P US(U N YFANS IK153 429A ENTER LOGON
D	6614104	6613882	6906780	38	PL2	TY1	#LOGON TS0217 W (DESK) T(99) NAC WAGNER)
F	6937472	6955646	6955656	89	PL2	TY1	#
F	7055498	7255700	7255911	89	PL2	TY1	ENTER PASSWOPO FO P TS0217
K	7313864	7375718	7375928	89	PL2	TY1	
D	7718054	7718085	7745043	12	MAINTSO	TY2	#X
K	7774123	7877581	7877792	23	MAINTSO	TY2	#OW
K	7411269	7815801	7816013	89	PL2	TY1	AAAAAAAA 00000000 XXXXXXXX
R	7814228	7928037	7928250	23	MAINTSO	TY2	43929P US(U N YOA
K	7974684	7948115	7948325	23	MAINTSO	TY2	05
D	7917768	7917791	7971545	147	PL2	TY1	#PASSWORD
R	7842483	7858886	7975185	122	PL2	TY1	IK1
K	7954767	8004625	8004835	23	MAINTSO	TY2	IK1
K	8003990	8045847	8046056	122	PL2	TY1	#
F	8011270	8051469	8051680	23	MAINTSO	TY2	53029A
R	8080420	8125862	8126071	122	PL2	TY1	
R	8058113	8145154	8145364	23	MAINTSO	TY2	ENTER LOGON
R	8162211	8675966	8676176	122	PL2	TY1	TS0217 LOGON IN PROGRESS AT 16: 31:32 ON JUNE 14, 1973
D	8448814	8448567	8783646	38	MAINTSO	TY2	#LOGON TS032P W(DESK) T(99) NAC(WAGNER)
	↓	↓	↓	↓	↓	↓	
D	9867966	9867800	9928434	132	PL2	TY1	#LOGOFF
F	9950493	10006019	10006408	148	PL2	TY1	#
K	10055088	11526517	11526716	148	PL2	TY1	#6K CORE USED #1 TRETS #2 IPUTS #1 DISK EXCPS
F	11558477	12106613	12106808	148	PL2	TY1	JOB LOG 73165594 95P 2.45 CP U SECS 22.76 E LAPSED SECS
K	12117581	12626716	12626925	148	PL2	TY1	TS0217 LOGGED O FF TSO AT 16:32 19P ON JUNE 14 1973+

END-OF-FILE  
P

Figure 30. Actual Time Output Format



USER INPUT: DATAR/L/I/S RUN2

DEVICES REQUESTED ARE 1 PAGE 1

1 CT0  
17 TY1  
18 TY2  
23 OS9

---

TIME-INTERVAL LIST OF RUN2 MESSAGE 4 PAGE 3

TERMINAL IDENTIFICATION: CT0

REC TRANS	TASK	CPU	RESP	SCENARIO	TEXT
TYPE	TIME	TIME	TIME	NAME	
M					MOD 0 TABLE I
M					MOD 0 TABLE E
M					MOD 0 TABLE S
C	393003	371	U		[START CT0 G-E
E	-----	341	1325	G-E	W20 ACTION TAKEN
B	V	489	K	G-E	* PAW
C	V	224	F	G-E	[EWR0W V
E	V	276	2452	G-E	W20 ACTION TAKEN
S	V	1142	F	G-E	CIERROW V
C	V	343	K	G-E	[START TY2 SC2
E	V	347	4449	G-E	W20 ACTION TAKEN

---

TIME-INTERVAL LIST OF RUN2 MESSAGE 10 PAGE 3

TERMINAL IDENTIFICATION: TY1

REC TRANS	TASK	CPU	RESP	SCENARIO	TEXT
TYPE	TIME	TIME	TIME	NAME	
M					MOD 0 TABLE I
M					MOD 0 TABLE E
M					MOD 0 TABLE S
B	V	500	U	SC1	* W40
C	10700	10903	V	SC1	
S	V	11851	0	SC1	0
B	V	256	0	SC1	0
B	0	231	U	P.10 SC1	
S	0	200	U	SC1	0 X,
S	0	354	U	SC1	0
R	20000	230	0	0.20 SC1	X,
S	0	291	0	SC1	0 X,
B	0	351	0	SC1	R
B	00119	215	0	0.50 SC1	SIGN ON
B	V	274	U	SC1	S)SIGN ON

---

TIME-INTERVAL LIST OF RUN2 MESSAGE 12 PAGE 12

TERMINAL IDENTIFICATION: TY2

REC TRANS	TASK	CPU	RESP	SCENARIO	TEXT
TYPE	TIME	TIME	TIME	NAME	
M					MOD 0 TABLE I
M					MOD 0 TABLE E
M					MOD 0 TABLE S
B	V	484	U	SC2	* 040
C	10307	10500	U	SC2	
B	V	11243	0	SC2	0
S	0	233	U	SC2	0
B	0	213	U	0.40 SC2	
B	V	270	0	SC2	3 X,

---

FOLLOWING DEVICES ARE INACTIVE: PAGE 20

23 OS9  
END-OF-FILE

Figure 31. Time Interval Output Format

```

USER INPUT:  DATAR/L/R/O,
DEVICES REQUESTED ARE 1
PAGE 1

1 CT0
2 DS14
3 TY1
4 TY2
5 TY3

```

---

```

TIME-RELATIVE LIST OF 2 MESSAGE 4 PAGE 2
TERMINAL IDENTIFICATION: CT0 RUN START TIME: 0.07950
USER START TIME: 0.07950
REC START END START END SCENARIO
TYP REL UST REL G ADDR NAME TEXT

```

---

```

M MOD 0 TABLE 1
M MOD 0 TABLE E
M MOD 0 TABLE S
C 0.0000 0.0000 0.0000 0.0000 (START TY1 ED2741
E 0.0000 0.0000 0.0000 0.0000 020 ACTION TAKEN
USER START TIME: NONE

```

---

```

TIME-RELATIVE LIST OF 2 MESSAGE 6 PAGE 4
TERMINAL IDENTIFICATION: TY1 RUN START TIME: 0.07950
USER START TIME: 7.31451
REC START END START END SCENARIO
TYP REL UST REL G ADDR NAME TEXT

```

---

```

M MOD 0 TABLE 1
M MOD 0 TABLE E
M MOD 0 TABLE S
G 0.0000 3.0191 -3.0191 0.0000 3 ED2741 MLOGON T50217 M(OE
S -0.0007 3.0233 -3.0233 0.0041 3 ED2741 MLOGON T50217 M(O
S 3.0242 3.0272 0.2030 0.2001 51 ED2741 R
R 3.2647 12.3997 0.2456 9.3806 54 ED2741 R
ENTER PASSWORD FOR
T50217- AAAAAAAAAA
00000000
XXXXXXXXXX
S 12.4040 12.4114 0.3040 0.3022 54 ED2741 S ZXXXXXXXXX
G 12.4150 13.0245 0.3967 10.0053 67 ED2741 #PASSWORD
S 12.4122 13.0203 -0.6123 0.0037 67 ED2741 Q#PASSWORD
S 13.2290 13.2325 0.0045 0.3200 79 ED2741 R
R 13.2627 25.7621 0.2300 12.7355 82 ED2741 M T50217 LOGON IN
PROGRESS AT 14103

```

---

```

FOLLOWING DEVICES ARE INACTIVE
PAGE 8
2 DS14
4 TY2
5 TY3
END-OF-FILE

```

Figure 32. Relative Time Output Format

## APPENDIX V

Example of teletype on-line listing for preparation of a single scenario, a real-time emulation, and a single data reduction listing.

XFER/A SCOR KAP  
LOAD SCDS, SIFIXE ANY KEY.  
R

SSUR KAP TYPE SCENLIB  
R

CVT TYPE 3 4

TO CANCEL RUN, USE CONTROL-A

R

QPO  
WAIT  
ENTER RUN ID

TYPE  
READY

[STAR1 TA32 TYPE  
020 ACTION TAKEN  
TCB MAX 000006 TPO MAX 000004  
CORE LINKS 000014 CORE AVAIL 027554

DOS REV 05.

R

DATAR/L

END-OF-FILE

DATAR TERMINATED  
R

## APPENDIX VI

### Timing Samples for Non-Real Time Programs

In Figure 34 where macros are expanded, lower case op-codes and some special characters do not print. These instructions can be referenced from Figure 33 in conjunction with the SCENLIB library macro substitutions.

```

34FORTN
1  ALLOCHEGS 13
2  ETUREG R9 5 R9
3  ETUREG R9 5 R10
4  *** R10 IS TRANSMISSION RATE
5  LDR 3 R9
6  *** R9 IS TYPING RATE
7  -R10 R9 R11
8  *R10 R9 R13
9  TYPE(8)
10 QEDITOR.
11 FIND(..)
12 TYPE(9)
13 QFORMAT,F
14 FIND(..)
15 C(SUB COST
16 TYPE(13)
17 QCREATE 10 10
18 LLAB1
19 R11
20 SLAB2 =
21 EXECUTE
22 *R9 R11 R12
23 /R12 R10 R12
24 LDR 1000 R9
25 *R9 R12 R12
26 ADY R12
27 EXECUTE
28 JLAB1
29 LLAB2
30 SLAB1 ..
31 TYPE(6)
32 QRUN,F
33 FIND(..)
34 TYPE(19)
35 R240=15 CONTINUE
36 TYPE(24)
37 R330= WRITE (6,100)
38 TYPE(62)
39 R390=104 FORMAT (1H0,3X,*EQUIPMENT COSTS*/7X,*SUBSYSTEM 1*,
40 TYPE(10)
41 QLIST 240
42 FIND(..)
43 TYPE(10)
44 QLIST 330
45 FIND(..)
46 TYPE(10)
47 QLIST 420
48 FIND(..)
49 TYPE(13)
50 QSAVE,TEST,0
51 FIND(..)
52 C(SUB INFO
53 TYPE(15)
54 QCREATE 100 10
55 LLAB3
56 R11
57 SLAB4 =
58 EXECUTE
59 *R9 R11 R12
60 /R12 R10 R12
61 LDR 1000 R9
62 *R9 R12 R12

```

Figure 33. Fortran Cost Scenario with Macros not Expanded



```

63  ADY R12
64  EXECUTE
65  JLAB3
66  LEND
67  SLAB3 ..
68  TYPE(17)
69  QSAVE,INFO,NOSEQ
70  FIND(..)
71  TYPE(10)
72  QEDIT,TEST
73  FIND(..)
74  TYPE(5)
75  QRUN,F
76  FIND(..)
77  TYPE(12)
78  QSAVE,TEST,0
79  FIND(..)
80  TYPE(12)
81  QEDIT,INFO,S
82  FIND(..)
83  TYPE(30)
84  R14F=201000          PTR WITH CONTROLLER
85  TYPE(25)
86  R490=201000          PROGRAMMING
87  TYPE(7)
88  QLIST,A
89  FIND(..)
90  TYPE(14)
91  QSAVE,INFO,0,N
92  FIND(..)
93  TYPE(10)
94  QEDIT,TEST
95  FIND(..)
96  TYPE(6)
97  QRUN,F
98  FIND(..)
99  TYPE(8)
100 QBYE,BYE
101 FIND(COMMAND)
102 TYPE(4)
103 QLOGOUT.
104 FIND(AT)
105 C(SUB DOFT1111

```

Figure 33. Fortran Cost Scenario with Macros not Expanded (Concluded)

```

      FORTRAN
1      13
2      R9 R9
3      R9 R10
4      *** R10 IS TRANSMISSION RATE
5      3 R9
6      *** R9 IS TYPING RATE
7      -R10 R9 R11
8      *R10 R9 R10
9      R9 R9
10     R9 R10
11     3 R9
12     -R10 R9 R11
13     *R9 R10 R10
14     R9
15     *R9 R11 R11
16     /R11 R10 R10
17     1000 R9
18     *R9 R10 R10
19     R10
20     QEDITOK.
21     L LL12
22     R''
23     S LL12 ..
24     R9 R9
25     R9 R10
26     3 R9
27     -R10 R9 R11
28     *R9 R10 R10
29     9 R9
30     *R9 R11 R11
31     /R11 R10 R10
32     1000 R9
33     *R9 R10 R10
34     R10
35     QFORMAT,F
36     L LL20
37     R''
38     S LL20 ..
39     C(SUB COST
40     R9 R9
41     R9 R10
42     3 R9
43     -R10 R9 R11
44     *R9 R10 R10
45     13 R9
46     *R9 R11 R11
47     /R11 R10 R10
48     1000 R9
49     *R9 R10 R10
50     R10
51     UCREATE 10 10
52     LLAB1
53     R''
54     SLAB2 =
55
56     *R9 R11 R12
57     /R12 R10 R12
58     1000 R9
59     *R9 R12 R12
60     R12
61
62     JLAB1

```

Figure 34. Scenarios for Fortran Cost Problem with Macros Expanded

```

63 LLAB2
64 SLAB1 ..
65 R0 0 R9
66 R0 6 R10
67 3 R9
68 -R10 R9 R11
69 *R9 R10 R10
70 6 R9
71 *R9 R11 R11
72 /R11 R10 R10
73 1000 R9
74 *R9 R10 R10
75 R10
76 GRUN,F
77 L LL39
78 R11
79 S LL39 ..
80 R0 0 R9
81 R9 6 R10
82 3 R9
83 -R10 R9 R11
84 *R9 R10 R10
85 19 R9
86 *R9 R11 R11
87 /R11 R10 R10
88 1000 R9
89 *R9 R10 R10
90 R10
91 R240=15 CONTINUE
92 R0 0 R9
93 R9 6 R10
94 3 R9
95 -R10 R9 R11
96 *R9 R10 R10
97 24 R9
98 *R9 R11 R11
99 /R11 R10 R10
100 1000 R9
101 *R9 R10 R10
102 R10
103 R330= WRITE (6,100)
104 R0 0 R9
105 R9 6 R10
106 3 R9
107 -R10 R9 R11
108 *R9 R10 R10
109 62 R9
110 *R9 R11 R11
111 /R11 R10 R10
112 1000 R9
113 *R9 R10 R10
114 R10
115 R390=104 FORMAT (1H0,3X,*EQUIPMENT COSTS*/7X,*SUBSYSTEM 1*,
116 R0 0 R9
117 R9 6 R10
118 3 R9
119 -R10 R9 R11
120 *R9 R10 R10
121 10 R9
122 *R9 R11 R11
123 /R11 R10 R10
124 1000 R9
125 *R9 R10 R10

```

Figure 34. Scenarios for Fortran Cost Problem with Macros Expanded  
(Continued)

```

126 R10
127 OLIST 240
128 L LL68
129 R11
130 S LL68 ..
131 R0 0 R9
132 R9 6 R10
133 3 R9
134 -R10 R9 R11
135 +R9 R10 R10
136 10 R9
137 +R9 R11 R11
138 /R11 R10 R10
139 1000 R9
140 +R9 R10 R10
141 R10
142 OLIST 330
143 L LL76
144 R11
145 S LL76 ..
146 R0 0 R9
147 R9 6 R10
148 3 R9
149 -R10 R9 R11
150 +R9 R10 R10
151 10 R9
152 +R9 R11 R11
153 /R11 R10 R10
154 1000 R9
155 +R9 R10 R10
156 R10
157 OLIST 420
158 L LL84
159 R11
160 S LL84 ..
161 R0 0 R9
162 R9 6 R10
163 3 R9
164 -R10 R9 R11
165 +R9 R10 R10
166 13 R9
167 +R9 R11 R11
168 /R11 R10 R10
169 1000 R9
170 +R9 R10 R10
171 R10
172 QSAVE,TEST,0
173 L LL92
174 R11
175 S LL92 ..
176 C(SUB INFO
177 R0 0 R9
178 R9 6 R10
179 3 R9
180 -R10 R9 R11
181 +R9 R10 R10
182 15 R9
183 +R9 R11 R11
184 /R11 R10 R10
185 1000 R9
186 +R9 R10 R10
187 R10
188 UCREATE 100 10

```

Figure 34. Scenarios for Fortran Cost Problem with Macros Expanded  
(Continued)

```

189 LLAB3
190 R11
191 SLAB4 =
192
193 *R9 R11 R12
194 /R12 R10 R12
195 1000 R9
196 *R9 R12 R12
197 R12
198
199 JLAB3
200 LLAB4
201 SLAB5 ..
202 R0 0 R9
203 R9 0 R10
204 3 R9
205 -R10 R9 R11
206 *R9 R10 R10
207 17 R9
208 *R9 R11 R11
209 /R11 R10 R10
210 1000 R9
211 *R9 R10 R10
212 R10
213 QSAVE,INFO,NOSEQ
214 L LL111
215 R11
216 S LL111 ..
217 R0 0 R9
218 R9 0 R10
219 3 R9
220 -R10 R9 R11
221 *R9 R10 R10
222 10 R9
223 *R9 R11 R11
224 /R11 R10 R10
225 1000 R9
226 *R9 R10 R10
227 R10
228 QEDIT,TEST
229 L LL119
230 R11
231 S LL119 ..
232 R0 0 R9
233 R9 0 R10
234 3 R9
235 -R10 R9 R11
236 *R9 R10 R10
237 6 R9
238 *R9 R11 R11
239 /R11 R10 R10
240 1000 R9
241 *R9 R10 R10
242 R10
243 QRUN,F
244 L LL127
245 R11
246 S LL127 ..
247 R0 0 R9
248 R9 0 R10
249 3 R9
250 -R10 R9 R11
85-251 *R9 R10 R10

```

Figure 34. Scenarios for Fortran Cost Problem with Macros Expanded  
(Continued)

```

252 12 R9
253 *R9 R11 R11
254 /R11 R10 R10
255 1000 R9
256 *R9 R10 R10
257 R10
258 QSAVE,TEST,0
259 L LL135
260 R11
261 S LL135 ..
262 R0 0 R9
263 R9 6 R10
264 3 R9
265 -R10 R9 R11
266 *R9 R10 R10
267 12 R9
268 *R9 R11 R11
269 /R11 R10 R10
270 1000 R9
271 *R9 R10 R10
272 R10
273 QEDIT,INFO,S
274 L LL143
275 R11
276 S LL143 ..
277 R0 0 R9
278 R9 6 R10
279 3 R9
280 -R10 R9 R11
281 *R9 R10 R10
282 36 R9
283 *R9 R11 R11
284 /R11 R10 R10
285 1000 R9
286 *R9 R10 R10
287 R10
288 R140=001000
289 R0 0 R9
290 R9 6 R10
291 3 R9
292 -R10 R9 R11
293 *R9 R10 R10
294 28 R9
295 *R9 R11 R11
296 /R11 R10 R10
297 1000 R9
298 *R9 R10 R10
299 R10
300 R490=001000
301 R0 0 R9
302 R9 6 R10
303 3 R9
304 -R10 R9 R11
305 *R9 R10 R10
306 7 R9
307 *R9 R11 R11
308 /R11 R10 R10
309 1000 R9
310 *R9 R10 R10
311 R10
312 QLIST,A
313 L LL166
314 R11

```

WITH CONTROLLER

PROGRAMMING

Figure 34. Scenarios for Fortran Cost Problem with Macros Expanded  
(Continued)

```

315 S LL166 ..
316 R0 0 R9
317 R9 6 R10
318 3 R9
319 -R10 R9 R11
320 *R9 R10 R10
321 14 R9
322 *R9 R11 R11
323 /R11 R10 R10
324 1000 R9
325 *R9 R10 R10
326 R10
327 USAVE,INFO,0,N
328 L LL174
329 R!!
330 S LL174 ..
331 R0 0 R9
332 R9 6 R10
333 3 R9
334 -R10 R9 R11
335 *R9 R10 R10
336 10 R9
337 *R9 R11 R11
338 /R11 R10 R10
339 1000 R9
340 *R9 R10 R10
341 R10
342 UEDIT,TEST
343 L LL182
344 R!!
345 S LL182 ..
346 R0 0 R9
347 R9 6 R10
348 3 R9
349 -R10 R9 R11
350 *R9 R10 R10
351 6 R9
352 *R9 R11 R11
353 /R11 R10 R10
354 1000 R9
355 *R9 R10 R10
356 R10
357 ORUN,F
358 L LL190
359 R!!
360 S LL190 ..
361 R0 0 R9
362 R9 6 R10
363 3 R9
364 -R10 R9 R11
365 *R9 R10 R10
366 8 R9
367 *R9 R11 R11
368 /R11 R10 R10
369 1000 R9
370 *R9 R10 R10
371 R10
372 DBYE,BYE
373 L LL198
374 R!!
375 S LL198 COMMAND
376 R0 0 R9
377 R9 6 R10

```

Figure 34. Scenarios for Fortran Cost Problem with Macros Expanded  
(Continued)



```

378      3 R9
379 -R10 R9 R11
380 +R9 R10 R10
381      8 R9
382 +R9 R11 R11
383 /R11 R10 R10
384      1000 R9
385 +R9 R10 R10
386      R10
387 QLOGOUT.
388 L LL206
389 R!!
390 S LL206 AT
391 C(SUB DUFT1111

```

Figure 34. Scenarios for Fortran Cost Problem with Macros Expanded  
(Continued)

```

COST
1  X
2  51 9 R6
3  Q;PROGRAM COST(OUTPUT,INFO,TAPE5=INFO,TAPE6=OUTPUT)
4  45 9 R6
5  QC ****PROGRAM TO COMPUTE COST ESTIMATES ****
6  39 9 R6
7  Q;DIMENSION IM(20),IP(20),IO(20)
8  18 9 R6
9  Q;DO 75 I=COUNT=1,2
10  9 9 R6
11  Q;ISUMM=0
12  17 9 R6
13  Q;READ (5,1) NUMM
14  14 9 R6
15  Q17FORMAT (I6)
16  15 9 R6
17  Q;DO 5 I=1,NUMM
18  18 9 R6
19  Q;READ (5,1) IM(I)
20  19 9 R6
21  Q;ISUMM=IM(I)+ISUMM
22  11 9 R6
23  Q5;CONTINUE
24  9 9 R6
25  Q;ISUMN=0
26  17 9 R6
27  Q;READ (5,1) NUMN
28  16 9 R6
29  Q;DO 10 I=1,NUMN
30  16 9 R6
31  Q;READ (5,1) IN(I)
32  19 9 R6
33  Q;ISUMN=IN(I)+ISUMN
34  12 9 R6
35  Q10;CONTINUE
36  20 9 R6
37  Q;ISUMM=ISUMN+ISUMM
38  9 9 R6
39  Q;ISUMP=0
40  17 9 R6
41  Q;READ (5,1) NUMP
42  16 9 R6
43  Q;DO 15 I=1,NUMP
44  18 9 R6
45  Q;READ (5,1) IP(I)
46  19 9 R6
47  Q;ISUMP=IP(I)+ISUMP
48  11 9 R6
49  Q15;CONTINUE
50  9 9 R6
51  Q;ISUMQ=0
52  17 9 R6
53  Q;READ (5,1) NUMQ
54  16 9 R6
55  Q;DO 20 I=1,NUMQ
56  18 9 R6
57  Q;READ (5,1) IO(I)
58  19 9 R6
59  Q;ISUMQ=IO(I)+ISUMQ
60  12 9 R6
61  Q20;CONTINUE
62  27 9 R6

```

Figure 34. Scenarios for Fortran Cost Problem with Macros Expanded  
(Continued)

```

63 Q1ITOTAL=IEQSUM+ISUMP+ISUMQ
64      29 9 R6
65 Q1IF (ICOUNT.EQ. 2) GO TO 56
66      14 9 R6
67 Q1WRITE (,100)
68      64 9 R6
69 Q100;FORMAT (1H0//6X,*PRELIMINARY COST ESTIMATE*//1X,*SYSTEM A*)
70      10 9 R6
71 Q1GO TO 55
72      17 9 R6
73 Q50;WRITE (5,102)
74      32 9 R6
75 Q102;FORMAT (1H0//1X,*SYSTEM B*)
76      36 9 R6
77 Q55;WRITE (6,104) ISUMH,ISUMN,IEQSUM
78      55 9 R6
79 Q104;FORMAT (1H0,3X,*EQUIPMENT COSTS*//7X,*SUBSYSTEM 1*,
80      55 9 R6
81 Q      +5X,I8/7X,*SUBSYSTEM 2*,5X,I8/19X,*TOTAL*,2X,I10)
82      34 9 R6
83 Q1WRITE (6,106) ISUMP,ISUMQ,ITOTAL
84      57 9 R6
85 Q106;FORMAT (1H0,3X,*DEVELOPMENT COSTS*,5X,I10/4X,*O & M*
86      44 9 R6
87 Q      ** COSTS*,11X,I10//19X,*TOTAL*,2X,I10)
88      12 9 R6
89 Q75;CONTINUE
90      6 9 R6
91 Q1STOP
92      5 9 R6
93 Q1END
94      2 9 R6
95 Q=

```

Figure 34. Scenarios for Fortran Cost Problem with Macros Expanded  
(Continued)

INFO	
1	X
2	29 9 R6
3	0000000
4	29 9 R6
5	0001000
6	36 9 R5
7	0001000
8	37 9 R6
9	0001000
10	32 9 R6
11	0001000
12	32 9 R6
13	0001000
14	38 9 R6
15	0001000
16	39 9 R6
17	0001000
18	46 9 R6
19	0001000
20	29 9 R6
21	0000002
22	22 9 R6
23	0001000
24	23 9 R6
25	0001000
26	29 9 R6
27	0000005
28	23 9 R6
29	0001000
30	23 9 R6
31	0001000
32	24 9 R6
33	0001000
34	26 9 R6
35	0001000
36	18 9 R6
37	0001000
38	29 9 R6
39	0000004
40	33 9 R6
41	0001000
42	29 9 R6
43	0001000
44	34 9 R5
45	0001000
46	28 9 R6
47	0001000
48	27 9 R6
49	0000006
50	28 9 R6
51	0001000
52	32 9 R6
53	0001000
54	32 9 R6
55	0001000
56	30 9 R6
57	0001000
58	42 9 R6
59	0001000
60	40 9 R6
61	0001000
62	27 9 R6

NUMBER IN LIST M
CPU WITH 24K MEM
FM DISC WITH CONTROLLER
MAG TAPE WITH CONTROLLER
WITH CONTROLLER
TTY WITH CONTROLLER
LINE PRINTER & CONTROLLER
16 ASYNCHRONOUS LINE ADAPT
1 HISPEED ASYNCHRONOUS LINE ADAPT
NUMBER IN LIST N
16 MODEMS
MODEM RACK
NUMBER IN LIST P
ELEC ENGIN
MECH ENGIN
PROGRAMMING
DOCUMENTATION
T & E
NUMBER IN LIST Q
OPERATIONS PERSONNEL
SERVICE CONTRACT
TELEPHONE & DAA LEASE
TELEPHONE USAGE
NUM IN LIST M*
CPU WITH 8K MEM
WITH CONTROLLER
TTY WITH CONTROLLER
0 LJ-LINE OIG I/O
16 ASYNCHRONOUS LINE ADAPTERS
8 SYNCHRONOUS LINE ADAPTERS

Figure 34. Scenarios for Fortran Cost Problem with Macros Expanded  
(Continued)

63	0000005	NUM IN LIST N*
64	29 9 R6	
65	0001000	2 HISPEED MODEMS
66	24 9 R6	
67	0001000	MODEM CLOCK
68	17 9 R6	
69	0001000	RACK
70	33 9 R6	
71	0001000	PANEL & SPECIAL CKTS
72	45 9 R6	
73	0001000	HISPEED SYNCHRONOUS LINE ADAPTER
74	26 9 R6	
75	0000005	NUM IN LIST P*
76	23 9 R6	
77	0001000	ELEC ENGIN
78	23 9 R6	
79	0001000	MECH ENGIN
80	24 9 R6	
81	0001000	PROGRAMMING
82	26 9 R6	
83	0001000	DOCUMENTATION
84	18 9 R6	
85	0001000	T & E
86	25 9 R6	
87	0000004	NUM IN LIST Q*
88	33 9 R6	
89	0001000	OPERATIONS PERSONNEL
90	29 9 R6	
91	0001000	SERVICE CONTRACT
92	35 9 R6	
93	0001000	TELEPHONE & DATA LEASE
94	28 9 R6	
95	0001000	TELEPHONE USAGE
96	2 9 R6	
97	0*	

Figure 34. Scenarios for Fortran Cost Problem with Macros Expanded  
(Concluded)

Contents of SCENLIB Macro Library

Name	Value
ALLOCREGS	a
RESPTOREG	c
ADY	d
EXECUTE	e
FREEBUFF	f
GTR	g
ETOREG	b
INPUTPARAM	i
LDR	l
BROFF	n
PTR	p
BDP	q
RANDOM	r
TYPEOUT	t

Figure 35. Macro Libraries for Fortran Cost Problem

```

KAPLIB
1  MDEF FIND(1)
2  L LLST
3  R11
4  S LLST S1
5  MEND
6  MDEF NDEV
7  1
8  MEND
9  MDEF TYPE (1)
10 ETOREG R0 0 R9
11 ETOREG R9 6 R10
12 LDR 3 R9
13 -R10 R9 R11
14 *R9 R10 R10
15 LDR S1 R9
16 *R9 R11 R11
17 /R11 R10 R10
18 LDR 1000 R9
19 *R9 R10 R10
20 ADY R10
21 MEND

```

Figure 35. Macro Libraries for Fortran Cost Problem (Concluded)